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捷多邦,专业PCB打样工厂,24小时加急**S附**74HC04-Q1 HEX INVERTER

SCLS583 - MAY 2004

 Qualification in Accordance With AEC-Q100[†] 	PW PACKAGE (TOP VIEW)
 Qualified for Automotive Applications 	
 Customer-Specific Configuration Control Can Be Supported Along With Major-Change Approval 	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$
Wide Operating Voltage Range of 2 V to 6 V	3A 5 10 5Y
Outputs Can Drive Up To 10 LSTTL Loads	3Y [6 9] 4A
Low Power Consumption, 20-μA Max I _{CC}	GND 🛛 7 8 🕽 4Y
Typical t _{pd} = 8 ns	
• ±4-mA Output Drive at 5 V	
Low Input Current of 1 μA Max	

⁺ Contact factory for details. Q100 qualification data available on request.

description/ordering information

The SN74HC04 device contains six independent inverters. It performs the Boolean function $Y = \overline{A}$ in positive logic.



ORDERING INFORMATION

TA	PACKAG	GE‡	ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 85°C	TSSOP – PW	Tape and reel	SN74HC04IPWRQ1	HC04I
–40°C to 125°C	TSSOP – PW	Tape and reel	SN74HC04QPWRQ1	HC04Q

Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

FUNCTION TABLE (each inverter)						
INPUT A	OUTPUT Y					
Н	L					
L	Н					

logic diagram (positive logic)



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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V _{CC}	–0.5 V to 7 V
Input clamp current, IIK (VI < 0 or VI > VCC) (see Note 1)	±20 mA
Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC}) (see Note 1)	±20 mA
Continuous output current, I_O ($V_O = 0$ to V_{CC})	±25 mA
Continuous current through V _{CC} or GND	
Package thermal impedance, θ_{JA} (see Note 2)	113°C/W
Storage temperature range, T _{stg}	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

2. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions (see Note 3)

			MIN	NOM	MAX	UNIT	
VCC	Supply voltage				6	V	
		$V_{CC} = 2 V$	1.5				
ViH	VIH High-level input voltage	V _{CC} = 4.5 V	3.15			V	
		$V_{CC} = 6 V$	4.2				
		$V_{CC} = 2 V$			0.5		
V _{IL} Low-level input voltage	Low-level input voltage	V _{CC} = 4.5 V			1.35	V	
		V _{CC} = 6 V			1.8		
VI	Input voltage		0		VCC	V	
VO	Output voltage		0		VCC	V	
		$V_{CC} = 2 V$			1000		
$\Delta t / \Delta v$	Input transition rise/fall time	V _{CC} = 4.5 V			500	ns	
		V _{CC} = 6 V			400		
т.	Operating free air temperature	I-suffix device	-40		85	°C	
Τ _Α	Operating free-air temperature	Q-suffix device	-40		125	J	

NOTE 3: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.



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PARAMETER TEST C			N	T _A = 25°C		SN74HC04Q		SN74HC04I			
PARAMETER	ARAMETER TEST CON		Vcc	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
			2 V	1.9	1.998		1.9		1.9		
		I _{OH} = -20 μA	4.5 V	4.4	4.499		4.4		4.4		
∨он	$V_I = V_{IH} \text{ or } V_{IL}$		6 V	5.9	5.999		5.9		5.9		V
		$I_{OH} = -4 \text{ mA}$	4.5 V	3.98	4.3		3.7		3.84		
		I _{OH} = -5.2 mA	6 V	5.48	5.8		5.2		5.34		
			2 V		0.002	0.1		0.1		0.1	
	VI = VIH or VIL	I _{OL} = 20 μA	4.5 V		0.001	0.1		0.1		0.1	
VOL			6 V		0.001	0.1		0.1		0.1	V
		$I_{OL} = 4 \text{ mA}$	4.5 V		0.17	0.26		0.4		0.33	
		I _{OL} = 5.2 mA	6 V		0.15	0.26		0.4		0.33	
Ц	$V_{I} = V_{CC} \text{ or } 0$		6 V		±0.1	±100		±1000		±1000	nA
ICC	$V_{I} = V_{CC} \text{ or } 0,$	I _O = 0	6 V			2		40		20	μΑ
Ci			2 V to 6 V		3	10		10		10	pF

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

switching characteristics over recommended operating free-air temperature range, $C_L = 50 \text{ pF}$ (unless otherwise noted) (see Figure 1)

DADAMETED	FROM	то	N.	Τį	ς = 25°C	;	SN74H	C04Q	SN74F	IC04I		
PARAMETER	(INPUT)	(OUTPUT)	vcc	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT	
			2 V		45	95		145		120		
^t pd	А	Y	4.5 V		9	19		29		24	ns	
				6 V		8	16		25		20	
			2 V		38	75		110		95		
tt		Y	4.5 V		8	15		22		19	ns	
			6 V		6	13		19		16		

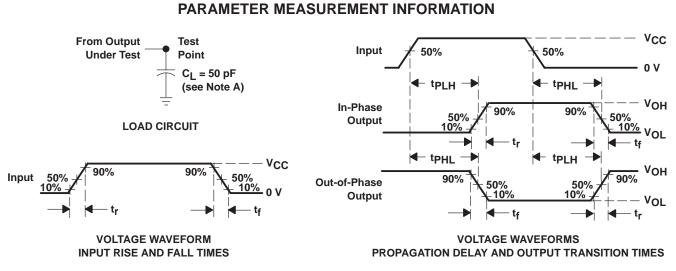
operating characteristics, $T_A = 25^{\circ}C$

	PARAMETER	TEST CONDITIONS	TYP	UNIT
C _{pd}	Power dissipation capacitance per inverter	No load	20	pF



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- NOTES: A. CL includes probe and test-fixture capacitance.
 - B. Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, Z_O = 50 Ω , t_f = 6 ns. t_f = 6 ns.
 - C. The outputs are measured one at a time, with one input transition per measurement.
 - D. t_{PLH} and t_{PHL} are the same as t_{pd} .

Figure 1. Load Circuit and Voltage Waveforms

29-May-2007

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins P	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
SN74HC04IPWRQ1	ACTIVE	TSSOP	PW	14	2000	Pb-Free (RoHS)	CU NIPDAU	Level-1-250C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

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Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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MECHANICAL DATA

MTSS001C - JANUARY 1995 - REVISED FEBRUARY 1999

PLASTIC SMALL-OUTLINE PACKAGE





NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153



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