

## **HD74AC32**

# Quad 2-Input OR Gate

REJ03D0268-0200Z (Previous ADE-205-360 (Z)) Rev.2.00 Jul.16.2004

#### **Features**

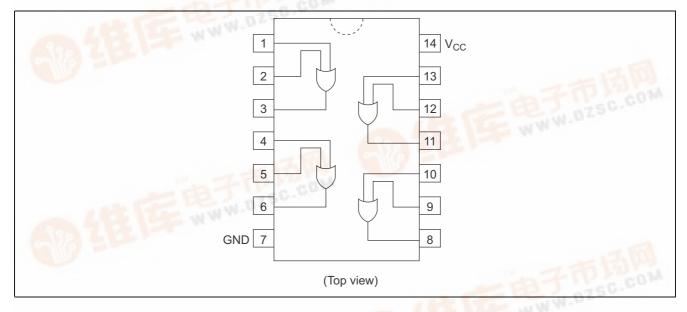
- Outputs Source/Sink 24 mA
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74AC32P	DIP-14 pin	DP-14, -14AV	P	_
HD74AC32FPEL	S <mark>OP-14</mark> pin (JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)
HD74AC32RPEL	SOP-14 pin (JEDEC)	FP-14DNV	RP	EL (2,500 pcs/reel)
HD74AC32TELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)

Notes: 1. Please consult the sales office for the above package availability.

2. The packages with lead-free pins are distinguished from the conventional products by adding V at the end of the package code.

#### **Pin Arrangement**





## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V <sub>cc</sub>	-0.5 to 7	٧	
DC input diode current	I <sub>IK</sub>	-20	mA	$V_1 = -0.5V$
		20	mA	$V_1 = Vcc+0.5V$
DC input voltage	V <sub>I</sub>	-0.5 to Vcc+0.5	٧	
DC output diode current	I <sub>OK</sub>	-50	mA	$V_0 = -0.5V$
		50	mA	$V_O = Vcc+0.5V$
DC output voltage	Vo	-0.5 to Vcc+0.5	٧	
DC output source or sink current	Io	±50	mA	
DC V <sub>CC</sub> or ground current per output pin	$I_{CC}, I_{GND}$	±50	mA	
Storage temperature	Tstg	-65 to +150	°C	

## **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V <sub>cc</sub>	2 to 6	V	
Input and output voltage	$V_{I}, V_{O}$	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to +85	°C	
Input rise and fall time	tr, tf	8	ns/V	$V_{CC} = 3.0V$
(except Schmitt inputs)				V <sub>CC</sub> = 4.5 V
V <sub>IN</sub> 30% to 70% V <sub>CC</sub>				V <sub>CC</sub> = 5.5 V

#### **DC Characteristics**

Item	Sym- bol	Vcc (V)	-	Га = 25°	C		–40 to 5°C	Unit	Condition
		` ′	min.	typ.	max.	min.	max.		
Input Voltage	V <sub>IH</sub>	3.0	2.1	1.5	_	2.1	_	٧	$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	3.15	2.25	_	3.15	_		
		5.5	3.85	2.75	_	3.85	_		
	V <sub>IL</sub>	3.0	_	1.50	0.9	_	0.9		$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	_	2.25	1.35	_	1.35		
		5.5	_	2.75	1.65	_	1.65		
Output voltage	V <sub>OH</sub>	3.0	2.9	2.99	_	2.9		V	$V_{IN} = V_{IL}$ or $V_{IH}$
		4.5	4.4	4.49	_	4.4			$I_{OUT} = -50 \mu A$
		5.5	5.4	5.49	_	5.4			
		3.0	2.58	_	_	2.48			$V_{IN} = V_{IL}$ or $V_{IH}$ $I_{OH} = -12$ mA
		4.5	3.94	—	—	3.80	_		$I_{OH} = -24 \text{ mA}$
		5.5	4.94	—	—	4.80	_		$I_{OH} = -24 \text{ mA}$
	$V_{OL}$	3.0	—	0.002	0.1	—	0.1		$V_{IN} = V_{IL}$ or $V_{IH}$
		4.5	—	0.001	0.1	—	0.1		I <sub>OUT</sub> = 50 μA
		5.5	_	0.001	0.1	_	0.1		
		3.0	_	_	0.32	_	0.37		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OL} = 12 \text{ mA}$
		4.5	—	—	0.32	—	0.37		$I_{OL} = 24 \text{ mA}$
		5.5	—	—	0.32	—	0.37		$I_{OL} = 24 \text{ mA}$
Input leakage current	I <sub>IN</sub>	5.5	_	_	±0.1	_	±1.0	μΑ	$V_{IN} = V_{CC}$ or GND
Dynamic output	I <sub>OLD</sub>	5.5	_	_	_	86	_	mA	V <sub>OLD</sub> = 1.1 V
current*	I <sub>OHD</sub>	5.5	_	_	_	-75	_	mA	V <sub>OHD</sub> = 3.85 V
Quiescent supply current	I <sub>CC</sub>	5.5	_	_	4.0	_	40	μΑ	$V_{IN} = V_{CC}$ or ground

<sup>\*</sup>Maximum test duration 2.0 ms, one output loaded at a time.

#### **AC Characteristics**

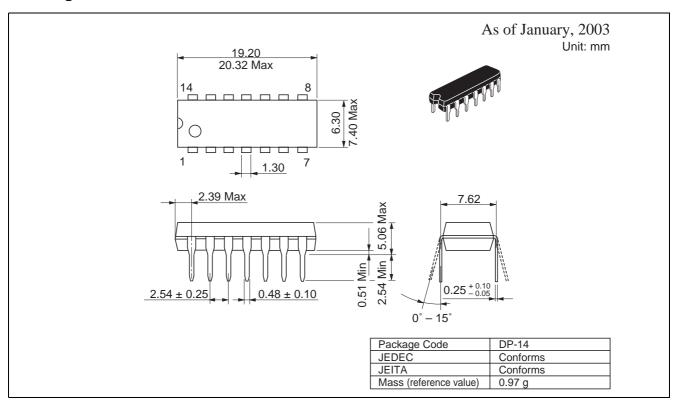
			Ta = +25°C C <sub>L</sub> = 50 pF			C to +85°C 50 pF		
Item	Symbol	V <sub>cc</sub> (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay	t <sub>PLH</sub>	3.3	1.0	7.0	9.0	1.0	10.0	ns
		5.0	1.0	5.5	7.5	1.0	8.5	
Propagation delay	t <sub>PHL</sub>	3.3	1.0	7.0	8.5	1.0	9.0	ns
		5.0	1.0	5.0	7.0	1.0	7.5	

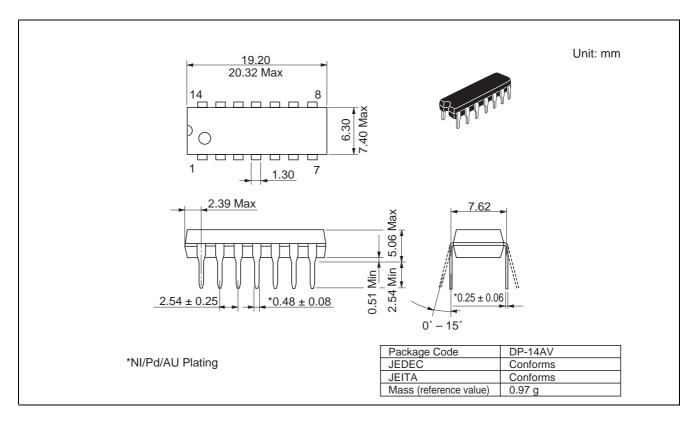
Note: 1. Voltage Range 3.3 is  $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is  $5.0 \text{ V} \pm 0.5 \text{ V}$ 

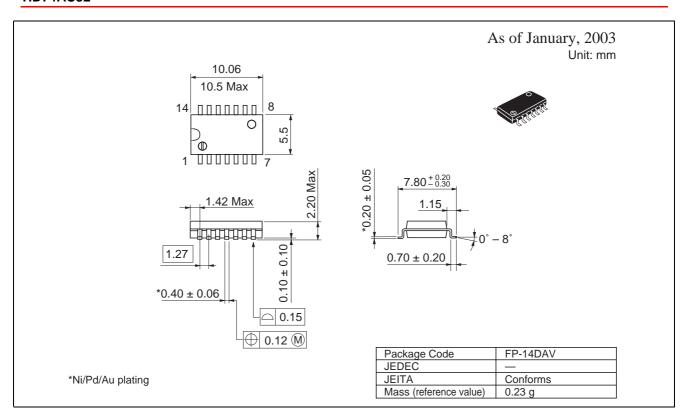
## Capacitance

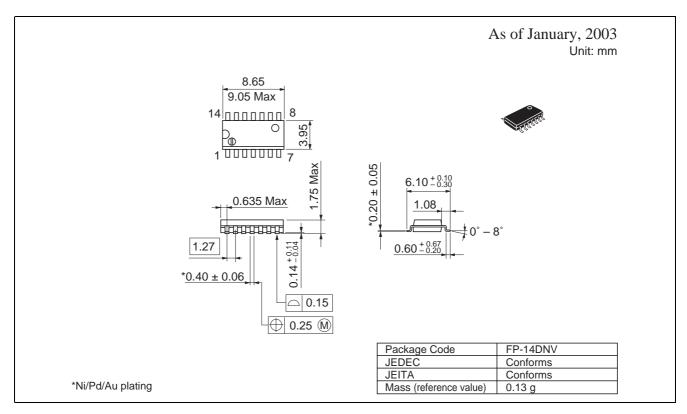
Item	Symbol	Тур	Unit	Condition
Input capacitance	C <sub>IN</sub>	4.5	pF	V <sub>CC</sub> = 5.5 V
Power dissipation capacitance	$C_{PD}$	20.0	pF	V <sub>CC</sub> = 5.0 V

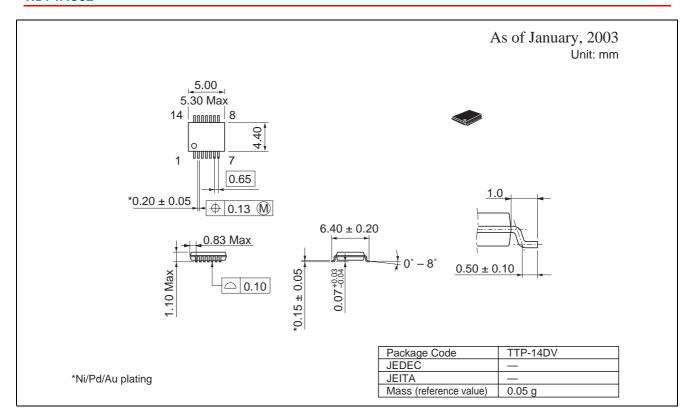
#### **Package Dimensions**











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