VISHAY

捷多邦,专业PCB打样工厂 ,24小时加急出货

HFA120FA120P

Vishay High Power Products

HEXFRED[®] Ultrafast Soft Recovery Diode, 120 A



FEATURES

- Fast recovery time characteristic
- Electrically isolated base plate
- Large creepage distance between terminal
- Simplified mechanical designs, rapid assembly
- UL pending
- Totally lead (Pb)-free
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

The dual diode series configuration (HFA120FA120P) is used for output rectification or freewheeling/clamping operation and high voltage application.

The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as HV power supplies, electronic welders, motor control and inverters.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R	1 Ht The The	1200	V	
Continuous forward current	١ _F	T _C = 62 °C	60		
Single pulse forward current	I _{FSM}	$T_J = 25 \degree C$	350	А	
Maximum repetitive forward current	IFRM	Rated V_{R} , square wave, 20 kHz, T_{C} = 60 °C	130		
	PD	T _C = 25 °C	337	w	
Maximum power dissipation		T _C = 100 °C	135		
RMS isolation voltage	VISOL	Any terminal to case, t = 1 minute	2500	V	
Operating junction and storage temperature range	T _J , T _{Stg}		- 55 to + 150	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	1200	-	-	
Forward voltage	da J'	I _F = 60 A	-	2.8	4.0	V
	I _F = 120 A	-	3.6	5.3		
	1. St. St. St. St. St. St. St. St. St. St	I _F = 60 A, T _J = 125 °C	-	2.7	-	
	1	$V_{R} = V_{R}$ rated	-	2.0	75	μA
Reverse leakage current		$T_J = 150 \ ^{\circ}C, V_R = V_R \text{ rated}$	-	2.7	10	mA



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PRODUCT SUMMARY			
V _R	1200 V		
V _F (typical)	2.8 V		
t _{rr} (typical)	145 ns		
I _{F(DC)} at T _C	60 A at 62 °C		

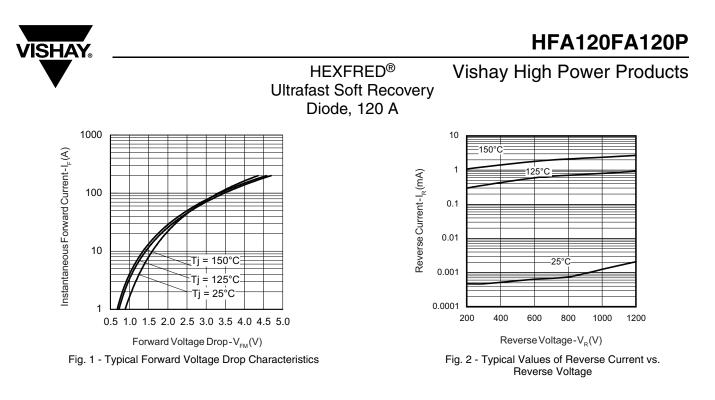
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	$T_J = 25 \ ^{\circ}C$		-	145	-		
Reverse recovery time	t _{rr}	T _J = 125 °C	I _F = 50 A dI _F /dt = - 200 A/μs V _R = 200 V	-	218	-	ns
Deals recovery ourrent	I _{RRM}	T _J = 25 °C		-	13	-	A
Peak recovery current		T _J = 125 °C		-	18	-	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	910	-	
	T _J = 125 °C		-	1920	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	Р		-	-	0.37	
Junction to case, both legs conducting	– R _{thJC}		-	-	0.185	°C/W
Case to heatsink	R _{thCS}	Flat, greased and surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	Nm





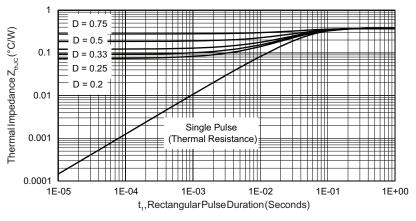


Fig. 3 - Maximum Thermal Impedance Z_{thJC} Characteristics

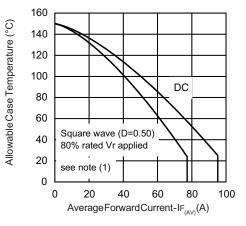
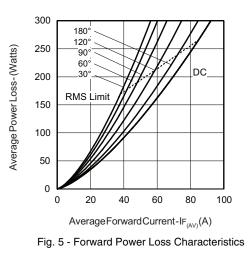


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current



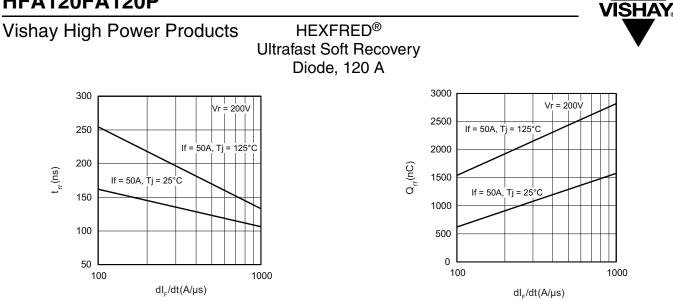


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

Fig. 7 - Typical Stored Charge vs. dl_F/dt

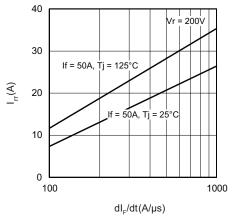


Fig. 8 - Typical Peak Recovery Current vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig. 5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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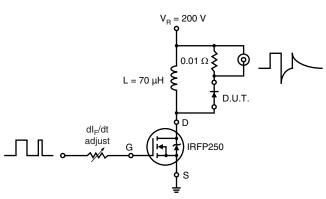


Fig. 9 - Reverse Recovery Parameter Test Circuit

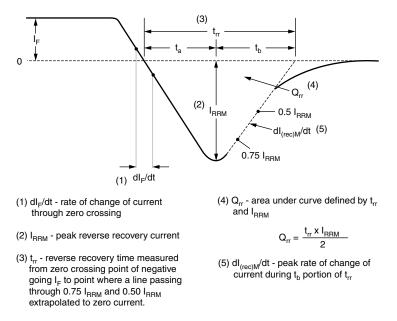


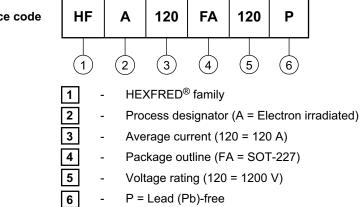
Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code



LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95036				
Packaging information	http://www.vishay.com/doc?95037			





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