



HMC607

GaAs MMIC HIGH ISOLATION SPDT SWITCH, DC - 15 GHz

Typical Applications

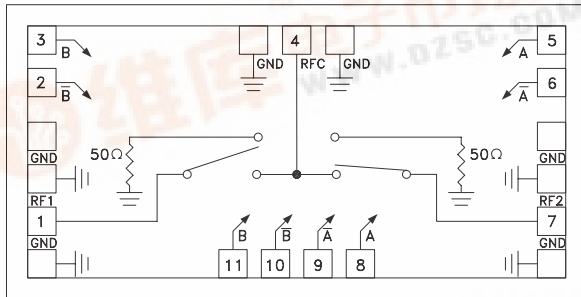
The HMC607 is ideal for:

- Telecom Infrastructure
- Microwave Radio & VSAT
- Military Radios, Radar & ECM
- Space Systems
- Test Instrumentation

Features

- High Isolation: >50 dB @ 10 GHz
- Low Insertion Loss: 1.4 dB Typical @ 6.0 GHz
- Non-Reflective Design
- Die Size: 2.05 mm x 1.04 mm x 0.1 mm

Functional Diagram



General Description

The HMC607 is a broadband high isolation non-reflective GaAs MESFET SPDT MMIC chip. Covering DC to 15 GHz, the switch features >55 dB isolation at lower frequencies and >45 dB at higher frequencies. The switch operates using complementary negative control voltage logic lines of -5/0V and requires no bias supply.

Electrical Specifications, $T_A = +25^\circ C$, With 0/-5V Control, 50 Ohm System

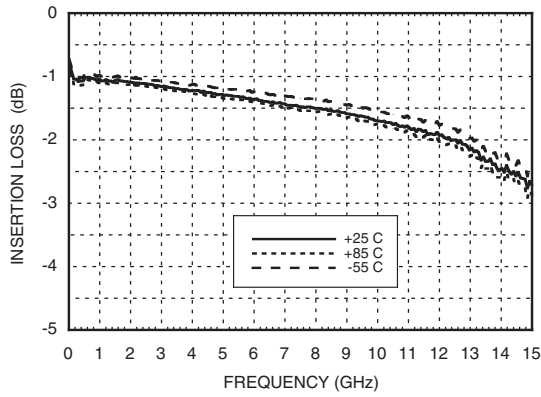
Parameter	Frequency	Min.	Typ.	Max.	Units
Insertion Loss	DC - 6 GHz		1.4	1.7	dB
	DC - 10 GHz		1.7	2.5	dB
	DC - 15 GHz		2.7	3.4	dB
Isolation*	DC - 6 GHz	55	65		dB
	DC - 10 GHz	50	60		dB
	DC - 15 GHz	45	55		dB
Return Loss	"On State"	DC - 6 GHz	17		dB
		DC - 15 GHz	11		dB
Return Loss RF1, RF2	"Off State"	DC - 6 GHz	13		dB
		DC - 15 GHz	17		dB
Input Power for 1 dB Compression	0.5 - 15 GHz	21	26		dBm
Input Third Order Intercept (Two-Tone Input Power= +7 dBm Each Tone, 1 MHz Tone Separation)	0.5 - 15 GHz	44	49		dBm
Switching Characteristics	DC - 15 GHz	tRISE, tFALL (10/90% RF)	3		ns
		tON, tOFF (50% CTL to 10/90% RF)	5		ns

*Isolation data taken with probe on the die

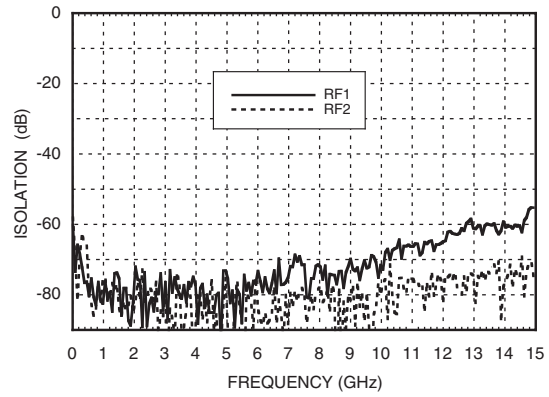


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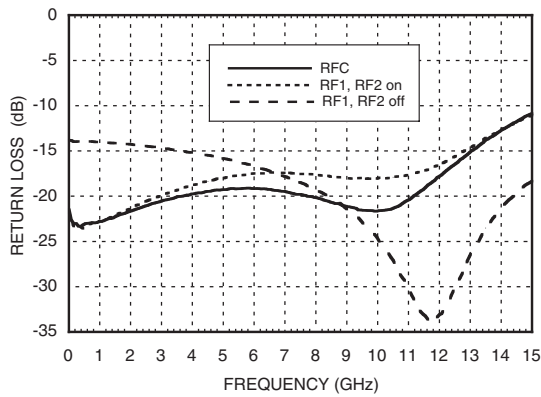
Insertion Loss



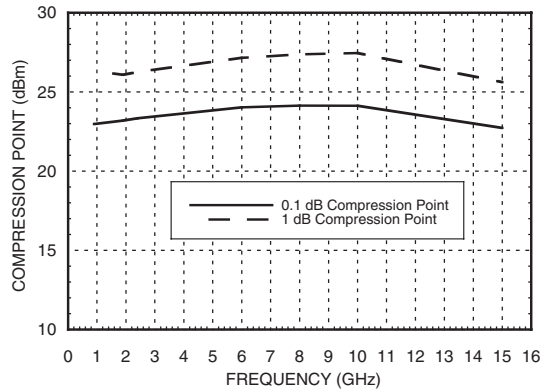
Isolation*



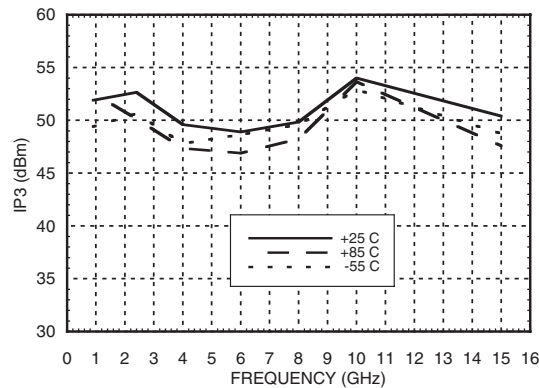
Return Loss



0.1 and 1 dB Input Compression Point



Input Third Order Intercept Point



*Isolation data taken with probe on the die

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Absolute Maximum Ratings

RF Input Power (A, \bar{A} , B, \bar{B} = 0/-5V) (0.5 - 6 GHz)	+30 dBm (@ +50 °C)
Control Voltage Range (A, \bar{A} , B, \bar{B})	+1.0V to -7.5 Vdc
Channel Temperature	150 °C
Thermal Resistance (R_{TH}) (junction to lead)	94 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

Control Voltages

State	Bias Condition
Low	0 to -0.2V @ 10 uA Max.
High	-5V @ 10 uA Typ. to -7V @ 45 uA Typ.



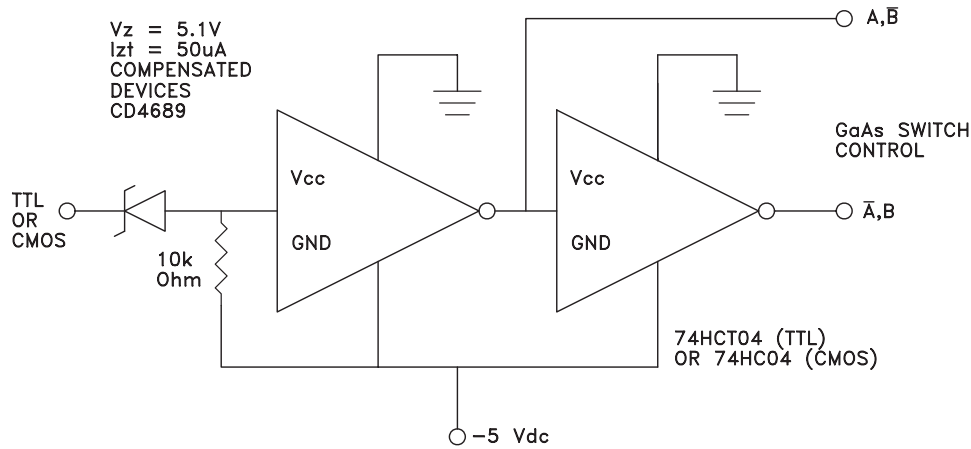
**ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS**

Truth Table

Control Input				Signal Path State	
B	\bar{B}	\bar{A}	A	RFC to RF1	RFC to RF2
Low	High	Low	High	ON	OFF
High	Low	High	Low	OFF	ON
High	Low	Low	High	OFF	OFF
Low	High	High	Low	ON	ON

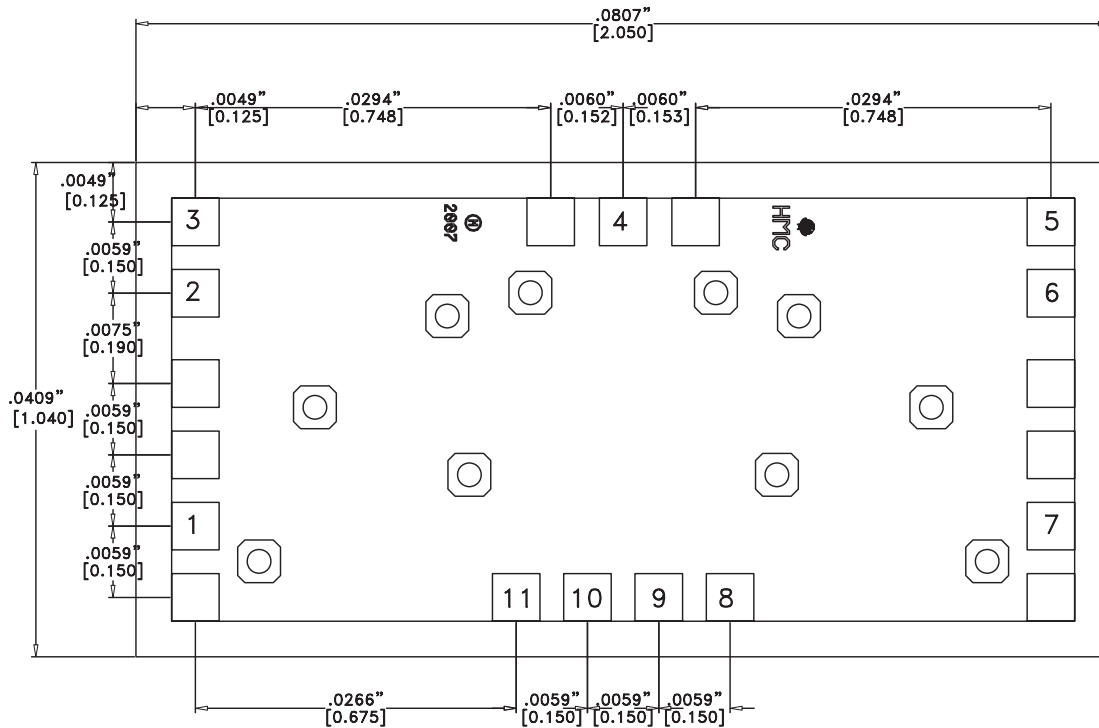
Caution: Do not "Hot Switch" power levels greater than +27 dBm (A, \bar{A} , B, \bar{B} = 0/-5V).

Suggested Driver Circuit for Single Line Control



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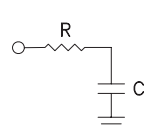

Outline Drawing



NOTES:

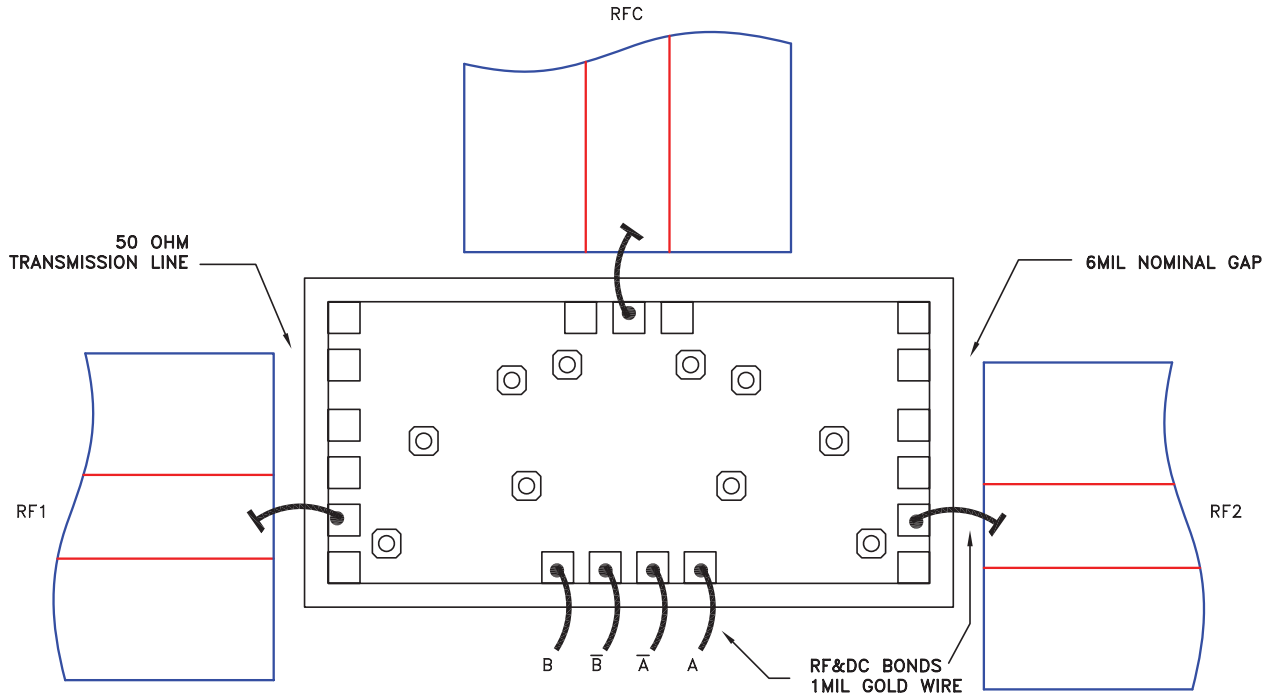
1. PACKAGE BODY MATERIAL: WHITE ALUMINA 92%
2. CONDUCTOR TRACES MATERIAL: THICK FILM TUNGSTEN.
3. LEAD, BASE, COVER MATERIAL: KOVAR™.
4. PLATING: ELECTROLYTIC GOLD 50 MICROINCHES MIN, OVER ELECTROLYTIC NICKEL 50 MICROINCHES MIN.
5. ALL DIMENSIONS ARE IN INCHES [MILLIMETERS].
6. TOLERANCES: ± 0.005 [0.13] UNLESS OTHERWISE SPECIFIED.
7. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

Pad Descriptions

Pad Number	Function	Description	Interface Schematic
1, 4, 7	RF1, RFC, RF2	This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required if RF line potential is not equal to 0V.	
2, 10	\bar{B}	See truth table and control voltage table. Alternate A & B control pads provided.	
3, 11	B		
5, 8	A		
6, 9	\bar{A}		
Die Bottom	GND	Die bottom must be connected to RF ground.	

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Assembly Diagram



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Mounting & Bonding Techniques for Millimeterwave GaAs MMICs

The die should be attached directly to the ground plane eutectically or with conductive epoxy (see HMC general Handling, Mounting, Bonding Note).

50 Ohm Microstrip transmission lines on 0.127mm (5 mil) thick alumina thin film substrates are recommended for bringing RF to and from the chip (Figure 1). If 0.254mm (10 mil) thick alumina thin film substrates must be used, the die should be raised 0.150mm (6 mils) so that the surface of the die is coplanar with the surface of the substrate. One way to accomplish this is to attach the 0.102mm (4 mil) thick die to a 0.150mm (6 mil) thick molybdenum heat spreader (moly-tab) which is then attached to the ground plane (Figure 2).

Microstrip substrates should be brought as close to the die as possible in order to minimize bond wire length. Typical die-to-substrate spacing is 0.076mm (3 mils).

Handling Precautions

Follow these precautions to avoid permanent damage.

Storage: All bare die are placed in either Waffle or Gel based ESD protective containers, and then sealed in an ESD protective bag for shipment. Once the sealed ESD protective bag has been opened, all die should be stored in a dry nitrogen environment.

Cleanliness: Handle the chips in a clean environment. DO NOT attempt to clean the chip using liquid cleaning systems.

Static Sensitivity: Follow ESD precautions to protect against $> \pm 250V$ ESD strikes.

Transients: Suppress instrument and bias supply transients while bias is applied. Use shielded signal and bias cables to minimize inductive pick-up.

General Handling: Handle the chip along the edges with a vacuum collet or with a sharp pair of bent tweezers. The surface of the chip has fragile air bridges and should not be touched with vacuum collet, tweezers, or fingers.

Mounting

The chip is back-metallized and can be die mounted with AuSn eutectic preforms or with electrically conductive epoxy. The mounting surface should be clean and flat.

Epoxy Die Attach: Apply a minimum amount of epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip once it is placed into position. Cure epoxy per the manufacturer's schedule.

Wire Bonding

Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire (DC bias, IF1 and IF2) or Ribbon Bond (RF and LO ports) 0.076 mm x 0.013 mm (3 mil x 0.5 mil) size is recommended. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. All bonds should be as short as possible <0.31 mm (12 mils).

