HRW0203B

Silicon Schottky Barrier Diode for Rectifying

REJ03G0155-0100Z
(Previous: ADE-208-1475)
Rev.1.00
Jan.06.2004

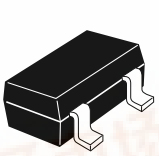
Features

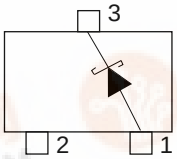
- Low forward voltage drop and suitable for high efficiency rectifying.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HRW0203B	S21	MPAK

Pin Arrangement





(Top View)

1. Anode
2. NC
3. Cathode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM} *1	30	V
Average rectified current	I_o *1	200	mA
Non-Repetitive peak forward surge current	I_{FSM} *2	3	A
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

Notes: 1. See from Fig.1 to Fig.5, with polyimide board
2. 50 Hz sine wave 1 pulse

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	0.5	V	I_F = 200 mA
Reverse current	I_R	—	—	50	μA	V_R = 30 V
Capacitance	C	—	40	—	pF	V_R = 0 V, f = 1 MHz

Main Characteristic

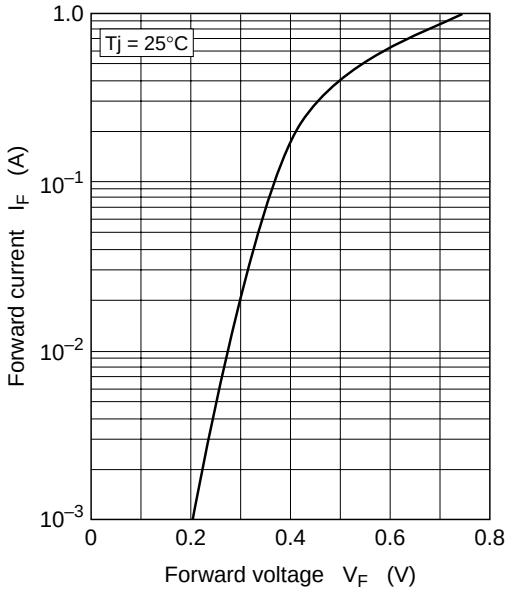


Fig.1 Forward current vs. Forward voltage

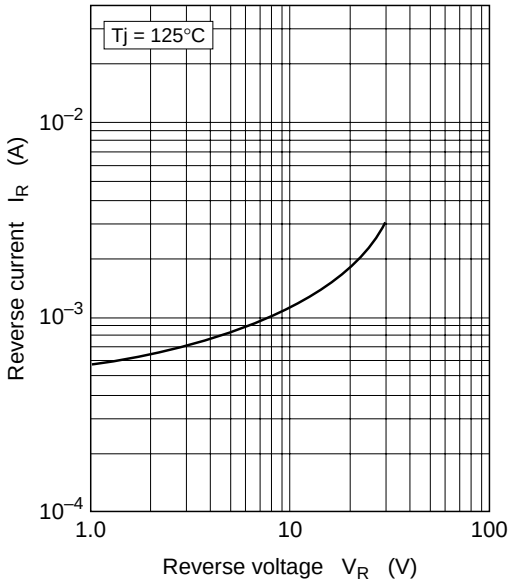


Fig.2 Reverse current vs. Reverse voltage

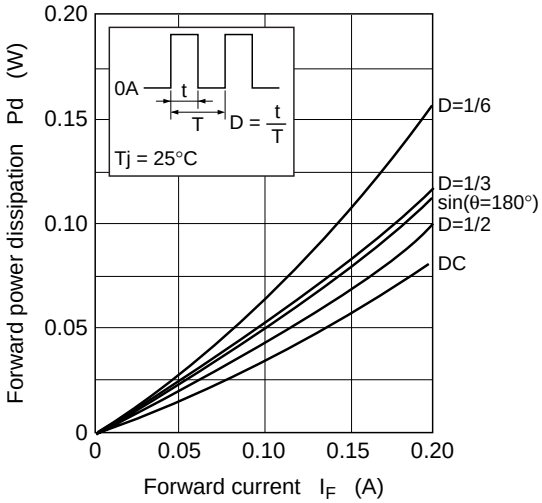


Fig.3 Forward power dissipation vs. Forward current

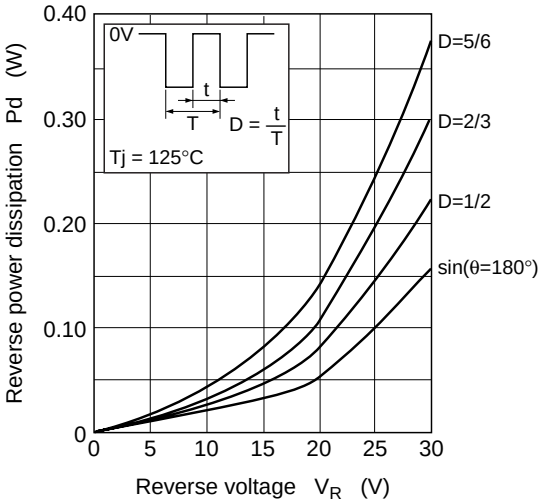


Fig.4 Reverse power dissipation vs. Reverse voltage

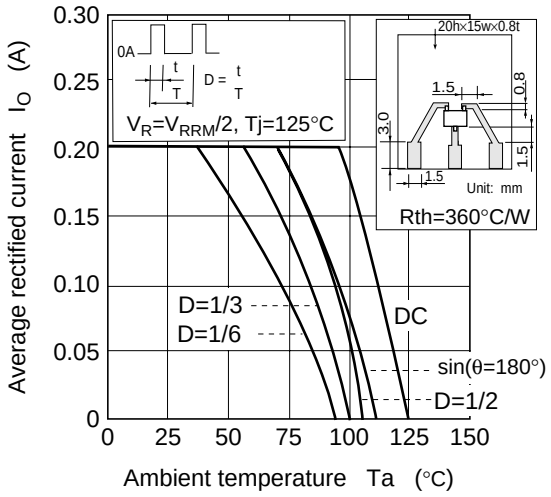


Fig.5 Average rectified current vs. Ambient temperature

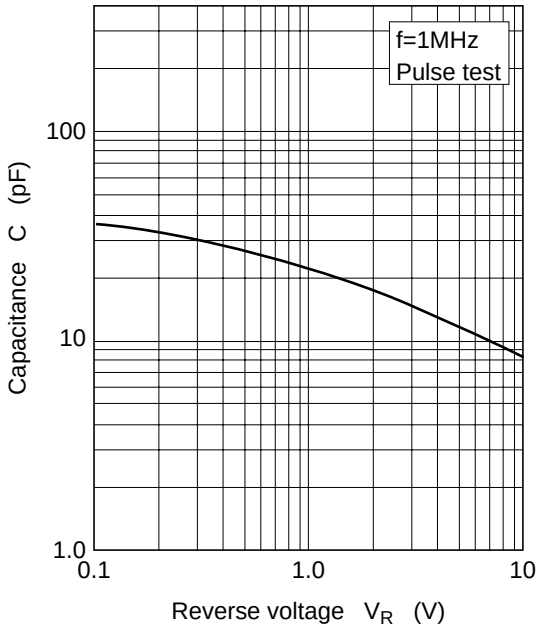
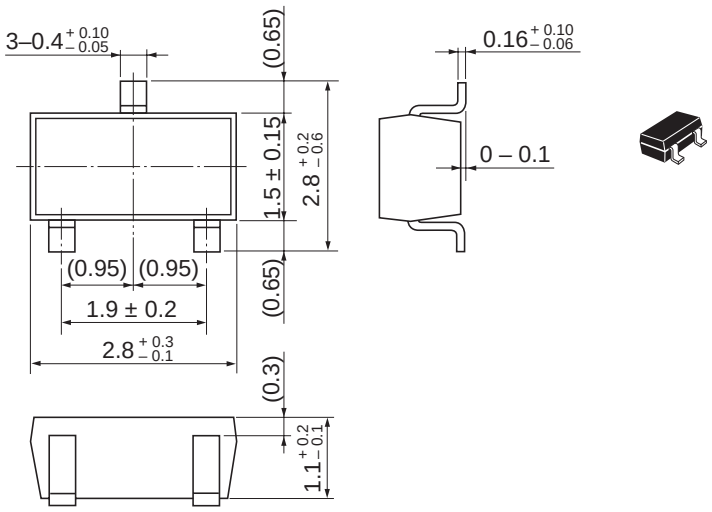


Fig.6 Capacitance vs. Reverse voltage

Package Dimensions

As of January, 2003

Unit: mm



Package Code	MPAK
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.011 g

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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http://www.renesas.com

Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500 Fax: <1> (408) 382-7501

Renesas Technology Europe Limited.
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom
Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

Renesas Technology Europe GmbH
Dornacher Str. 3, D-85622 Feldkirchen, Germany
Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd.
7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2375-6836

Renesas Technology Taiwan Co., Ltd.
FL 10, #99, Fu-Hsing N. Rd., Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.
26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001