HSB88WK

Silicon Schottky Barrier Diode for High Speed Switching

HITACHI

ADE-208-1042 (Z) Rev. 0 Jan. 2001

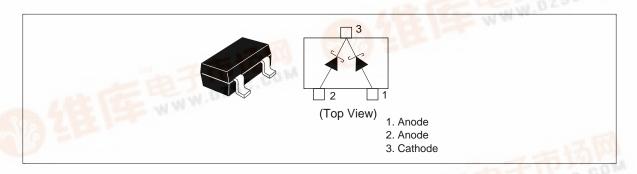
Features

- Proof against high voltage.
- CMPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code	
HSB88WK	C4	CMPAK	

Outline





HSB88WK

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Value	Unit
Reverse voltage	V_R	10	V
Average rectified current	I ₀ *1	15	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Note: Per one device.

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F1}	350	_	420	mV	I _F = 1 mA
	V_{F2}	500	_	580	_	I _F = 10 mA
Reverse current	I _{R1}	_	_	0.2	μΑ	$V_R = 2V$
	I _{R2}	_	_	10	_	V _R = 10V
Capacitance	С	_		0.80	pF	$V_R = 0V$, $f = 1 MHz$
Capacitance deviation	ΔC	_	_	0.10	pF	V _R = 0V, f = 1 MHz
Forward voltage deviation	ΔV_{F}	_	_	10	mV	I _F = 10 mA
ESD-Capability *1		30		_	V	$C = 200 \text{ pF}, R = 0 \Omega$, Both forward and reverse direction 1 pulse.

Note: Failure criterion ; IR > 0.4μ A at VR = 2 V

Main Characteristic

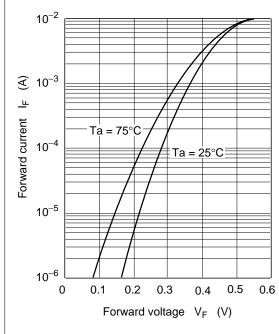
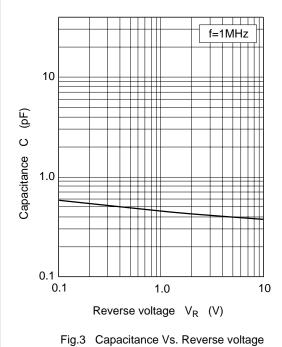


Fig.1 Forward current Vs. Forward voltage



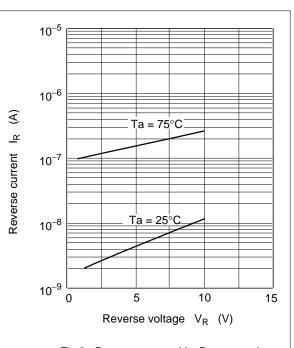
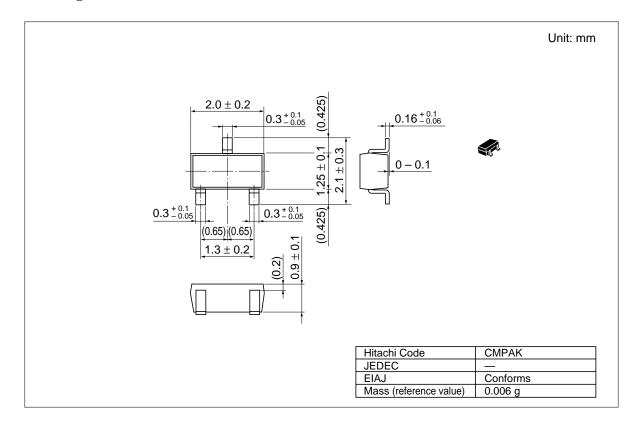


Fig.2 Reverse current Vs. Reverse voltage

HSB88WK

Package Dimensions



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Semiconductor & Integrated Circuits. Nippon Bldg., 2-6-2, Ŏhte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose,CA 95134 Tel: <1> (408) 433-1990 Fax: <1>(408) 433-0223 Tel: <49> (89) 9 9180-0

Hitachi Europe GmbH Electronic Components Group Dornacher Stra§e 3 D-85622 Feldkirchen, Munich Germany

Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd. Electronic Components Group. Whitebrook Park Lower Cookham Road Maidenhead

Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 585160

Hitachi Tower 16 Collyer Quay #20-00, Singapore 049318 Tel: <65>-538-6533/538-8577 Fax: <65>-538-6933/538-3877 URL: http://www.hitachi.com.sq

Hitachi Asia Ltd.

Hitachi Asia Ltd. (Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road, Hung-Kuo Building,

Taipei (105), Taiwan Tel: <886>-(2)-2718-3666 Fax: <886>-(2)-2718-8180 Telex : 23222 HAS-TP URL: http://www.hitachi.com.tw

Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower, World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon, Hong Kong

Tel: <852>-(2)-735-9218 Fax: <852>-(2)-730-0281 URL: http://www.hitachi.com.hk

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