RENESAS

HZN6.8ZMFA

Silicon Planar Zener Diode for Surge Absorb

REJ03G0032-0100Z

(Previous: ADE-208-1456)

Rev.1.00 May.08. 2003

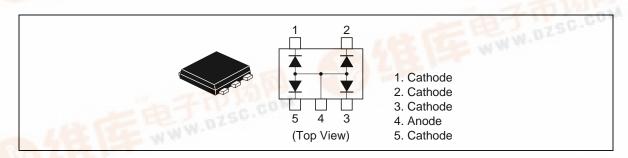
Features

- HZN6.8ZMFA has four devices in a monolithic, and can absorb surge.
- VSON-5T Package is suitable for high density surface mounting.

Ordering Information

Type No.	Laser Mark	Package Code
HZN6.8ZMFA	68*(* : Let to Month Code)	VSON-5T

Pin Arrangement



HZN6.8ZMFA

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Power dissipation	Pd *1	150	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. Four device total, See Fig.2.

Electrical Characteristics *1

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Zener voltage	V _z	6.47	_	7.00	V	I _z = 5 mA, 40 ms pulse
Reverse current	I _R	_	_	0.5	μА	V _R = 3.5 V
Capacitance	С	_	_	25	pF	$V_R = 0 \text{ V, } f = 1 \text{ MHz}$
Dynamic resistance	r _d	_	_	30	Ω	$I_z = 5 \text{ mA}$
ESD-Capability *2 *3	_	25	_	_	kV	$C = 150 \text{ pF}, R = 330 \Omega, Both forward}$ and reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ; $I^{}_{R}\!>\!0.5~\mu\text{A}$ at $V^{}_{R}\!=\!3.5~V.$

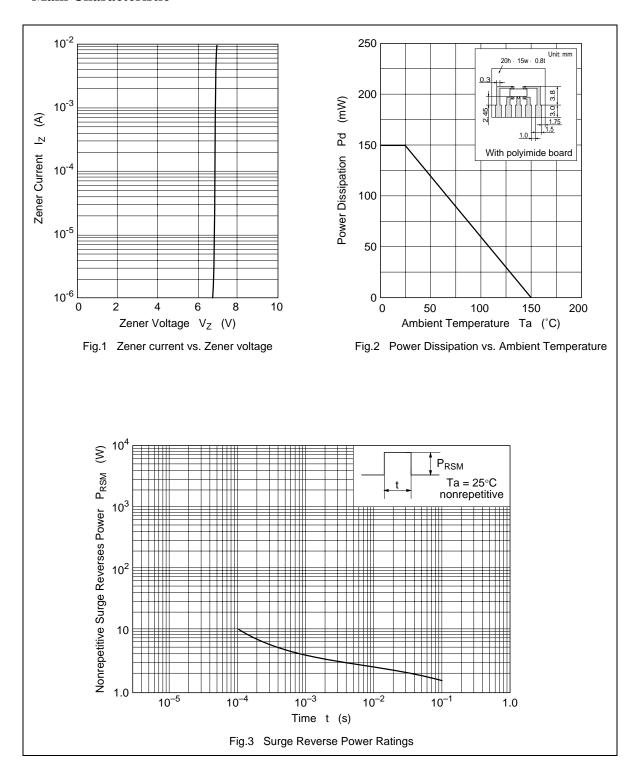
3. Between cathode and anode.

Month Code

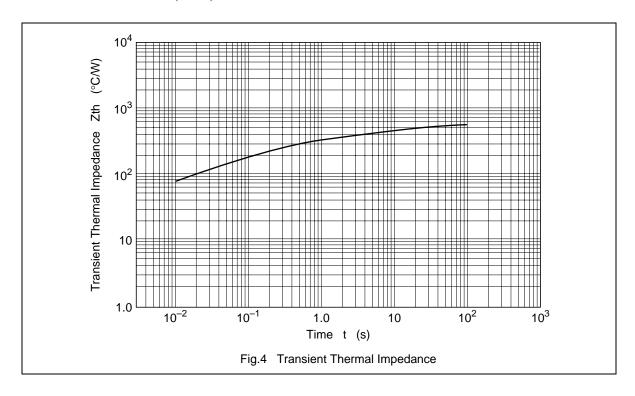
Month of Manufacture	Month Code
January	A
February	В
March	С
April	D
May	E
June	F

Month of Manufacture	Month Code
July	G
August	Н
September	J
October	K
November	L
December	M

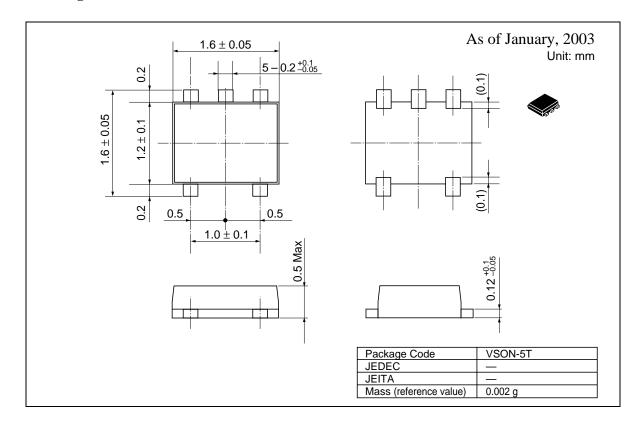
Main Characteristic



Main Characteristic (cont.)



Package Dimensions



Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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