

FAIRCHILD

SEMICONDUCTOR



KA2S0965/KA2S09655 Fairchild Power Switch(SPS)

Features

- Wide operating frequency range up to (150kHz)
- Pulse by pulse over current limiting
- Over load protection
- Over voltage protecton (Min. 23V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- External sync terminal
- · Latch up Mode

Description

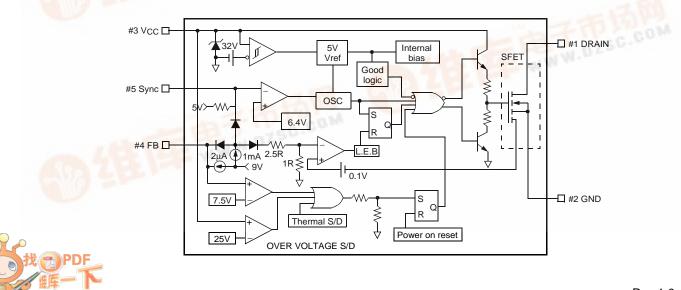
TO-3P-5L

The SPS product family is specially designed for an offline SMPS with minimal external components. The SPS consist of high voltage power SenseFET and current mode PWM Controller IC. control IC features a trimmed oscillator, under voltage lock-out, leading edge blanking, optimized gate turnon/turn-off driver, thermal shut down protection, over voltage protection, temperature compensated precision current sources for loop compensation and fault protection circuit. Compared to discrete MOSFET and controller or RCC switching converter solution, a SPS can reduce total component count, design size, weight and at the same time increase &efficiency, productivity, and system reliability. It has a basic platform well suited for cost-effective Monitor power supply.

1. DRAIN 2. GND 3. Vcc 4. FB 5. Sync



WWW.B



Internal Block Diagram

Absolute Maximum Ratings

Characteristic	Symbol	Value	Unit	
Maximum Drain voltage ⁽¹⁾	VD,MAX	650	V	
Drain-Gate voltage ($R_{GS}=1M\Omega$)	Vdgr	650	V	
Gate-source (GND) voltage	Vgs	±30	V	
Drain current pulsed ⁽²⁾	IDM	36.0	ADC	
Single pulsed avalanche energy ⁽³⁾	Eas	950	mJ	
Continuous drain current (Tc=25°C)	ID	9.0	ADC	
Continuous drain current (T _C =100°C)	۱D	5.8	ADC	
Maximum Supply voltage	VCC,MAX	30	V	
Input voltage range	VFB	-0.3 to V _{SD}	V	
Total newsr dissipation	PD	170	W	
Total power dissipation	Derating	1.33	W/°C	
Operating ambient temperature	TA	-25 to +85	٥C	
Storage temperature	TSTG	-55 to +150	°C	

Notes:

1.Tj=25°C to 150°C 2.Repetitive rating: Pulse width limited by maximum junction temperature 3.L=20mH, V_{DD}=50V, R_G=27 Ω , starting Tj=25 °C

Electrical Characteristics (SFET part)

 $(Ta = 25^{\circ}C \text{ unless otherwise specified})$

Characteristic	Symbol	Test condition	Min.	Тур.	Max.	Unit
Drain-source breakdown voltage	BVDSS	V _{GS} =0V, I _D =50μA	650	-	-	V
Zero gate voltage drain current	IDSS	VDS=Max., Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max., Rating, VGS=0V, TC=125°C	-	-	200	mA
Static drain-source on resistance ^(note)	RDS(ON)	VGS=10V, ID=4.5A	-	0.96	1.2	W
Forward transconductance (note)	g fs	V _{DS} =50V, I _D =4.5A	5.0	-	-	S
Input capacitance	Ciss		-	1750	-	pF
Output capacitance	Coss	VGS=0V, VDS=25V, f=1MHz	-	190	-	
Reverse transfer capacitance	C _{rss}		-	78	-	
Turn on delay time	td(on)	VDD=0.5BVDSS, ID=9.0A	-	20	50	
Rise time	tŗ	(MOSFET switching time are essentially independent of operating temperature)	-	23	55	nS
Turn off delay time	td(off)		-	85	180	
Fall time	tf		-	30	70	
Total gate charge (gate-source+gate-drain)	Qg	V _{GS} =10V, I _D =9.0A, V _{DS} =0.5BV _{DSS} (MOSFET switching time are essentially independent of	-	74	95	
Gate-source charge	Qgs		-	12	-	nC
Gate-drain (Miller) charge	Qgd	operating temperature)	-	35.4	-	

Note:

Pulse test: Pulse width $\leq 300\mu$ S, duty cycle $\leq 2\%$ S = $\frac{1}{5}$

$$S = \frac{1}{R}$$

Electrical Charcteristics (CONTROL part)

(Ta = 25°C unless otherwise specified)

Characteristic	Symbol	Test condition	Min.	Тур.	Max.	Unit
UVLO SECTION						
Start threshold voltage	VSTART	-	14	15	16	V
Stop threshold voltage	VSTOP	After turn on	9	10	11	V
OSCILLATOR SECTION			1			
Initial accuracy	Fosc	Ta=25°C	18	20	22	kHz
Frequency change with temperature ⁽²⁾	$\Delta F / \Delta T$	–25°C≤Ta≤+85°C	-	±5	±10	%
Maximum duty cycle	Dmax	-	92	95	98	%
FEEDBACK SECTION						
Feedback source current	IFB	Ta=25°C, Vfb=GND	0.8	1	1.2	mA
Shutdown Feedback voltage	VSD	-	6.9	7.5	8.1	V
Shutdown delay current	Idelay	Ta=25°C, 5V≤Vfb≤VSD	1.4	1.8	2.2	μA
SYNC. & SOFT START SECTION			1			
Soft start voltage	Vss	VFB=2V	4.7	5.0	5.3	V
Soft start current	Iss	Sync & S/S=GND	0.8	-	-	mA
Sync threshold voltage ⁽³⁾	Vsyth	Vfb=5V	6.0	6.4	6.8	V
REFERENCE SECTION						
Output voltage ⁽¹⁾	Vref	Ta=25°C	4.80	5.00	5.20	V
PROTECTION SECTION						
Thermal shutdown temperature (Tj) ⁽¹⁾	TSD	-	140	160	-	°C
Peak Current Limit	IOVER	KA2S0965	5.28	6.00	6.72	A
		KA2S09655	4.40	5.00	5.60	
TOTAL DEVICE SECTION			1			
Start Up current	ISTART	V _{CC} =14V	0.1	0.3	0.55	mA
Operating supply current (control part only)	IOP	Ta=25°C	6	12	18	mA
V _{CC} zener voltage	Vz	ICC=20mA	30	32.5	35	V

NOTE:

1. These parameters, although guaranteed, are not 100% tested in production

2. These parameters, although guaranteed, are tested in EDS(water test) process

3. The amplitude of the sync. pulse is recommended to be between 2V and 3V for stable sync. function.

Typical Performance Characteristics

(These characteristic graphs are normalized at $Ta = 25^{\circ}C$)

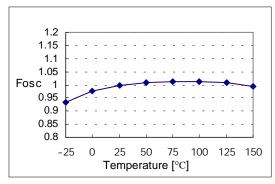


Figure 1. Operating Frequency

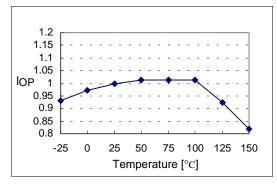


Figure 3. Operating Supply Current

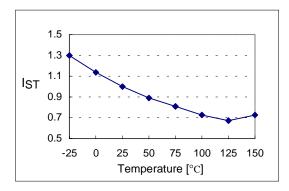


Figure 5. Start up Current

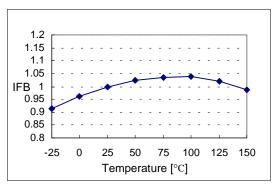


Figure 2. Feedback Source Current

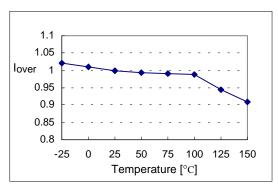


Figure 4. Peak Current Limit

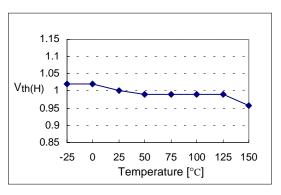


Figure 6. Start Threshold Voltage

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at $Ta = 25^{\circ}C$)

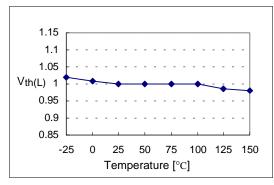


Figure 7. Stop Threshold Voltage

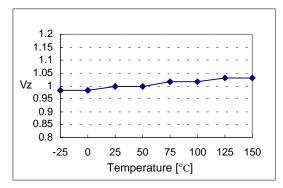


Figure 9. VCC Zener Voltage

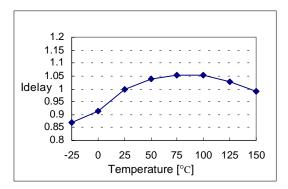


Figure 11. Shutdown Delay Current

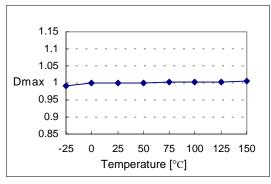


Figure 8. Maximum Duty Cycle

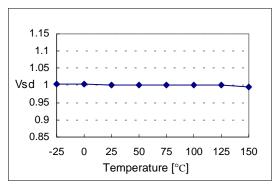


Figure 10. Shutdown Feedback Voltage

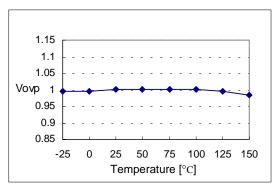


Figure 12. Over Voltage Protection

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at $Ta = 25^{\circ}C$)

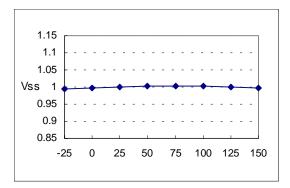


Figure13. Soft Start Voltage

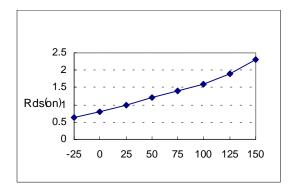
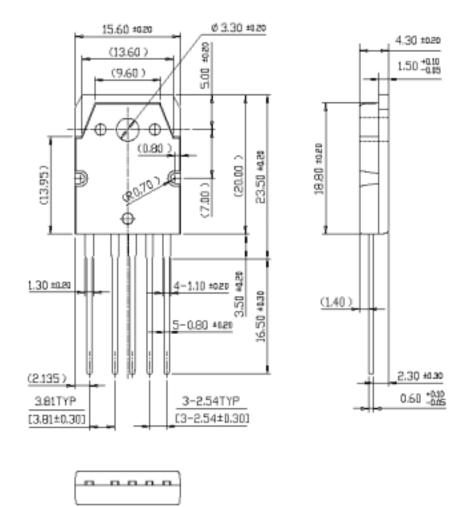


Figure 14. Static Drain-Source on Resistance

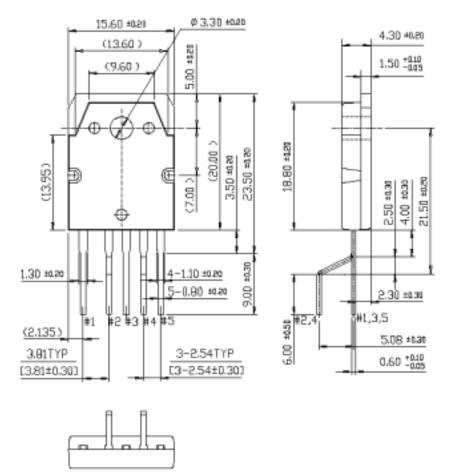
Package Dimensions

TO-3P-5L



Package Dimensions (Continued)

TO-3P-5L (Forming)



Ordering Information

Product Number	Package	Rating	Over current protection		
KA2S0965-TU	TO-3P-5L	650V, 9A	6A		
KA2S0965-YDTU	TO-3P-5L(Forming)	050V, 9A			
KA2S09655-TU	TO-3P-5L	650V, 9A	5A		
KA2S09655-YDTU	TO-3P-5L(Forming)	050V, 9A	JA		

TU : Non Forming Type YDTU : Forming Type

KA2S0965/KA2S09655

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