查询TC4001BP\_07供应商 TOSHIBA

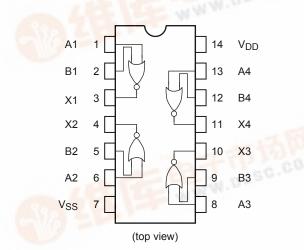
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4001BP,TC4001BF,TC4001BFN,TC4001BFT

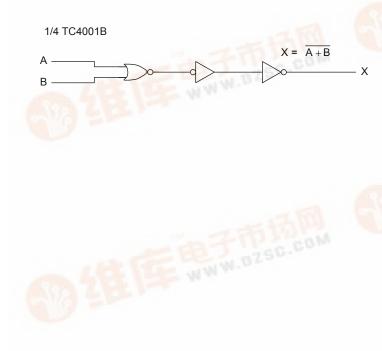
TC4001B Quad 2 Input NOR Gate

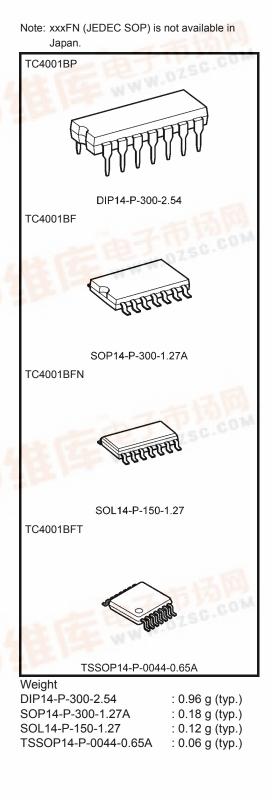
The TC4001B is 2-input positive NOR gate, respectively. Since the outputs of these gates are equipped with the buffers, the input/output transmission characteristics have been improved and the variation of transmission time due to an increase in the load capacity is kept minimum.

#### **Pin Assignment**



### Logic Diagram





#### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
DC supply voltage	V <sub>DD</sub>	$V_{\rm SS}$ – 0.5 to $V_{\rm SS}$ + 20	V
Input voltage	V <sub>IN</sub>	V <sub>SS</sub> – 0.5 to V <sub>DD</sub> + 0.5	V
Output voltage	V <sub>OUT</sub>	V <sub>SS</sub> – 0.5 to V <sub>DD</sub> + 0.5	V
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Operating Ranges (V<sub>SS</sub> = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V <sub>DD</sub>	—	3	_	18	V
Input voltage	V <sub>IN</sub>		0		V <sub>DD</sub>	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

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### TC4001BP/BF/BFN/BFT

### Static Electrical Characteristics (V<sub>SS</sub> = 0 V)

Characteristics			Test Condition		-40°C		25°C			85°C		
		Symbol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
		V <sub>OH</sub>		5	4.95	_	4.95	5.00	_	4.95	_	
High-level output voltage	$ I_{OUT}  < 1 \mu A$		10	9.95	—	9.95	10.00	—	9.95	—	V	
•	0		$V_{IN} = V_{SS}, V_{DD}$	15	14.95	_	14.95	15.00	-	14.95	-	
				5		0.05	_	0.00	0.05	_	0.05	
Low-leve output ve		V <sub>OL</sub>	l <sub>OUT</sub>   < 1 μΑ	10	—	0.05	—	0.00	0.05	—	0.05	V
			$V_{IN} = V_{SS}, V_{DD}$	15	—	0.05	—	0.00	0.05	—	0.05	
			V <sub>OH</sub> = 4.6 V	5	-0.61	-	-0.51	-1.0	-	-0.42	-	mA
			V <sub>OH</sub> = 2.5 V	5	-2.50	—	-2.10	-4.0	_	-1.70	—	
Output h current	nigh	IOH	V <sub>OH</sub> = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_	
ourront			V <sub>OH</sub> = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_	
			V <sub>IN</sub> = V <sub>SS</sub>									
		I <sub>OL</sub>	V <sub>OL</sub> = 0.4 V	5	0.61	-	0.51	1.2		0.42		mA
Output lo	w		V <sub>OL</sub> = 0.5 V	10	1.50	_	1.30	3.2	_	1.10	_	
current			V <sub>OL</sub> = 1.5 V	15	4.00	_	3.40	12.0	_	2.80	_	
			V <sub>IN</sub> = V <sub>SS</sub> , V <sub>DD</sub>									
		V <sub>IH</sub>	V <sub>OUT</sub> = 0.5 V	5	3.5		3.5	2.75		3.5		v
Input hig	ıh		V <sub>OUT</sub> = 1.0 V	10	7.0	_	7.0	5.50	_	7.0	_	
voltage	,		V <sub>OUT</sub> = 1.5 V	15	11.0	_	11.0	8.25	_	11.0	_	
			l <sub>OUT</sub>   < 1 μΑ									
			V <sub>OUT</sub> = 4.5 V	5	_	1.5	_	2.25	1.5	_	1.5	
Input low	v	V <sub>IL</sub>	V <sub>OUT</sub> = 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0	V
voltage			V <sub>OUT</sub> = 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	
			l <sub>OUT</sub>   < 1 μΑ									
Input	"H" level	IIН	V <sub>IH</sub> = 18 V	18	_	0.1	_	10 <sup>-5</sup>	0.1	_	1.0	
current	"L" level	IIL	V <sub>IL</sub> = 0 V	18		-0.1	_	-10 <sup>-5</sup>	-0.1	_	-1.0	μA
		ent I <sub>DD</sub>		5		0.25	_	0.001	0.25	_	7.5	
Quiesce supply c			V <sub>IN</sub> = V <sub>SS</sub> , V <sub>DD</sub> (Note)	10	—	0.50	—	0.001	0.50	—	15.0	μA
				15	_	1.00	—	0.002	1.00	_	30.0	

Note: All valid input combinations.

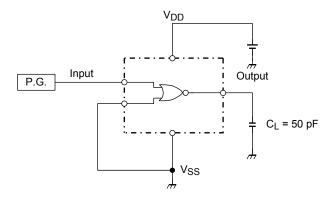
### Dynamic Electrical Characteristics (Ta = 25°C, V<sub>SS</sub> = 0 V, C<sub>L</sub> = 50 pF)

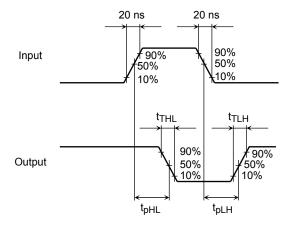
Characteristics	Symbol	Test Condition	Min	Tun	Max	Unit	
Characteristics	Symbol		V <sub>DD</sub> (V)	IVIIII	Тур.	Max	Unit
Output transition time	tтLH		5	_	70	200	
		_	10	—	35	100	ns
			15	—	30	80	
Output transition time			5	_	70	200	
	t <sub>THL</sub>	—	10	—	35	100	ns
			15	—	30	80	
Propagation delay time	<sup>t</sup> pLH		5	_	65	200	
		—	10	—	30	100	ns
			15	—	25	80	
Propagation delay time	t <sub>pHL</sub>		5	_	65	200	
		—	10	—	30	100	ns
			15	—	25	80	
Input capacitance	C <sub>IN</sub>	—	_	5	7.5	pF	

### **Circuit and Waveform for Measurement of Dynamic Characteristics**

Circuit

#### Waveform





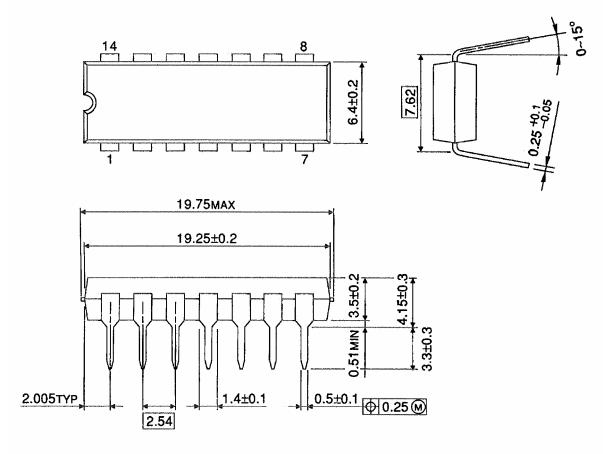
## <u>TOSHIBA</u>

### TC4001BP/BF/BFN/BFT

### **Package Dimensions**

DIP14-P-300-2.54

Unit : mm



Weight: 0.96 g (typ.)

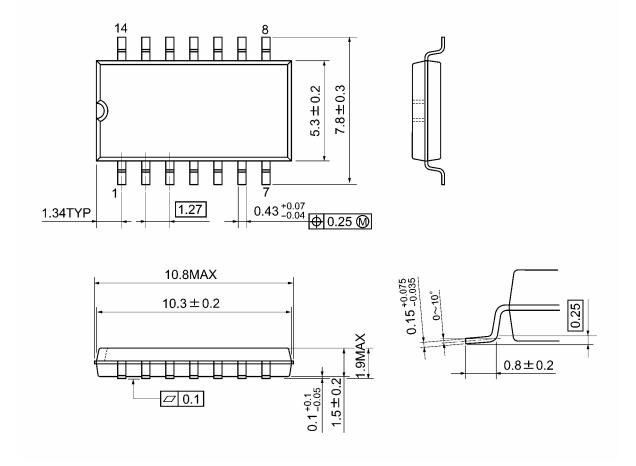
## <u>TOSHIBA</u>

### Package Dimensions

SOP14-P-300-1.27A



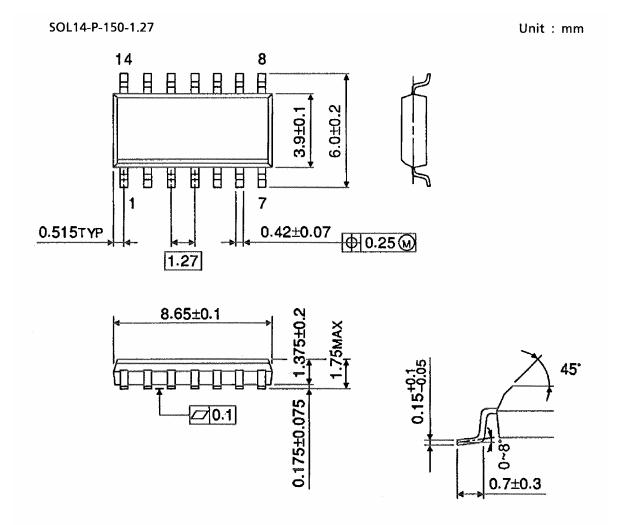
TC4001BP/BF/BFN/BFT



Weight: 0.18 g (typ.)

## **TOSHIBA**

### Package Dimensions (Note)



Note: This package is not available in Japan.

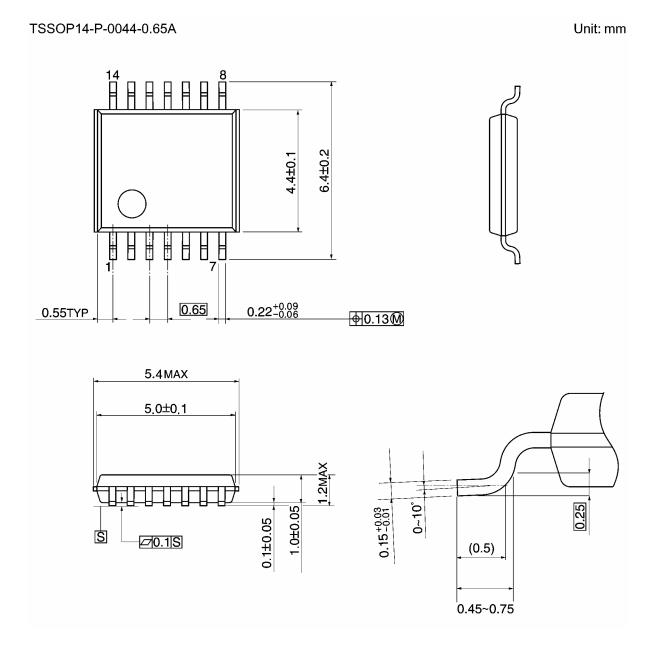
Weight: 0.12 g (typ.)

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## <u>TOSHIBA</u>

### TC4001BP/BF/BFN/BFT

### Package Dimensions



Weight: 0.06 g (typ.)

## TOSHIBA

#### **RESTRICTIONS ON PRODUCT USE**

Handbook" etc.

20070701-EN GENERAL

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