TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC4SU69F

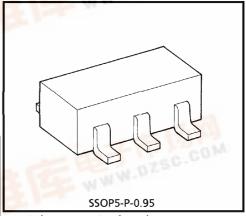
## **INVERTER GATE**

The TC4SU69F is single inverter.

Therefore, this is suitable for the applications of C, R oscillator circuits, crystal oscillator circuits and linear amplifiers in addition to its application as inverters.

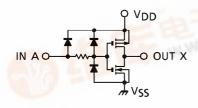
## MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	$V_{DD}$	Vss - 0.5~Vss + 20	V
Input Voltage	V <sub>IN</sub>	$V_{SS} = 0.5 \sim V_{DD} + 0.5$	V
Output Voltage	Vout	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
DC Input Current	IN	± 10	mA
Power Dissipation	PD	200	mW
Operating Temperature Range	T <sub>opr</sub>	<b>- 40∼85</b>	ů
St <mark>orage Tempera</mark> ture Range	T <sub>stg</sub>	- 65~150	°C
Lead Temperature (10s)	TL	260	°C

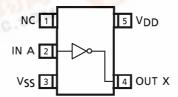


Weight: 0.016g (Typ.)

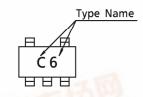
## LOGIC DIAGRAM



## PIN CONFIGURATION (TOP VIEW)



### Marking





# RECOMMENDED OPERATING CONDITIONS ( $V_{SS} = 0V$ )

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	$V_{DD}$	_	3	_	18	V
Input Voltage	VIN	_	0	_	$V_{DD}$	V

# STATIC ELECTRICAL CHARACTERISTICS ( $V_{SS} = 0V$ )

CHARACTERISTIC		SYM- BOL	TEST CONDITION	(S) (DD	– 40°C		25°C			85°C		UNIT
					MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	1 UINI I
High-Level	Ì		llour / 1,4A	5	4.95	_	4.95	5.00	_	4.95	_	
Output Voltage		Vон	I <sub>OUT</sub>  <1μΑ  V <sub>IN</sub> =V <sub>SS</sub>	10	9.95		9.95	10.00	_	9.95		
			VIN - 422	15	14.95	_	14.95	15.00	_	14.95	_	v
Low-Level			  lout <1μΑ	5	_	0.05	—	0.00		_	0.05	Ů
Output Vo	oltage	VOL	$ V_{IN} = V_{DD}$	10	_	0.05	_	0.00		<b>—</b>	0.05	
Output ve	ortage		VIN	15	_	0.05	_	0.00	0.05	_	0.05	
			V <sub>OH</sub> = 4.6V	5	- 0.61	_	- 0.51	- 1.0	_	- 0.42	_	
Output Hi	ah		$V_{OH} = 2.5V$	5	<b>–</b> 2.5		- 2.1	- 4.0		<b>–</b> 1.7	_	
Current	gn	ЮН	V <sub>OH</sub> = 9.5V	10	<b>–</b> 1.5		- 1.3	- 2.2		- 1.1		
Current			V <sub>OH</sub> = 13.5V	15	- 4.0	_	- 3.4	- 9.0	_	- 2.8	_	
			$V_{IN} = V_{SS}$									m ^
			$V_{OL} = 0.4V$	5	0.61		0.51	1.2	_	0.42	_	mA
Output Lo	w	 	$V_{OL} = 0.5V$	10	1.5	_	1.3	3.2	_	1.1	_	
Current		lOL	V <sub>OL</sub> = 1.5V	15	4.0	_	3.4	12.0	_	2.8	_	
			$V_{IN} = V_{DD}$									
		VIH	V <sub>OUT</sub> = 0.5V	5	4.0	_	4.0	_	_	4.0	_	
Innut High	. \/al+aga		V <sub>OUT</sub> = 1.0V	10	8.0	_	8.0	_	_	8.0	_	
Input High	i voitage		V <sub>OUT</sub> = 1.5V	15	12.0	_	12.0	_	_	12.0	_	
			l <sub>OUT</sub>  <1μΑ									.,
			V <sub>OUT</sub> = 4.5V	5	_	1.0	_	_	1.0	_	1.0	V
Input Low Voltage	VIL	V <sub>OUT</sub> = 9.0V	10	—	2.0	—	_	2.0	—	2.0		
		V <sub>OUT</sub> = 13.5V	15	_	3.0	—	_	3.0	_	3.0		
		l <sub>OUT</sub>  <1μΑ	1									
Input	H Level	ΊΗ	V <sub>IH</sub> = 18V	18	_	0.1	_	<b>10</b> <sup>- 5</sup>	0.1	_	1.0	_
Current	L Level	IJL	V <sub>IL</sub> = 0V	18	_	- 0.1	_	<b>–</b> 10 <sup>– 5</sup>	- 0.1	_	- 1.0	$\mu$ A
Quiescent				5	_	0.25	_	0.001	0.25	_	7.5	
Device Current		IDD	$V_{IN} = V_{SS}$ , $V_{DD}$	10	_	0.5	_	0.001	0.5	_	15	$\mu$ A
				15	_	1.0	—	0.002	1.0	_	30	

2 2001-05-31

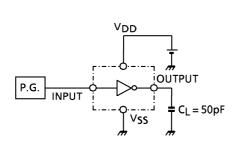
## **DYNAMIC ELECTRICAL CHARACTERISTICS** (Ta = $25^{\circ}$ C, V<sub>SS</sub> = 0V, C<sub>L</sub> = 50pF)

CHARACTERISTIC	SYMBOL	TEST CONDITION	V <sub>DD</sub> (V)	MIN.	TYP.	MAX.	UNIT	
Output Transition Time			5	_	70	200		
(Low to High)	tTLH	_	10	_	35	100		
(Low to High)			15	_	30	80		
Output Transition Time			5	_	70	200	ns	
Output Transition Time	tTHL	_	10	_	35	100		
(High to Low)			15	_	30	80		
	t <sub>pLH</sub>		5	_	55	110		
Propagation Delay Time		_	10	_	30	60		
			15	_	25	50		
	t <sub>pHL</sub>		5	_	55	110	ns	
Propagation Delay Time		_	10	_	30	60		
			15	_	25	50		
Input Capacitance	C <sub>IN</sub>		•	_	7.5	15	pF	

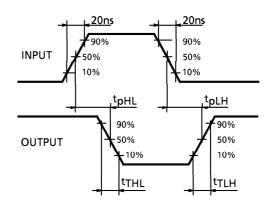
3

## CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

**TEST CIRCUIT** 

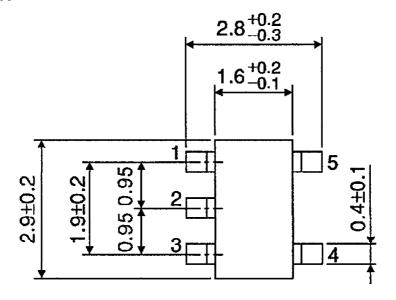


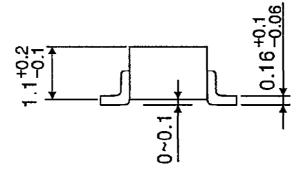
### **WAVEFORM**



## PACKAGE DIMENSIONS SSOP5-P-0.95

Unit: mm





Weight: 0.016g (Typ.)

4 2001-05-31

#### **RESTRICTIONS ON PRODUCT USE**

000707EBA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- ◆ The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.

5

■ The information contained herein is subject to change without notice.