



TC4523
PRE1_20040903

13.75 - 14.5 GHz 0.5W MMIC

FEATURES

- P₁ dB: 27 dBm
- Small Signal Gain: 28 dB
- IP3: 36 dBm
- Match to 50 Ω Operation
- Bias condition: 550 mA @ 7 V

PHOTO ENLARGEMENT

DESCRIPTION

The TC4523 is a 4 stage PHEMT MMIC power amplifier. It is designed for use in low cost, high volume, Ku band applications. The MMIC is matched to 50ohm operation. No external matching component is required. It provides a typical gain of 28 dB and P₁ dB power of more than 27 dBm. Typical bias condition is 7V at 550 mA. The MMIC is a packaged in a copper based ceramic 6 pins power package. The copper based carrier of the package allows direct soldering of the device to the PCB for proper heat sinking.

APPLICATIONS

- Ku Band VSAT Transmit Subsystem

ELECTRICAL SPECIFICATIONS (T_A=25 °C)

SYMBOL	DESCRIPTION	MIN	TYP	MAX	UNITS
FREQ	Frequency Range	13.75		14.5	GHz
SSG	Small Signal Gain	28	32		dB
P ₁ dB	Output Power at 1 dB Gain Compression	26	27		dBm
P ₃ dB	Output Power at 3 dB Gain Compression	27	28		dBm
IP3	Third Order Intercept Point	35	36		dBm
VSWR, In	Input VSWR		2.5:1		-
VSWR, Out	Output VSWR		2:1		
VDD	Supply Voltage		7		Volt
Vg	Gate Voltage	-0.5	-1.0	-1.5	Volt
IDD	Current Supply Without RF		550		mA
IDP ₁	Current Supply @ Pout = P ₁ dB		600		mA

Absolute Maximum Ratings

Symbol	Parameter/Conditions	Min.	Max.	Units
V _{dd}	Drain-Source Voltage		12	Volts
I _{dd}	Total Drain Current		1000	mA
P _{in}	RF Input Power		11	dBm
P _t	Power Dissipation		6	W
T _{ch}	Operating Channel Temperature		175	°C
T _{STG}	Storage Temperature	-65	175	°C

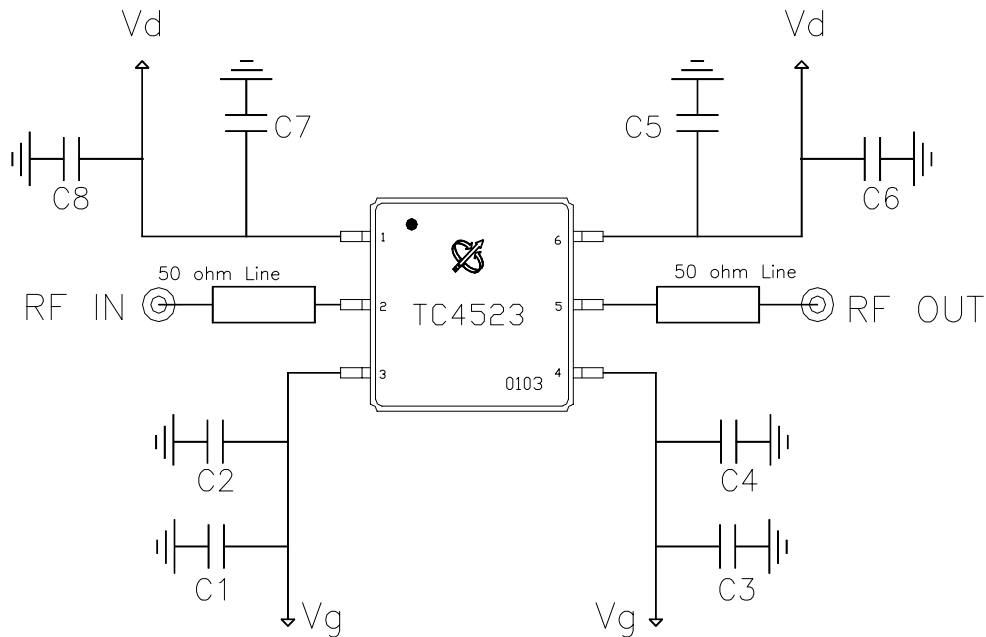
Note:

1. This GaAs MMIC is susceptible to damage from Electrostatic Discharge. Proper precautions should be used when handling these devices.
2. Specifications subject to change without notice.

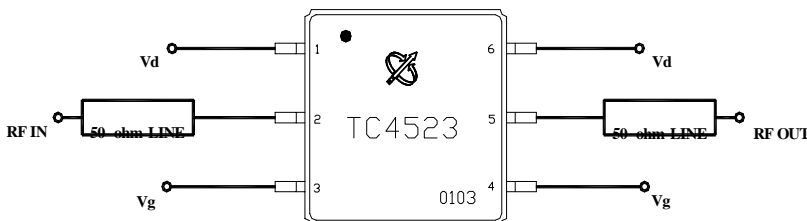


TEST CIRCUITS

Evaluation Board Schematic



Part Type	Reference Designator	Description	Manufacturer	Part Number
Capacitor	C1, C3, C5, C7	1000pF 0603	Murata	GRM39C0G102J50V
Capacitor	C2, C4, C6, C8	0.1 uF 0603	Murata	GRM39Y5V104Z25V

CONNECTION DIAGRAM AND PIN DESCRIPTIONS


Pin #	Name	Description
2	RF IN	RF input (Internally DC blocked)
1,6	Vd	MMIC drain bias
3,4	Vg	MMIC gate bias
5	RF OUT	RF output (Internally DC blocked)

PHYSICAL DIMENSIONS (Unit: inches)
