TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7W14F, TC7W14FU, TC7W14FK

# SCHMITT INVERTER

The TC7W14 is high speed C<sup>2</sup>MOS SCHMITT INVERTER fabricated with silicon gate C<sup>2</sup>MOS technology. It achieves the high speed operation similar to equivalent LSTTL while maintaining the C<sup>2</sup>MOS low power dissipation.

Pin configuration and function are the same as the TC7WU04 but the inputs have 25% V<sub>CC</sub> hysteresis and with its schmitt trigger function, the TC7W14 can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **FEATURES**

High Speed	$t_{pd} = 11ns$ (Typ.) at
------------	---------------------------

• Low Power Dissipation ............  $I_{CC} = 1\mu A$  (Max.) at

• High Noise Immunity ...... V<sub>H</sub> = 1.1V at

V<sub>CC</sub> = 5V

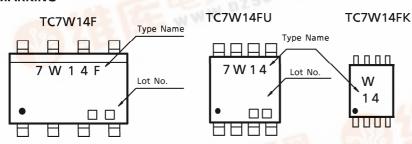
Output Drive Capability ............ 10 LSTTL Loads

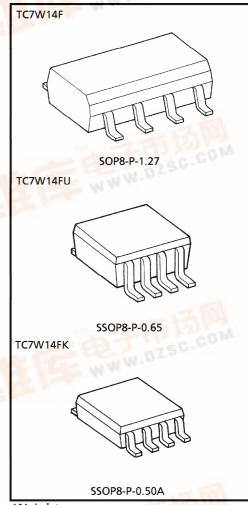
• Symmetrical Output Impedance ...  $|I_{OH}| = I_{OL} = 4mA$ 

Balanced Propagation Delays . . . . t<sub>pLH</sub>≒t<sub>pHL</sub>

Wide Operating Voltage Range ... VCC (opr) = 2~6V

#### MARKING





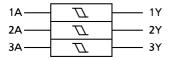
Weight SOP8-P-1.27 : 0.05g (Typ.) SSOP8-P-0.65 : 0.02g (Typ.) SSOP8-P-0.50A : 0.01g (Typ.)



#### **MAXIMUM RATINGS** (Ta = $25^{\circ}$ C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage Range	Vcc	- 0.5~7	V	
DC Input Voltage	VIN	-0.5~V <sub>CC</sub> +0.5	V	
DC Output Voltage	Vout	-0.5~V <sub>CC</sub> +0.5	V	
Input Diode Current	ΙΚ	± 20	mA	
Output Diode Current	loк	± 20	mA	
DC Output Current	IOUT	± 25	mA	
DC V <sub>CC</sub> /Ground Current	Icc	± 25	mA	
Barren Dissipation	D-	300 (FM8, SM8)	\^/	
Power Dissipation	PD	200 (US8)	mW	
Storage Temperature T <sub>stg</sub>		<b>-65∼150</b>	°C	
Lead Temperature (10s)	TL	260	°C	

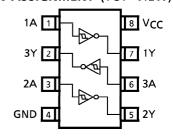
#### **LOGIC DIAGRAM**



#### TRUTH TABLE

Α	Y
L	Н
Н	L

# PIN ASSIGNMENT (TOP VIEW)



#### **RECOMMENDED OPERATING CONDITIONS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	2~6	V
Input Voltage	VIN	0~V <sub>CC</sub>	٧
Output Voltage	Vout	0~V <sub>CC</sub>	٧
Operating Temperature	T <sub>opr</sub>	<b>- 40∼85</b>	°C

# DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	CVMPOL	TEST CONDITION .			SYMBOL TEST CONDITION Ta = 25°C			C	Ta = $-40 \sim 85^{\circ}$ C			
CHARACTERISTIC	3 TIVIBUL	TEST CONDITION		Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT		
Positive Threshold				2.0	1.0	1.25	1.5	1.0	1.5			
Voltage	V <sub>P</sub>		_	4.5	2.3	2.7	3.15	2.3	3.15	V		
Voltage				6.0	3.0	3.5	4.2	3.0	4.2			
Nogotivo				2.0	0.3	0.65	0.9	0.3	0.9			
Negative	$V_N$		_	4.5	1.13	1.6	2.0	1.13	2.0	V		
Threshold Voltage				6.0	1.5	2.3	2.6	1.5	2.6			
			2.0	0.3	0.6	1.0	0.3	1.0				
Hysteresis Voltage	∣ ∨ <sub>H</sub>		_	4.5	0.6	1.1	1.4	0.6	1.4	V		
				6.0	0.8	1.2	1.7	0.8	1.7			
		V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -20μA	2.0	1.9	2.0	_	1.9	_			
lug to the site				4.5	4.4	4.5	—	4.4	_			
High-Level	V <sub>OH</sub>			6.0	5.9	6.0	—	5.9	<b> </b>	V		
Output Voltage			I <sub>OH</sub> = -4mA	4.5	4.18	4.31	<b>—</b>	4.13	_			
			$I_{OH} = -5.2 \text{mA}$	6.0	5.68	5.80	—	5.63	<b>—</b>			
				2.0	_	0.0	0.1	_	0.1			
l			$I_{OL} = 20 \mu A$	4.5	l —	0.0	0.1	_	0.1			
Low-Level	V <sub>OL</sub>	$V_{IN} = V_{IH}$		6.0	l —	0.0	0.1	_	0.1	V		
Output Voltage	"-		I <sub>OL</sub> = 4mA	4.5	_	0.17	0.26	_	0.33			
			$I_{OL} = 5.2 \text{mA}$	6.0	l —	0.18	0.26	_	0.33			
Input Leakage Current	IN	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_		± 0.1		± 1.0			
Quiescent Supply Current	lcc	V <sub>IN</sub> = V <sub>CC</sub> o	or GND	6.0	_	_	1.0	_	10.0	μΑ		

3 2001-05-31

# AC ELECTRICAL CHARACTERISTICS (C<sub>L</sub> = 15pF, V<sub>CC</sub> = 5V, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Т	UNIT		
CHARACTERISTIC	STIVIBOL	TEST CONDITION	MIN.	TYP.	MAX.	ONIT
Output Transition	tTLH			1	8	ne
Time	tTHL	_	_	4	0	ns
Propagation Delay	t <sub>pLH</sub>			11	21	ne
Time	t <sub>pHL</sub>	_	_	''		ns

#### AC ELECTRICAL CHARACTERISTICS ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

CHARACTERISTIC SYMBO		TEST CONDITION		Т	a = 25°	,C	Ta = -4	UNIT	
CHARACTERISTIC	3 I WIBOL	TEST CONDITION		MIN.	TYP.	MAX.	MIN.	MAX.	CIVII
Output Transition	t		2.0	_	30	75	_	95	
Time	t <sub>TLH</sub>	_	4.5	—	8	15	_	19	ns
Time t <sub>THL</sub>		6.0	<b> </b>	7	13	_	16		
Duana nation Dalou	<b>+</b>		2.0	_	42	125	_	155	
Propagation Delay Time	t <sub>pLH</sub>	_	4.5	l —	14	25	_	31	ns
Time t <sub>pHL</sub>		6.0	—	12	21	_	26		
Input Capacitance	CIN	_		_	5	10	_	10	
Power Dissipation Capacitance	C <sub>PD</sub>	(Note 1)		_	28	_	_	_	pF

(Note 1): CpD is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to Test Circuit).

Average operating current can be obtained by the equation hereunder.  $I_{CC}(opr) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 2$  (per gate)

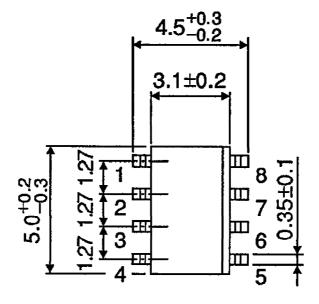
4

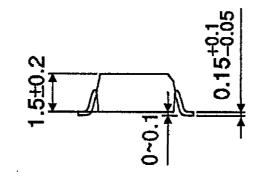
2001-05-31

# **PACKAGE DIMENSIONS**

SOP8-P-1.27

Unit: mm



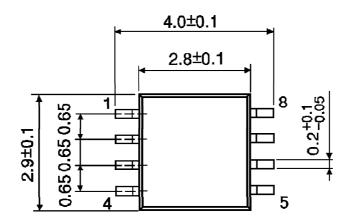


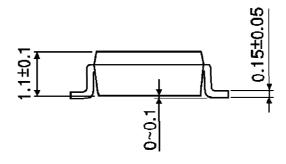
Weight: 0.05g (Typ.)

5 2001-05-31

#### PACKAGE DIMENSIONS SSOP8-P-0.65

Unit: mm



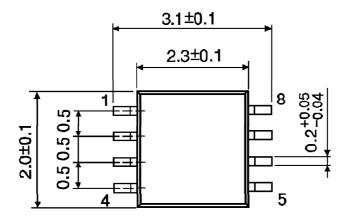


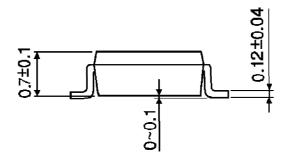
Weight: 0.02g (Typ.)

6 2001-05-31

#### PACKAGE DIMENSIONS SSOP8-P-0.50A

Unit: mm





Weight: 0.01g (Typ.)

2001-05-31

#### **RESTRICTIONS ON PRODUCT USE**

000707EBA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- ◆ The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

8 2001-05-31