TC7WG08FU/FK

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG08FU,TC7WG08FK

Dual 2-Input AND Gate

Features

• High-level output current: $I_{OH}/I_{OL} = \pm 8 \text{ mA (min)}$ at $V_{CC} = 3 \text{ V}$

• High-speed operation: $t_{pd} = 2.5 \text{ ns (typ.)}$

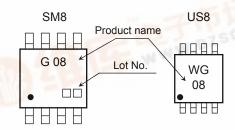
at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

Operating voltage range: V_{CC} = 0.9~3.6 V

• 5.5-V tolerant inputs

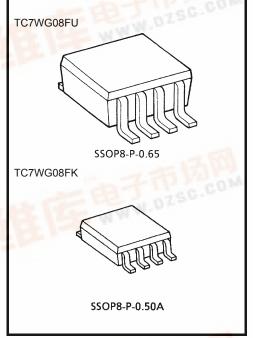
• 3.6-V power down protection outputs

Marking



Absolute Maximum Ratings (Ta = 25°C)

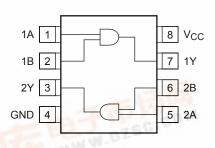
Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	VIN	-0.5~7.0	V
DC autout voltage	V	-0.5~4.6 (Note 1)	V
DC output voltage	Vout	-0.5~V _{CC} + 0.5 (Note 2)	
Input diode current	lικ	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±25	mA
DC V _{CC} / ground current	Icc	±50	mA
Power dissipation	PD	300 (SM8) 200 (US8)	mW
Storage temperature	T _{stg}	-65~150	°C



Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Pin Assignment (top view)



Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

 $V_{CC} = 0 V$

High or Low State. IOUT absolute maximum rating must be observed.

Vout < GND

2007-11-01

IEC Logic Symbol



Truth Table

Inp	Outputs			
Α	В	Y		
L	L	L		
L	Н	L		
Н	L	L		
Н	Н	Н		

Operating Ranges

Characteristics	Symbol	Value	Unit	
Power supply voltage	V_{CC}	0.9~3.6	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V	0~3.6 (Note 4)	V	
	V _{OUT}	0~V _{CC} (Note 5)	V	
Output Current		±8.0 (Note 6)		
	I _{OH} /I _{OL}	±4.0 (Note 7)		
		±3.0 (Note 8)	mA	
		±1.7 (Note 9)	MA	
		±0.3 (Note 10)		
		±0.02 (Note 11)		
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dV	0~10 (Note 12)	ns/V	

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 9: V_{CC} = 1.4~1.6 V

Note 10: V_{CC} = 1.1~1.3 V

Note 11: $V_{CC} = 0.9 \text{ V}$

Note 12: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

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Electrical Characteristics

DC Characteristics

Characteristics Sy		Symbol	Symbol Test Condition			Ta = 25°C		Ta = -40~85°C		Unit	
		Symbol	1650	V _{CC}		Min	Тур.	Max	Min	Max	Offic
High level					0.9	V _C C	_		V_{CC}		
					1.1~1.3	V _{CC} × 0.7	_	_	V _{CC} × 0.7		
	V _{IH}	_		1.4~1.6	V _{CC} × 0.65	_	_	V _{CC} × 0.65			
					1.65~ 1.95	V _{CC} × 0.65	_	_	V _{CC} × 0.65		
					2.3~2.7	1.7	_	_	1.7	_	
Input voltage					3.0~3.6	2.0	_	_	2.0		V
iliput voltage					0.9	_	_	GND	_	GND	V
					1.1~1.3	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	
	Low level	V _{IL}		_	1.4~1.6	_	_	V _{CC} × 0.35	_	V _{CC} × 0.35	
		VIL.			1.65~ 1.95	_	_	V _{CC} × 0.35		V _{CC} × 0.35	
					2.3~2.7	_	_	0.7	_	0.7	
					3.0~3.6	_	_	0.8	_	0.8	
				I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	_	
			$V_{IN} = V_{IH}$	I _{OH} = -0.3 mA	1.1~1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75		
	High level	V _{OH}		I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75		
	- ng	-011		I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45		
				I _{OH} = -4.0 mA	2.3~2.7	2.0	_	_	2.0	_	V
0				$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48	_	_	2.48	_	
Output voltage			V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
Low la				I _{OL} = 0.3 mA	1.1~1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
	Low level	Low level V _{OL}		I _{OL} = 1.7 mA	1.4~1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
				I _{OL} = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	-
				I _{OL} = 4.0 mA	2.3~2.7	_	_	0.4	_	0.4	
				I _{OL} = 8.0 mA	3.0~3.6	_	_	0.4	_	0.4	
Input leakage cu	rrent	I _{IN}	V _{IN} = 0~5.5 V		0~3.6		_	±0.1	_	±1.0	μА
Power off leakage current I _{OF}		loff	V _{IN} = 0~5. V _{OUT} = 0~	V _{IN} = 0~5.5 V V _{OUT} = 0~3.6 V		_	_	1.0	_	10.0	μА
Quiescent supply current I _C		Icc	$V_{IN} = V_{CC}$	or GND	3.6	_	_	1.0	_	10.0	μΑ

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AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
		C _L = 10 pF,	0.9	_	26.9	_	_	_	
			1.1~1.3	_	10.9	20.7	1.0	38.6	
			1.4~1.6	_	5.9	9.6	1.0	11.3	
		$R_L = 1 M\Omega$	1.65~ 1.95	_	4.5	7.0	1.0	7.5	
			2.3~2.7	_	2.9	4.4	1.0	4.9	
			3.0~3.6	_	2.2	3.5	1.0	4.1	
		C_L = 15 pF, R_L = 1 M Ω	0.9	_	30.0	_	_	_	ns
	^t pLH ^t pHL		1.1~1.3	_	12.0	24.2	1.0	42.0	
Propagation delay time			1.4~1.6	_	6.5	10.5	1.0	12.6	
Propagation delay time			1.65~ 1.95	_	5.0	7.7	1.0	8.0	
			2.3~2.7	_	3.2	4.9	1.0	5.6	
			3.0~3.6	_	2.5	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	45.0	_	_	_	
			1.1~1.3	_	18.0	33.4	1.0	63.2	
			1.4~1.6	_	8.9	14.8	1.0	17.9	
			1.65~ 1.95	_	6.9	10.3	1.0	10.8	
			2.3~2.7	_	4.4	6.4	1.0	6.8	
			3.0~3.6		3.5	4.9	1.0	5.4	
Input capacitance	C _{IN}	— 3.6		_	3	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note13)	0.9 ~ 3.6	_	10	_	_		pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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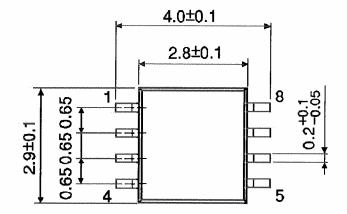
Average operating current can be obtained by the equation:

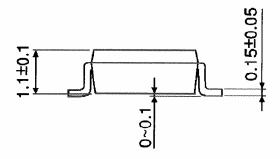
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Package Dimensions

SSOP8-P-0.65





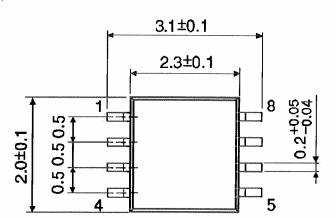


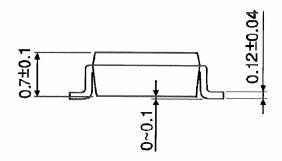
Weight: 0.02 g (typ.)

Unit: mm

Package Dimensions

SSOP8-P-0.50A





Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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