

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG32FU,TC7WG32FK

Dual 2-Input OR Gate

Features

 High-level output current: I_{OH}/I_{OL} = ±8 mA (min) at V_{CC} = 3 V

• High-speed operation: t_{pd} = 2.8 ns (typ.)

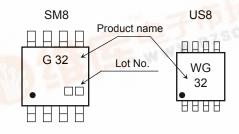
at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

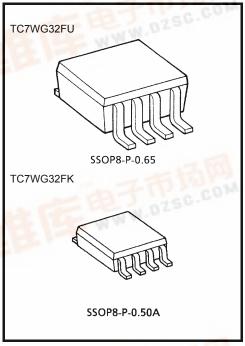
Operating voltage range: V_{CC} = 0.9~3.6 V

5.5-V tolerant inputs

• 3.6-V power down protection outputs

Marking





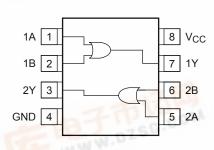
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit	
Power supply voltage	Vcc	-0.5~4.6	V	
DC input voltage	VIN	-0.5~7.0	V	
DC autout voltage	\/	-0.5~4.6 (Note 1)	V	
DC output voltage	Vout	-0.5~V _{CC} + 0.5 (Note 2)	V	
Input diode current	lικ	-20	mA	
Output diode current	lok	-20 (Note 3)	mA	
DC output current	lout	±25	mA	
DC V _{CC} / ground current	Icc	±50	mA	
Power dissipation	PD	300 (SM8) 200 (US8)	mW	
Storage temperature	T _{stg}	-65~150	°C	

Pin Assignment (top view)



Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

 $V_{CC} = 0 V$

High or Low State. IOUT absolute maximum rating must be observed.

VOUT < GND

2007-11-01

IEC Logic Symbol



Truth Table

Inp	Outputs			
Α	В	Y		
L	L	L		
L	Н	Н		
Н	L	Н		
Н	Н	Н		

Operating Ranges

Characteristics	Symbol	Value	Unit	
Power supply voltage	V_{CC}	0.9~3.6	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V	0~3.6 (Note 4)	V	
Output voltage	V _{OUT}	0~V _{CC} (Note 5)	V	
Output Current		±8.0 (Note 6)		
	I _{OH} /I _{OL}	±4.0 (Note 7)		
		±3.0 (Note 8)	mA	
		±1.7 (Note 9)	MA	
		±0.3 (Note 10)		
		±0.02 (Note 11)		
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dV	0~10 (Note 12)	ns/V	

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 9: V_{CC} = 1.4~1.6 V

Note 10: $V_{CC} = 1.1 \sim 1.3 \text{ V}$

Note 11: $V_{CC} = 0.9 \text{ V}$

Note 12: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

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Electrical Characteristics

DC Characteristics

Characteristics S		Symbol	Symbol Test Condition			Ta = 25°C		Ta = -40~85°C		Unit	
		Symbol	1650	V _{CC}		Min	Тур.	Max	Min	Max	Unit
Hiah leve					0.9	V _C C		_	V _C C		
					1.1~1.3	V _{CC} × 0.7	l		V _{CC} × 0.7	l	-
	High level	V _{IH}			1.4~1.6	V _{CC} × 0.65			V _{CC} × 0.65	ı	
					1.65~ 1.95	V _{CC} × 0.65			V _{CC} × 0.65	ı	
					2.3~2.7	1.7		_	1.7		
Input voltage					3.0~3.6	2.0		_	2.0	_	V
input voltage					0.9	_	_	GND	_	GND	v
					1.1~1.3	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	
	Low level	VIL		_	1.4~1.6	_	_	V _{CC} × 0.35	_	V _{CC} × 0.35	
		.=			1.65~ 1.95	_	_	V _{CC} × 0.35	_	V _{CC} × 0.35	
					2.3~2.7	_		0.7	_	0.7	-
					3.0~3.6	_	_	0.8	_	0.8	
				I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	_	
			V _{IN} = V _{IH}	I _{OH} = -0.3 mA	1.1~1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
	High level	V _{OH}		I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
			or V _{IL}	I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45	_	
				$I_{OH} = -4.0 \text{ mA}$	2.3~2.7	2.0	_	_	2.0	_	
Outrout valtage				$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48	_	_	2.48	_	
Output voltage				$I_{OL} = 0.02 \text{ mA}$	0.9	_		0.1	_	0.1	V
				I _{OL} = 0.3 mA	1.1~1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
	Low level	Low level V _{OL}	$V_{IN} = V_{IL}$	I _{OL} = 1.7 mA	1.4~1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
			TIN TIE	I _{OL} = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	
				I _{OL} = 4.0 mA	2.3~2.7	_		0.4	_	0.4	
				I _{OL} = 8.0 mA	3.0~3.6	_	_	0.4	_	0.4	
Input leakage cui	rrent	I _{IN}	V _{IN} = 0~5.5 V		0~3.6			±0.1	_	±1.0	μА
Power off leakag	e current	I _{OFF}	V _{IN} = 0~5. V _{OUT} = 0~	V _{IN} = 0~5.5 V V _{OUT} = 0~3.6 V		_	_	1.0	_	10.0	μА
Quiescent supply	/ current	Icc	$V_{IN} = V_{CC}$	V _{IN} = V _{CC} or GND		_		1.0	_	10.0	μА

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AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
			0.9	_	19.8	_	_	_	
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.1~1.3	_	10.1	18.7	1.0	34.5	
			1.4~1.6	_	5.9	8.9	1.0	10.8	
			1.65~ 1.95	_	4.5	6.4	1.0	6.9	
			2.3~2.7	_	3.1	4.2	1.0	4.7	
			3.0~3.6	_	2.3	3.4	1.0	4.0	
	^t pLH ^t pHL	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	22.5	_	_	_	ns
			1.1~1.3	_	11.6	21.5	1.0	37.2	
Propagation delay time			1.4~1.6	_	6.6	9.8	1.0	12.0	
			1.65~ 1.95	_	5.0	7.1	1.0	7.3	
			2.3~2.7	_	3.5	4.5	1.0	5.1	
			3.0~3.6	_	2.8	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	30.0	_	_	_	
			1.1~1.3	_	15.0	29.6	1.0	56.0	
			1.4~1.6	_	8.5	13.1	1.0	15.9	
			1.65~ 1.95	_	6.3	9.2	1.0	9.6	
			2.3~2.7	_	4.3	5.7	1.0	6.1	
			3.0~3.6		3.5	4.4	1.0	4.8	
Input capacitance	C _{IN}	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note13)	0.9 ~ 3.6	_	11	_			pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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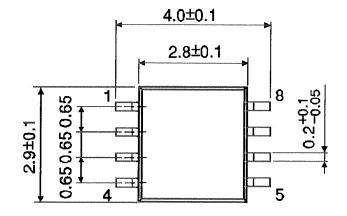
Average operating current can be obtained by the equation:

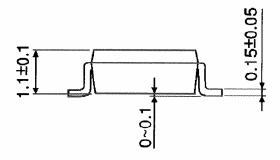
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Package Dimensions

SSOP8-P-0.65





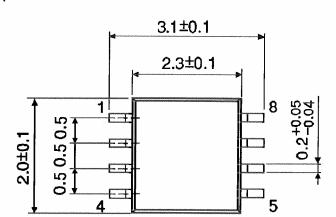


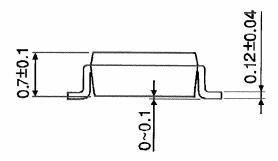
Weight: 0.02 g (typ.)

Unit: mm

Package Dimensions

SSOP8-P-0.50A





Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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