



TDA1910

10W AUDIO AMPLIFIER WITH MUTING

DESCRIPTION

The TDA 1910 is a monolithic integrated circuit in MULTIWATT® package, intended for use in Hi-Fi audio power applications, as high quality TV sets.

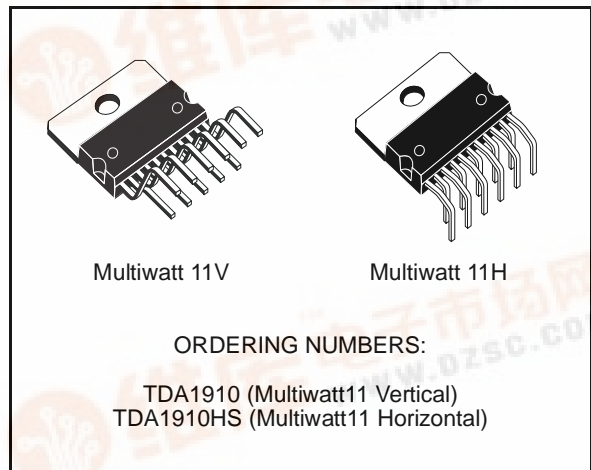
The TDA 1910 meets the DIN 45500 ($d = 0.5\%$) guaranteed output power of 10W when used at 24V/4W. At 24V/8W the output power is 7W min.

Features:

- muting facility
- protection against chip over temperature
- very low noise
- high supply voltage rejection
- low "switch-on" noise.

The TDA 1910 is assembled in MULTIWATT® package that offers:

- easy assembly
- simple heatsink

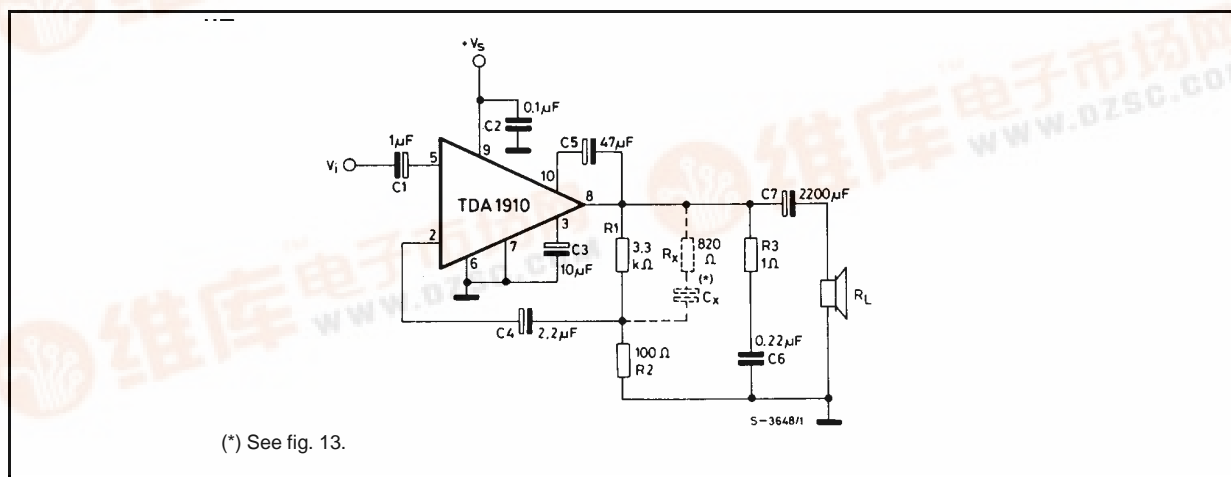


- space and cost saving
- high reliability

ABSOLUTE MAXIMUM RATINGS

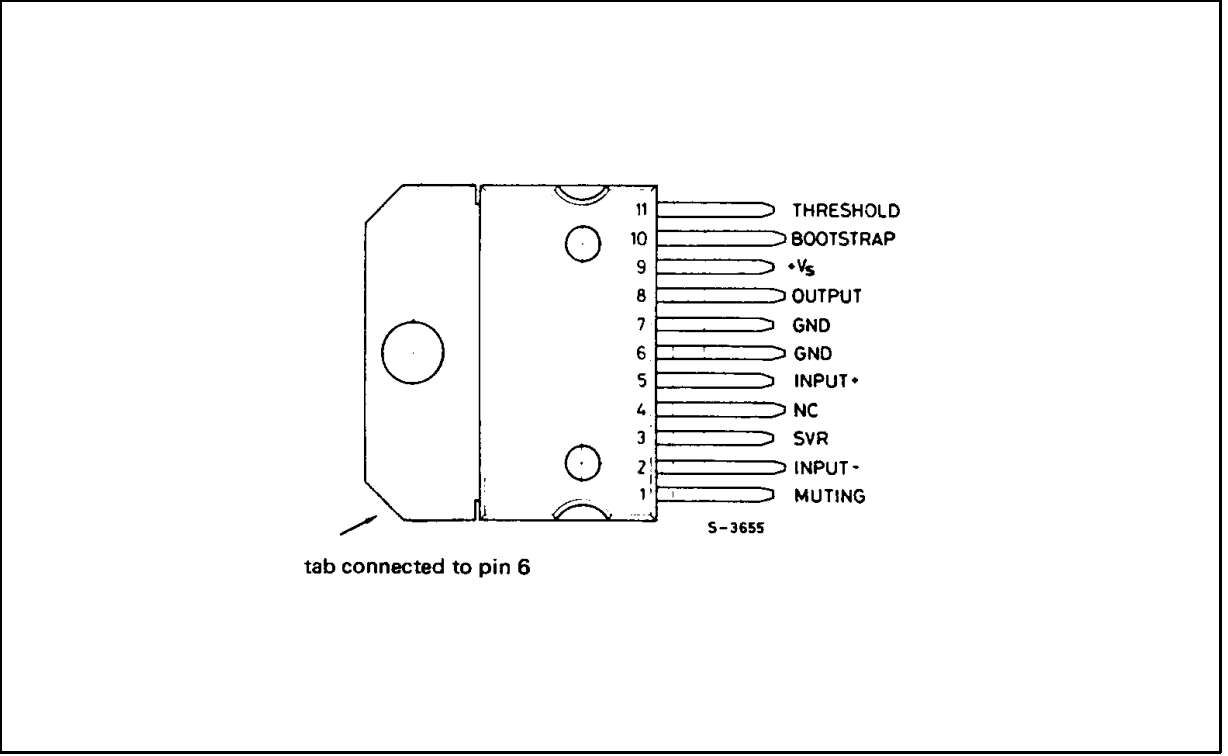
Symbol	Parameter	Value	Unit
V_s	Supply voltage	30	V
I_o	Output peak current (non repetitive)	3.5	A
I_o	Output peak current (repetitive)	3.0	A
V_i	Input voltage	0 to $+V_s$	V
V_i	Differential input voltage	± 7	V
V_{11}	Muting threshold voltage	V_s	V
P_{tot}	Power dissipation at $T_{case} = 90^\circ\text{C}$	20	W
T_{stg}, T_j	Storage and junction temperature	-40 to 150	$^\circ\text{C}$

TEST CIRCUIT

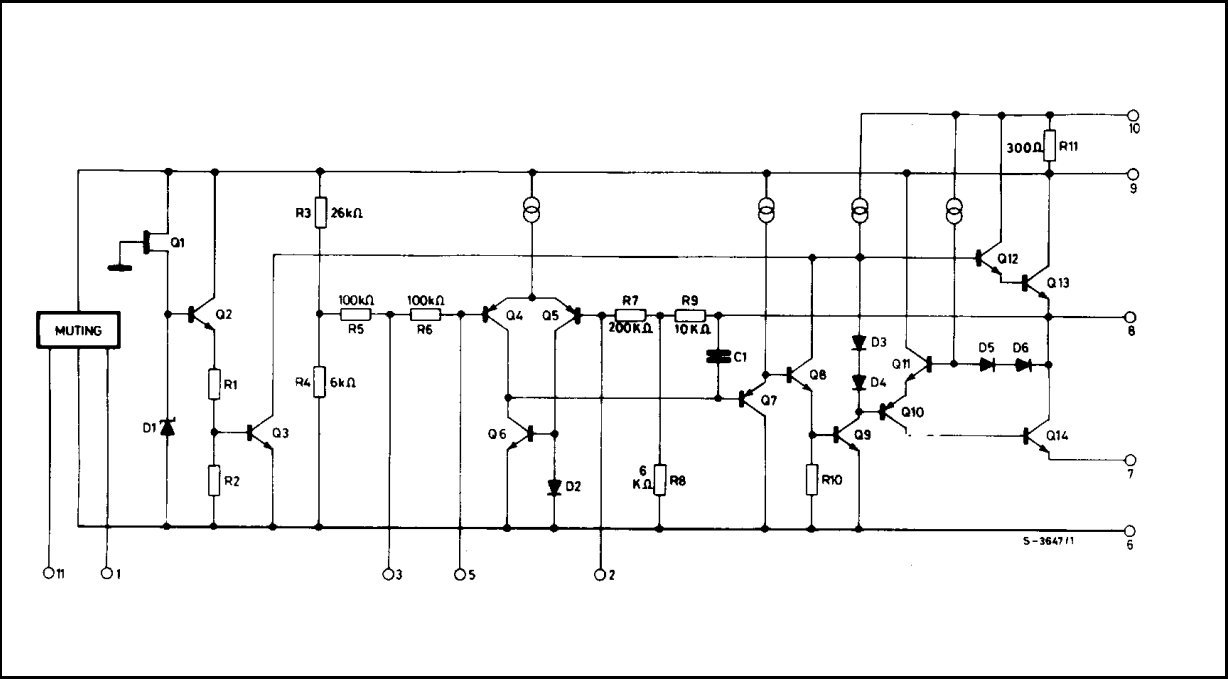


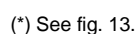
TDA1910

PIN CONNECTION (Top view)



SCHEMATIC DIAGRAM





TDA1910

THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{th\ j-case}$	Thermal resistance junction-case max	3	°C/W

ELECTRICAL CHARACTERISTICS (Refer to the test circuit, $T_{amb} = 25\text{ °C}$, R_{th} (heatsink) = 4 °C/W , unless otherwise specified)

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
V_s	Supply voltage		8		30	V
V_o	Quiescent output voltage	$V_s = 18V$ $V_s = 24V$	8.3 11.5	9.2 12.4	10 13.4	V
I_d	Quiescent drain current	$V_s = 18V$ $V_s = 24V$		19 21	32 35	mA
$V_{CE\ sat}$	Output stage saturation voltage	$I_C = 2A$		1		V
		$I_C = 3A$		1.6		
P_o	Output power	$d = 0.5\%$ $f = 40\text{ to }15,000\text{Hz}$ $V_s = 18V$ $R_L = 4\Omega$ $V_s = 24V$ $R_L = 4\Omega$ $V_s = 24V$ $R_L = 8\Omega$	6.5 10 7	7 12 7.5		W
		$d = 10\%$ $f = 1\text{ KHz}$ $V_s = 18V$ $R_L = 4\Omega$ $V_s = 24V$ $R_L = 4\Omega$ $V_s = 24V$ $R_L = 8\Omega$	8.5 15 9	9.5 17 10		
d	Harmonic distortion	$f = 40\text{ to }15,000\text{ Hz}$ $V_s = 18V$ $R_L = 4\Omega$ $P_o = 50\text{ mW to }6.5W$		0.2	0.5	%
		$V_s = 24V$ $R_L = 4\Omega$ $P_o = 50\text{ mW to }10W$		0.2	0.5	
		$V_s = 24V$ $R_L = 8\Omega$ $P_o = 50\text{ mW to }7W$		0.2	0.5	
d	Intermodulation distortion	$V_s = 24V$ $R_L = 4\Omega$ $P_o = 10W$ $f_1 = 250\text{ Hz}$ $f_2 = 8\text{ KHz}$ (DIN 45500)		0.2		%
V_i	Input sensitivity	$F = 1\text{ KHz}$, $V_s = 18V$ $R_L = 4\Omega$ $P_o = 7\text{ W}$ $V_s = 24V$ $R_L = 4\Omega$ $P_o = 12\text{ W}$ $V_s = 24V$ $R_L = 8\Omega$ $P_o = 7.5W$		170 220 245		mV
V_i	Input saturation voltage (rms)	$V_s = 18V$ $V_s = 24V$	1.8 2.4			V
R_i	Input resistance (pin 5)	$f = 1\text{ KHz}$	60	100		K Ω
I_d	Drain current	$V_s = 24V$ $f = 1\text{ KHz}$ $R_L = 4\Omega$ $P_o = 12W$ $R_L = 8\Omega$ $P_o = 7.5W$		820 475		mA

ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
h	Efficiency	$V_s = 24V$ $f = 1 \text{ KHz}$ $R_L = 4\Omega$ $P_o = 12W$ $R_L = 8\Omega$ $P_o = 7.5W$		62 65		%
BW	Small signal bandwidth	$V_s = 24V$ $R_L = 4\Omega$ $P_o = 1W$	10 to 120,000			Hz
BW	Power bandwidth	$V_s = 24V$ $R_L = 4\Omega$ $P_o = 12W$ $d \leq 5\%$	40 to 15,000			Hz
G_v	Voltage gain (open loop)	$f = 1 \text{ KHz}$		75		dB
G_v	Voltage gain (closed loop)	$V_s = 24V$ $R_L = 4\Omega$ $f = 1 \text{ KHz}$ $P_o = 1W$	29.5	30	30.5	dB
e_N	Total input noise	$R_g = 50\Omega$ $R_g = 1K\Omega$ $(^\circ)$ $R_g = 10K\Omega$		1.2 1.3 1.5	3.0 3.2 4.0	μV
		$R_g = 50\Omega$ $R_g = 1K\Omega$ $(^{\circ\circ})$ $R_g = 10K\Omega$		2.0 2.0 2.2	5.0 5.2 6.0	μV
S/N	Signal to noise ratio	$V_s = 24V$ $R_g = 10K\Omega$ $P_o = 12W$ $R_g = 0$ $(^\circ)$ $R_L = 4\Omega$	97	103 105		dB
		$R_g = 10K\Omega$ $(^{\circ\circ})$ $R_g = 0$	93	100 100		dB
SVR	Supply voltage rejection	$V_s = 24V$ $R_L = 4\Omega$ $f_{\text{ripple}} = 100 \text{ Hz}$ $R_g = 10 \text{ K}\Omega$	50	60		dB
T_{sd}	Thermal shut-down case (*) temperature	$P_{tot} = 8W$	110	125		$^\circ C$

MUTING FUNCTION (Refer to Muting circuit)

V_T	Muting-off threshold voltage (pin 11)		1.9		4.7	V
V_T	Muting-on threshold voltage (pin 11)		0		1.3	V
			6		V_s	
R_1	Input resistance (pin 1)	Muting off	80	200		$K\Omega$
		Muting on		10	30	Ω
R_{11}	Input resistance (pin 11)		150			$K\Omega$
A_T	Muting attenuation	$R_g + R_1 = 10 \text{ K}\Omega$	50	60		dB

Note :

($^\circ$) Weighting filter = curve A.($^{\circ\circ}$) Filter with noise bandwidth: 22 Hz to 22 KHz.

(*) See fig. 29 and fig. 30.

Figure 1. Quiescent output voltage vs. supply voltage

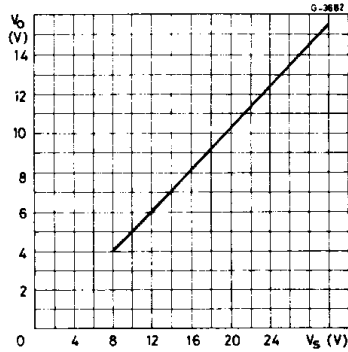


Figure 2. Quiescent drain current vs. supply voltage

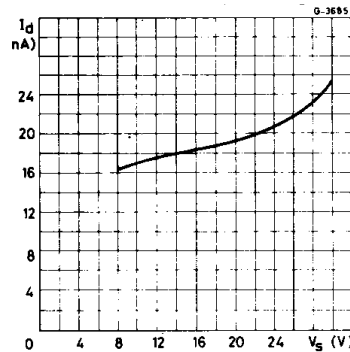


Figure 3. Open loop frequency response

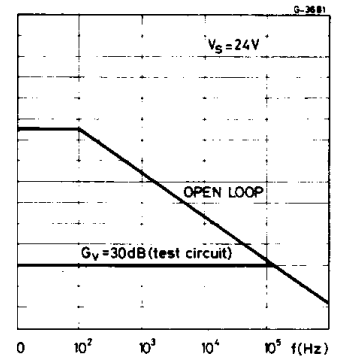


Figure 4. Output power vs. supply voltage

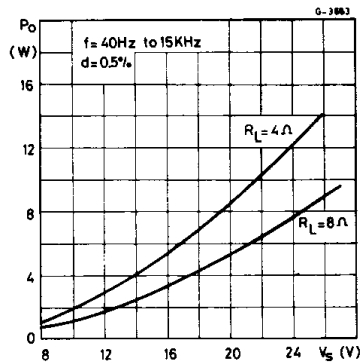


Figure 5. Output power vs. supply voltage

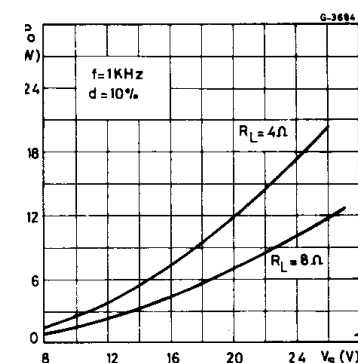


Figure 6. Distortion vs. output power

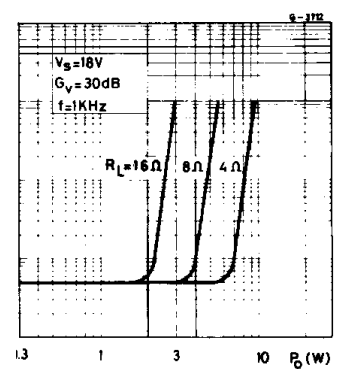


Figure 7. Distortion vs. output power

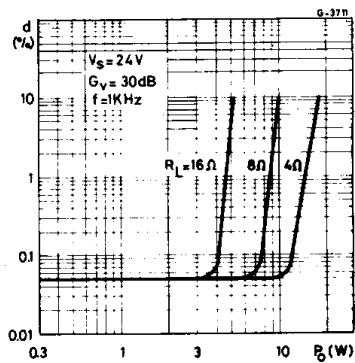


Figure 8. Output power vs. frequency

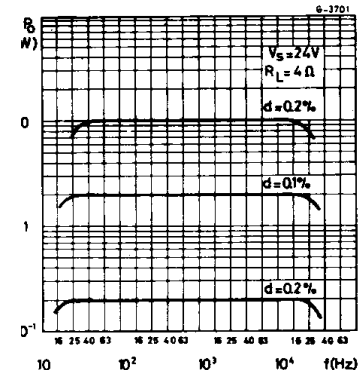


Figure 9. Output power vs. frequency

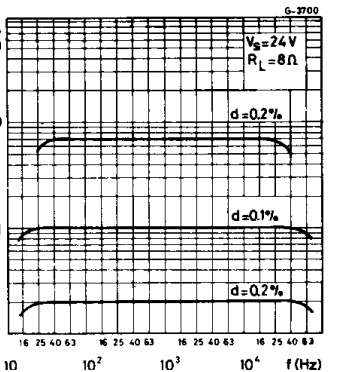


Figure 10. Output power vs. input voltage

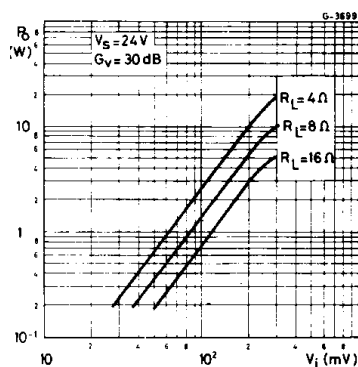


Figure 11. Output power vs. input voltage

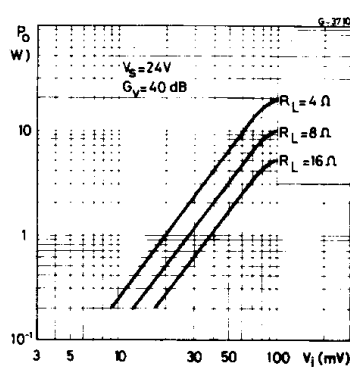


Figure 12. Total input noise vs. source resistance

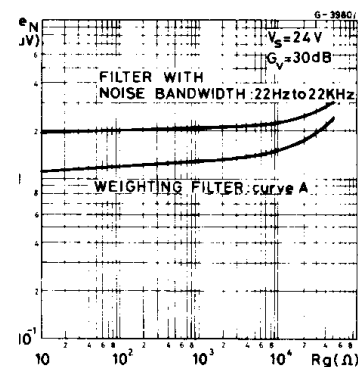
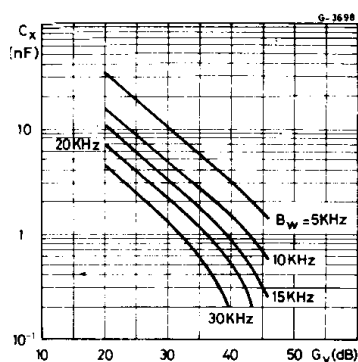
Figure 13. Values of capacitor C_X vs. bandwidth (BW) and gain (G_V)

Figure 14. Supply voltage rejection vs. voltage gain

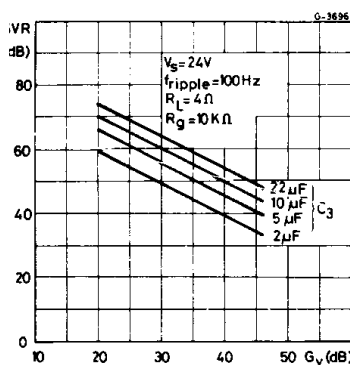


Figure 15. Supply voltage rejection vs. source resistance

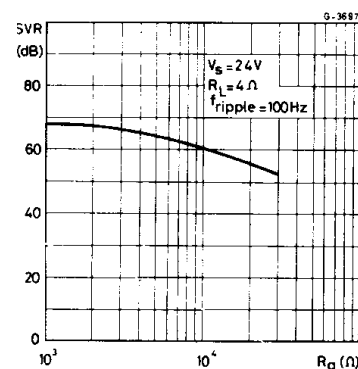


Figure 16. Power dissipation and efficiency vs. output power

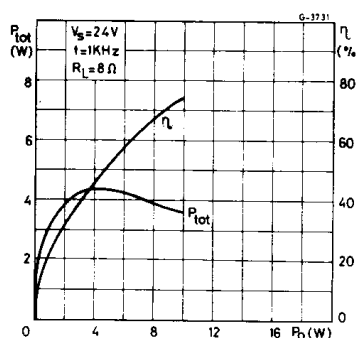


Figure 17. Power dissipation and efficiency vs. output power

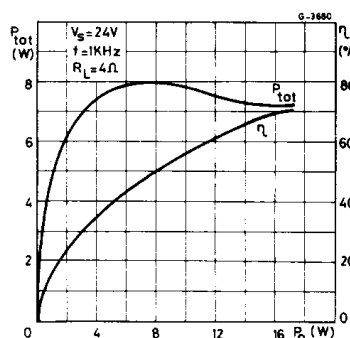
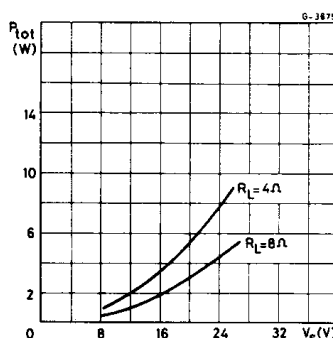


Figure 18. Max power dissipation vs. supply voltage



APPLICATION INFORMATION

Figure 19. Application circuit without muting

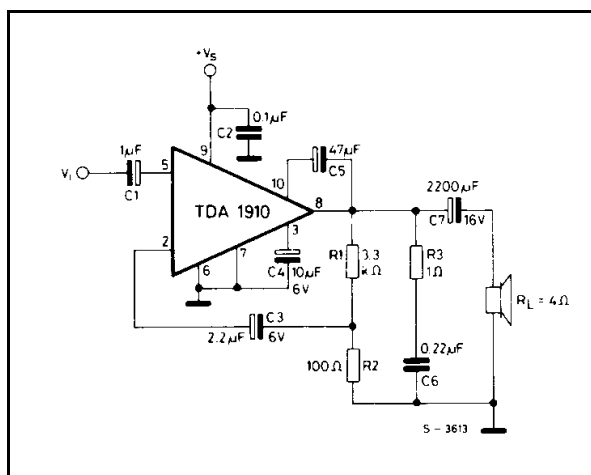


Figure 20. PC board and component lay-out of the circuit of fig. 19 (1:1 scale)

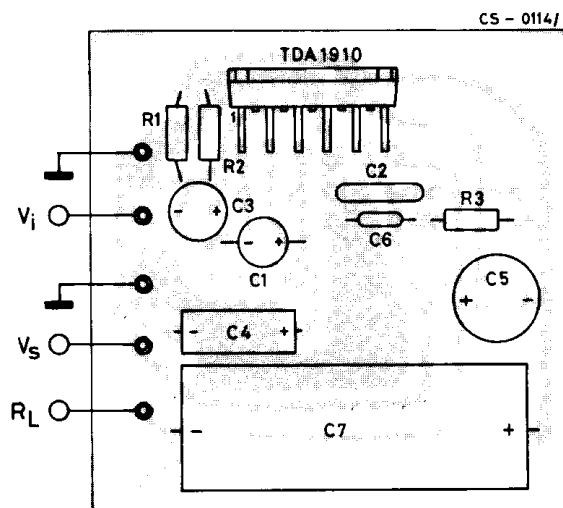
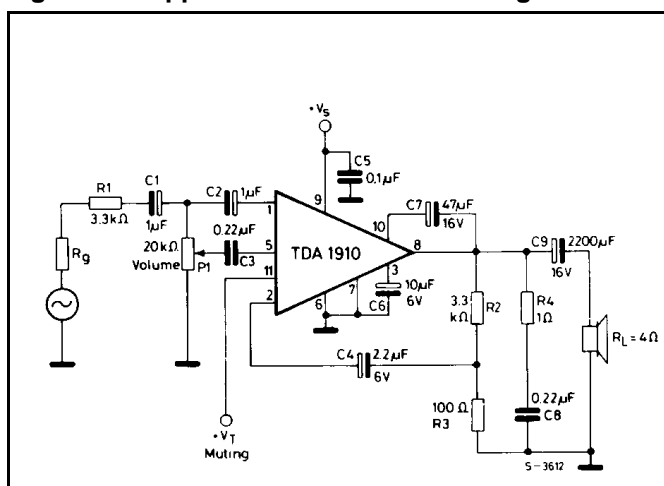


Figure 21. Application circuit with muting



Performance (circuits of fig. 19 and 21)

$P_o = 12W$ (40 to 15000 Hz, $d \leq 0.5\%$)

$V_s = 24V$

$I_d = 0.82A$

$G_v = 30 dB$

APPLICATION INFORMATION (continued)

Figure 22. Two position DC tone control (10 dB boost 50 Hz and 20 KHz) using change of pin 1 resistance (muting function)

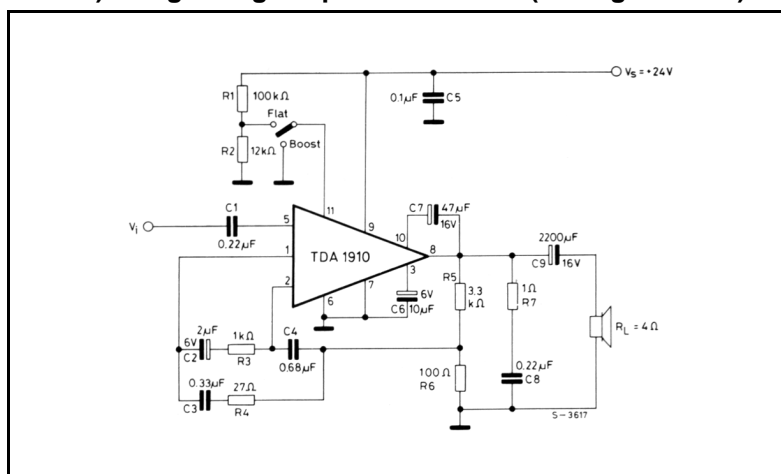


Figure 23. Frequency response of the circuit of fig. 22

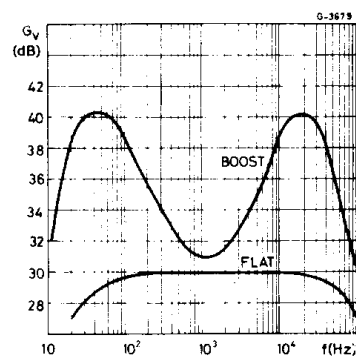


Figure 24. 10 dB 50 Hz boost tone control using change of pin 1 resistance (muting function)

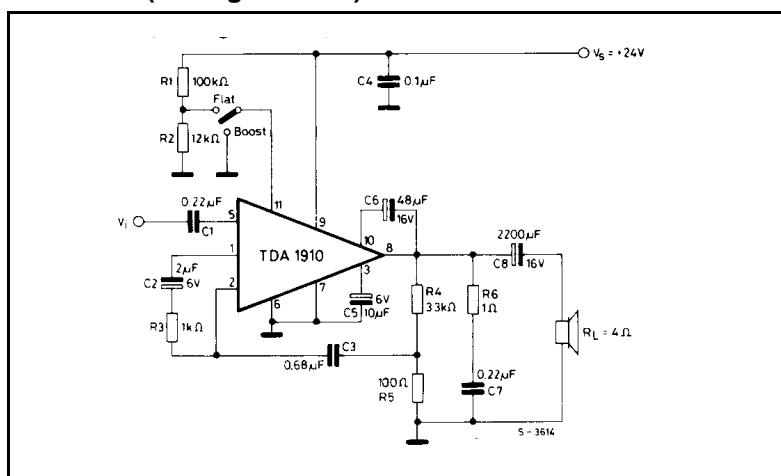


Figure 25. Frequency response of the circuit of fig. 24

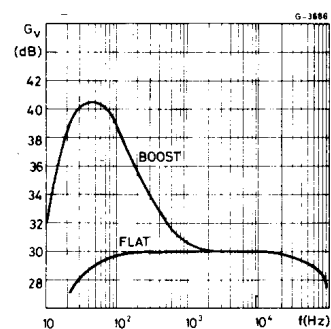


Figure 26. Squelch function in TV applications

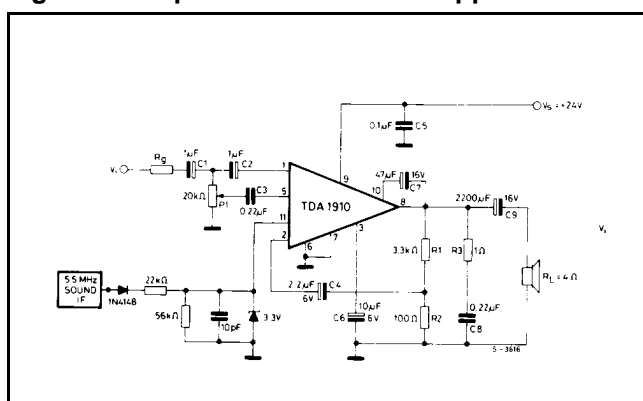
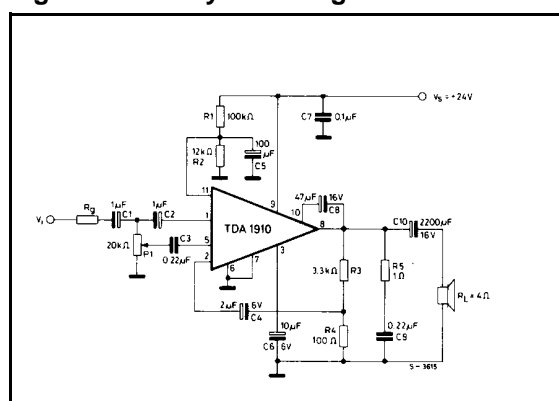


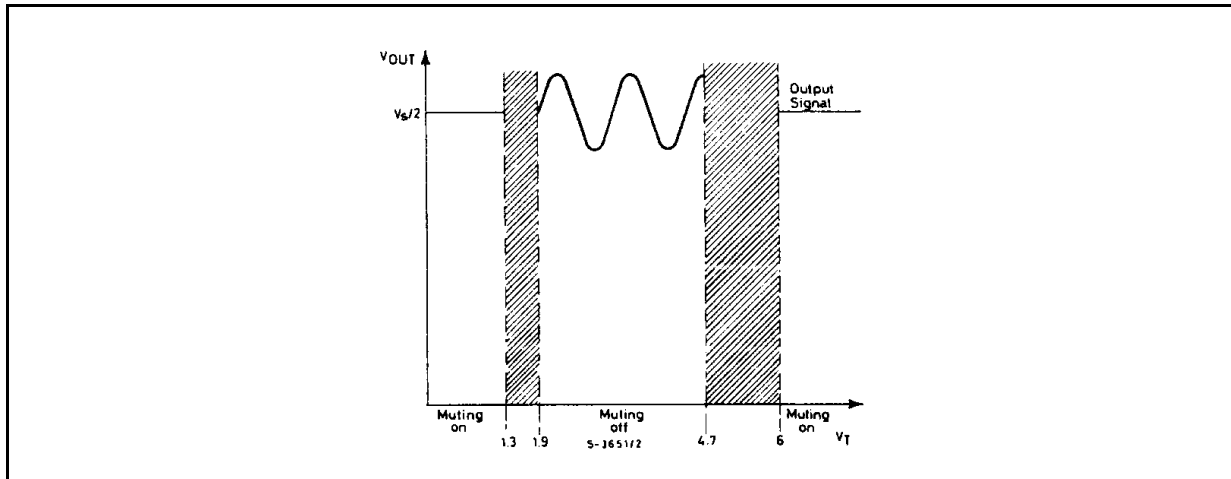
Figure 27. Delayed muting circuit



MUTING FUNCTION

The output signal can be inhibited applying a DC voltage V_T to pin 11, as shown in fig. 28

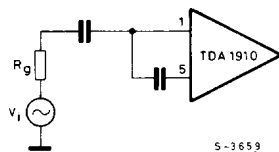
Figure 28



The input resistance at pin 1 depends on the threshold voltage V_T at pin 11 and is typically.

$R_1 = 200 \text{ K}\Omega$	@	$1.9\text{V} \leq V_T \leq 4.7\text{V}$	muting-off
$R_1 = 10 \text{ }\Omega$	@	$0\text{V} \leq V_T \leq 1.3\text{V}$	muting-on
		$6\text{V} \leq V_T \leq V_S$	muting-on

Referring to the following input stage, the possible attenuation of the input signal and therefore of the output signal can be found using the following expression.



$$A_T = \frac{V_i}{V_5} = \frac{R_g + R_5 // R_1}{R_5 // R_1}$$

where $R_5 \cong 100 \text{ K}\Omega$

Considering $R_g = 10 \text{ K}\Omega$ the attenuation in the muting-on condition is typically $A_T = 60 \text{ dB}$. In the muting-off condition, the attenuation is very low, typically 1.2 dB .

A very low current is necessary to drive the threshold voltage V_T because the input resistance at pin 11 is greater than $150 \text{ K}\Omega$. The muting function can be used in many cases, when a temporary inhibition of the output signal is requested, for example:

- in switch-on condition, to avoid preamplifier power-on transients (see fig. 27)

- during commutations at the input stages.
- during the receiver tuning.

The variable impedance capability at pin 1 can be useful in many applications and we have shown 2 examples in fig. 22 and 24, where it has been used to change the feedback network, obtaining 2 different frequency responses.

APPLICATION SUGGESTION

The recommended values of the components are those shown on application circuit of fig. 21. Different values can be used.

The following table can help the designer.

Component	Raccom. value	Purpose	Larger than recommended value	Smaller than recommended value	Allowed range	
					Min.	Max.
$R_g + R_1$	10K Ω	Input signal impeded for muting operation	Increase of the attenuation in muting-on condition. Decrease of the input sensitivity.	Decrease of the attenuation in muting on condition.		
R_2	3.3K Ω	Close loop gain setting.	Increase of gain.	Decrease of gain. Increase quiescent current.	$9 R_3$	
R_3	100 Ω	Close loop gain setting.	Decrease of gain.	Increase of gain.		$R_2/9$
R_4	1 Ω	Frequency stability	Danger of oscillation at high frequencies with inductive loads.			
P_1	20K Ω	Volume potentiometer.	Increase of the switch-on noise.	Decrease of the input impedance and of the input level.	10K Ω	100K Ω
C_1 C_2 C_3	1 μ F 1 μ F 0.22 μ F	Input DC decoupling.		Higher low frequency cutoff.		
C_4	2.2 μ F	Inverting input DC decoupling.	Increase of the switch-on noise.	Higher low frequency cutoff.	0.1 μ F	
C_5	0.1 μ F	Supply voltage bypass.		Danger of oscillations.		
C_6	10 μ F	Ripple rejection.	Increase of SVR. Increase of the switch-on time	Degradation of SVR	2.2 μ F	100 μ F
C_7	47 μ F	Bootstrap.		Increase of the distortion at low frequency.	10 μ F	100 μ F
C_8	0.22 μ F	Frequency stability.		Danger of oscillation.		
C_9	2200 μ F ($R_L = 4\Omega$) 1000 μ F ($R_L = 8\Omega$)	Output DC decoupling.		Higher low frequency cutoff.		

THERMAL SHUT-DOWN

The presence of a thermal limiting circuit offers the following advantages:

- 1) An overload on the output (even if it is permanent), or an above limit ambient temperature can be easily supported since the T_j cannot be higher than 150°C .
- 2) The heatsink can have a smaller factor of safety compared with that of a conventional

circuit. There is no possibility of device damage due to high junction temperature.

If for any reason, the junction temperature increases up to 150°C , the thermal shut-down simply reduces the power dissipation and the current consumption.

The maximum allowable power dissipation depends upon the size of the external heatsink (i.e. its thermal resistance); fig. 31 shows this dissippable power as a function of ambient temperature for different thermal resistance.

Figure 29. Output power and drain current vs. case temperature

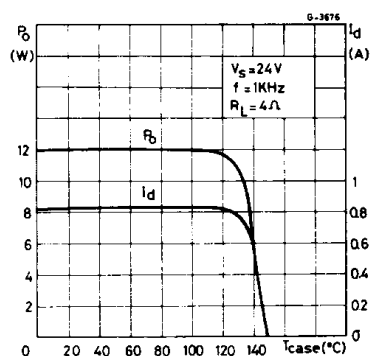


Figure 30. Output power and drain current vs. case temperature

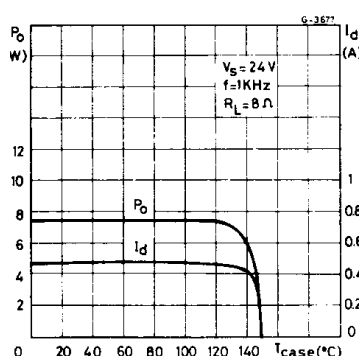
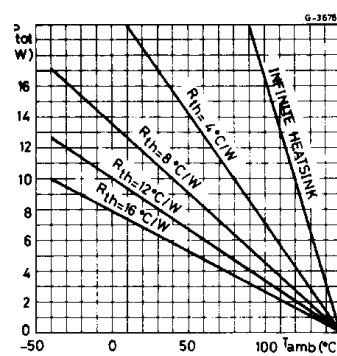


Figure 31. Maximum allowable power dissipation vs. ambient temperature



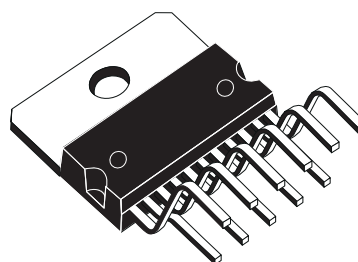
MOUNTING INSTRUCTIONS

The power dissipated in the circuit must be removed by adding an external heatsink. Thanks to the Multiwatt® package attaching the heatsink is very simple, a screw or a compression

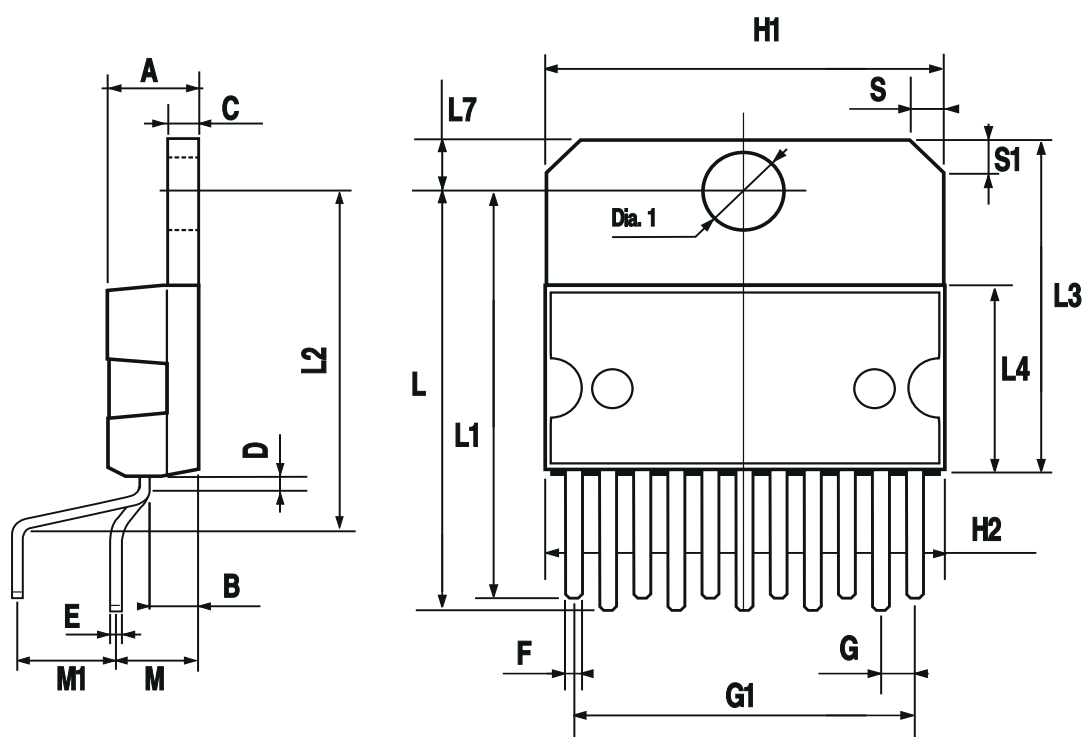
spring (clip) being sufficient. Between the heatsink and the package it is better to insert a layer of silicon grease, to optimize the thermal contact; no electrical isolation is needed between the two surfaces.

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			5			0.197
B			2.65			0.104
C			1.6			0.063
D		1			0.039	
E	0.49		0.55	0.019		0.022
F	0.88		0.95	0.035		0.037
G	1.45	1.7	1.95	0.057	0.067	0.077
G1	16.75	17	17.25	0.659	0.669	0.679
H1	19.6			0.772		
H2			20.2			0.795
L	21.9	22.2	22.5	0.862	0.874	0.886
L1	21.7	22.1	22.5	0.854	0.87	0.886
L2	17.4		18.1	0.685		0.713
L3	17.25	17.5	17.75	0.679	0.689	0.699
L4	10.3	10.7	10.9	0.406	0.421	0.429
L7	2.65		2.9	0.104		0.114
M	4.25	4.55	4.85	0.167	0.179	0.191
M1	4.73	5.08	5.43	0.186	0.200	0.214
S	1.9		2.6	0.075		0.102
S1	1.9		2.6	0.075		0.102
Dia1	3.65		3.85	0.144		0.152

OUTLINE AND MECHANICAL DATA



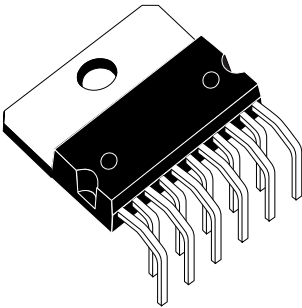
Multiwatt11 V



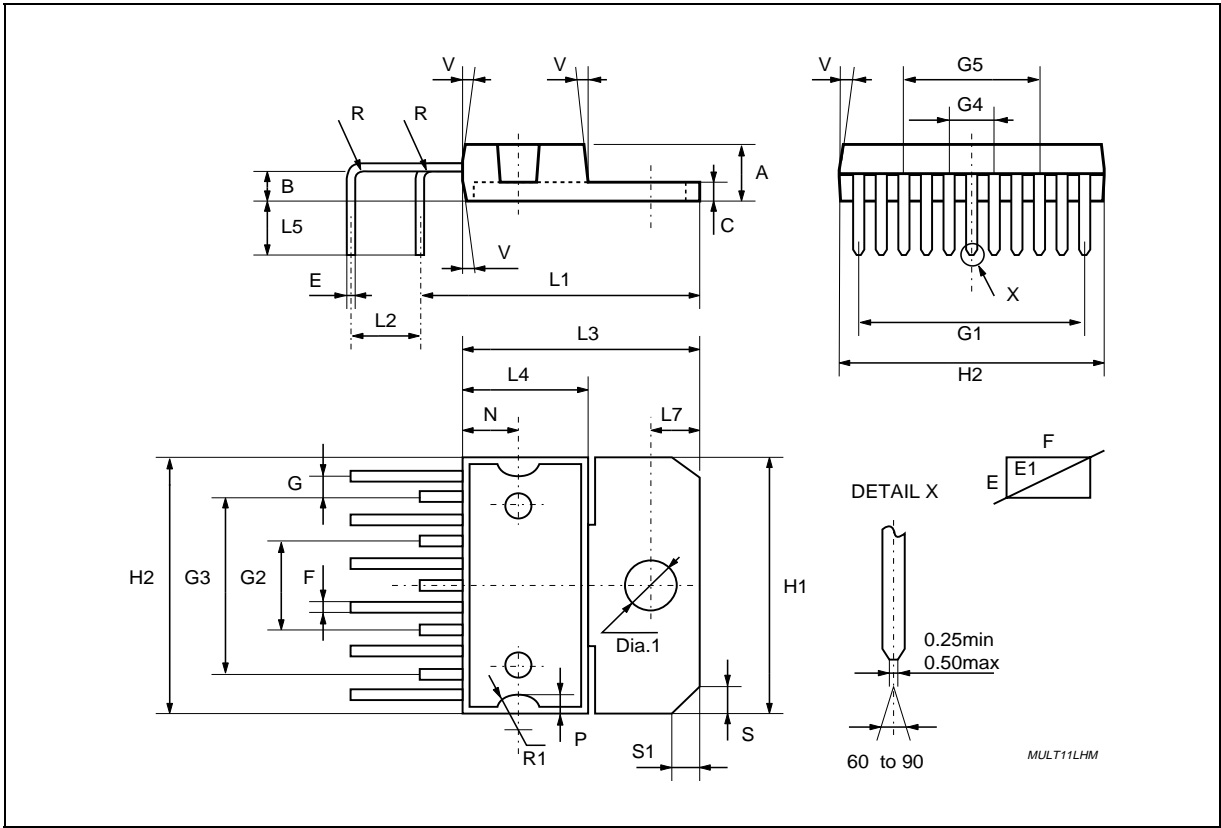
TDA1910

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.373	4.5	4.627	0.172	0.177	0.182
B			2.65			0.104
C			1.6			0.063
E	0.49	0.515	0.55	0.019	0.020	0.022
E1	1.007	1.037	1.07	0.040	0.041	0.042
F	0.88	0.9	0.95	0.035	0.035	0.037
G	1.5	1.7	1.9	0.059	0.067	0.075
G.1	16.82	17.02	17.22	0.662	0.670	0.678
G2	6.61	6.807	7.01	0.260	0.268	0.276
G3	13.41	13.61	13.81	0.528	0.536	13.810
G4	3.2	3.4	3.6	0.126	0.134	0.142
G5	10.01	10.21	10.41	0.394	0.402	0.410
H1	19.6			0.772		
H2			20.2			0.795
L1	19.28	19.58	19.88	0.759	0.771	0.783
L2	3.61	3.81	4.01	0.142	0.150	0.158
L3	17.25	17.5	17.75	0.679	0.689	0.699
L4	10.3	10.6	10.9	0.406	0.417	0.429
L5 (Inner)	3.4	3.75	4	0.134	0.148	0.157
L5 (Outer)	3.6	3.9	4.2	0.142	0.154	4.200
L7	2.65		2.9	0.104		0.114
R	0.75	1	1.25	0.030	0.039	0.049
S	1.9		2.6	0.075		0.102
S1	1.9		2.6	0.075		0.102
Dia1	3.65		3.85	0.144		0.152

OUTLINE AND
MECHANICAL DATA



Multiwatt11 H (Short leads)



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