TD62001~004APG/AFG

TOSHIBA Bipolar Digital Integrated Circuit Silicon Monolithic

## TD62001APG,TD62001AFG,TD62002APG,TD62002AFG, TD62003APG,TD62003AFG,TD62004APG,TD62004AFG

### 7-channel Darlington Sink Driver

The TD62001APG/AFG Series are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs. All units feature integral clamp diodes for switching inductive

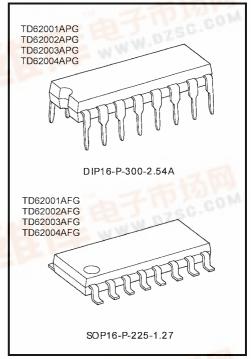
Applications include relay, hammer, lamp and display (LED) drivers.

#### **Features**

- Output current (single output): 500 mA (max)
- High sustaining voltage output: 50 V (min)
- Output clamp diodes
- Inputs compatible with various types of logic
- Package type

APG: DIP-16 pin (Pb free package) AFG: SOP-16 pin (Pb free package)

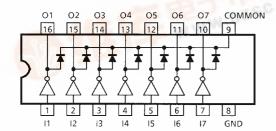
Туре	Input base resistor	Designation
TD62001APG/AFG	External	General purpose
TD62002APG/AFG	10.5-kΩ + 7-V Zenner diode	14-V to 25-V PMOS
TD62003APG/AFG	2.7 kΩ	TTL, 5-V CMOS
TD62004APG/AFG	10.5 kΩ	6-V to 15-V PMOS, CMOS



Weight

DIP16-P-300-2.54A: 1.11 g (Typ.) SOP16-P-225-1.27: 0.16 g (Typ.)

#### Pin Connection (top view)



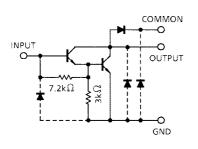


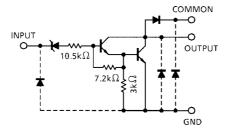
## Schematics (each driver)

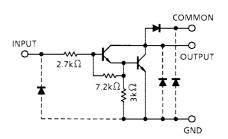
### TD62001APG/AFG

#### TD62002APG/AFG

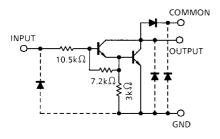
#### TD62003PAPG/AFG







### TD62004APG/AFG



Note: The input and output parasitic diodes cannot be used as clamp diodes.

### Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Output sustaining voltage	V <sub>CE (SUS)</sub>	-0.5 to 50	V	
Output current		lout	500	mA/ch
Input voltage		V <sub>IN</sub> (Note 1)	-0.5 to 30	<b>V</b>
Input current		I <sub>IN</sub> (Note 2) 25		mA
Clamp diode reverse voltage		V <sub>R</sub>	50	V
Clamp diode forward current		lF	500	mA
	APG		1.47	
Power dissipation	AFG	P <sub>D</sub>	0.625 (Note 3)	W
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Storage temperature	T <sub>stg</sub>	-55 to 150	°C	

Note 1: Except TD62001APG/AFG Note 2: Only TD62001APG/AFG

Note 3: When mounted on a glass-epoxy PCB (30 mm  $\times$  30 mm  $\times$  1.6 mm, Cu area: 50%)

# **TOSHIBA**

# Recommended Operating Conditions (Ta = -40°C to 85°C)

Characterist	ics	Symbol	Condition		Min	Тур.	Max	Unit
Output sustaining voltage		V <sub>CE (SUS)</sub>			0	_	50	V
Output current	APG	Гоит	T <sub>pw</sub> = 25 ms 7 circuits Ta = 85°C T <sub>i</sub> = 120°C	Duty = 10%	0	_	370	- mA/ch
				Duty = 50%	0	_	130	
	AFG			Duty = 10%	0	_	233	
	AFG		., .20 0	Duty = 50%	0	_	70	
Input voltage	Except TD62001APG/ AFG	V <sub>IN</sub>			0	_	24	V
Input voltage (output on)	TD62002	V <sub>IN (ON)</sub>	I <sub>OUT</sub> = 400 mA h <sub>FE</sub> = 800		14.5	_	24	V
	TD62003				2.8	_	24	
	TD62004		I		6.2	_	24	
	TD62001	VIN (OFF)			0	_	0.6	
land to alternate (autout aff)	TD62002				0	_	7.4	V
Input voltage (output off)	TD62003				0	_	0.7	V
	TD62004				0	_	1.0	
Input current	Only TD62001	I <sub>IN</sub>			0	_	10	mA
Clamp diode reverse voltage		V <sub>R</sub>			_	_	50	V
Clamp diode forward current		l <sub>F</sub>			_	_	350	mA
Power dissipation	APG	D-	Ta = 85°C		_	_	0.76	w
	AFG	P <sub>D</sub>	Ta = 85°C	(Note)	_	_	0.325	VV

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Note: When mounted on a glass-epoxy PCB (30 mm  $\times$  30 mm  $\times$  1.6 mm, Cu area: 50%)

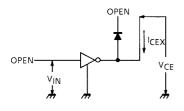
# **TOSHIBA**

# Electrical Characteristics (Ta = 25°C unless otherwise noted)

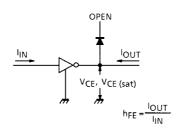
Characteristics	3	Symbol	Test Circuit	Test Condition		Min	Тур.	Max	Unit
Ooutput leakage current		la-v	1	V <sub>CE</sub> = 50 V, Ta = 25°C		_	_	50	
		I <sub>CEX</sub>		V <sub>CE</sub> = 50 V,	Ta = 85°C	_	_	100	μΑ
Collector-emitter saturation voltage			2	I <sub>OUT</sub> = 350 mA, I <sub>IN</sub> = 500 μA		_	1.3	1.6	
		V <sub>CE</sub> (sat)		I <sub>OUT</sub> = 200 mA, I <sub>IN</sub> = 350 μA		_	1.1	1.3	٧
				I <sub>OUT</sub> = 100 mA, I <sub>IN</sub> = 250 μA		_	0.9	1.1	
DC current transfer ratio		h <sub>FE</sub>	2	V <sub>CE</sub> = 2 V, I <sub>OUT</sub> = 350 mA		1000	_	_	
	TD62002		3	V <sub>IN</sub> = 20 V, I <sub>OUT</sub> = 350 mA		_	1.1	1.7	mA
Input current (output on)	TD62003	I <sub>IN (ON)</sub>		V <sub>IN</sub> = 2.4 V, I <sub>OUT</sub> = 350 mA		_	0.4	0.7	
	TD62004			V <sub>IN</sub> = 9.5 V, I <sub>OUT</sub> = 350 mA		_	0.8	1.2	
Input current (output off)		I <sub>IN (OFF)</sub>	4	I <sub>OUT</sub> = 500 μA, Ta = 85°C		50	65	_	μΑ
	TD62002	V		5 V <sub>CE</sub> = 2 V h <sub>FE</sub> = 800	I <sub>OUT</sub> = 350 mA	_	_	13.7	V
	1002002				I <sub>OUT</sub> = 200 mA	_	_	11.4	
Input voltage (output on)	TD62003				I <sub>OUT</sub> = 350 mA	_	_	2.6	
input voitage (output on)	1002003	V <sub>IN (ON)</sub>	3		I <sub>OUT</sub> = 200 mA	_	_	2.0	
	TD62004				I <sub>OUT</sub> = 350 mA	_	_	4.7	
	1002004				I <sub>OUT</sub> = 200 mA	_	_	4.4	
Clamp diode reverse curre	nt	1-	6	V <sub>R</sub> = 50 V, Ta = 25°C		_	_	50	μА
Clamp diode reverse current		I <sub>R</sub>	U	V <sub>R</sub> = 50 V, Ta = 85°C		_	_	100	μΑ
Clamp diode forward voltage		V <sub>F</sub>	7	I <sub>F</sub> = 350 mA		_	_	2.0	V
Input capacitance		C <sub>IN</sub>	_			_	15	_	pF
Turn-on delay		t <sub>ON</sub>	8	$V_{OUT}$ = 50 V, $R_{L}$ = 125 $\Omega$ $C_{L}$ = 15 pF			0.1		μS
Turn-off delay		t <sub>OFF</sub>	8	$V_{OUT}$ = 50 V, $R_{L}$ = 125 $\Omega$ $C_{L}$ = 15 pF		_	0.2	_	μο

## **Test Circuit**

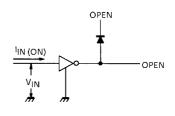
# 1. I<sub>CEX</sub>



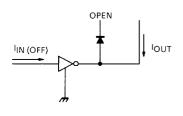
# 2. V<sub>CE (sat)</sub>, h<sub>FE</sub>



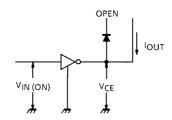
# 3. IIN (ON)



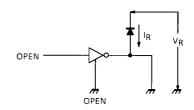
## 4. I<sub>IN</sub> (OFF)



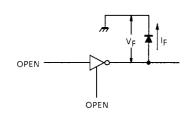
# 5. V<sub>IN (ON)</sub>



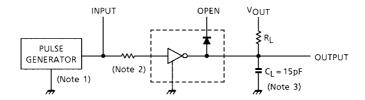
## 6. I<sub>R</sub>

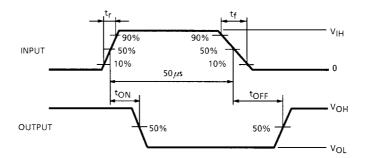


## 7. V<sub>F</sub>



### 8. ton, toff





Note 1: Pulse width 50 µs, duty cycle 10%

Output impedance 50  $\Omega$ ,  $t_r \le 5$  ns,  $t_f \le 10$  ns

Note 2: Input conditions are shown as following:

Input Condition

Type Number	R1	V <sub>IH</sub>		
TD62001APG/AFG	2.7 kΩ	3 V		
TD62002APG/AFG	0	13 V		
TD62003APG/AFG	0	3 V		
TD62004APG/AFG	0	8 V		

Note 3: C<sub>L</sub> includes probe and jig capacitance.

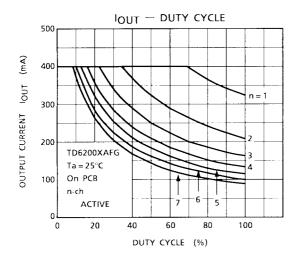
### **Precautions for Using**

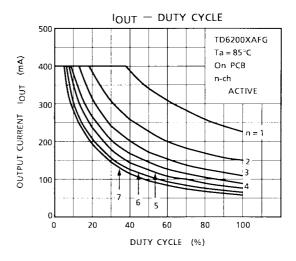
This IC does not include built-in protection circuits for excess current or overvoltage.

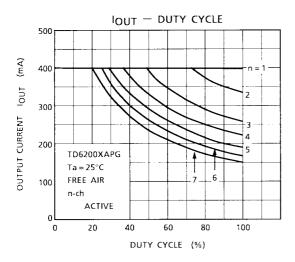
If this IC is subjected to excess current or overvoltage, it may be destroyed.

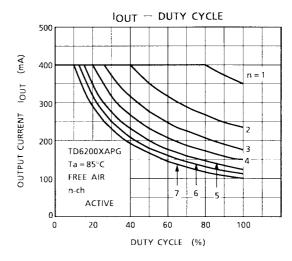
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

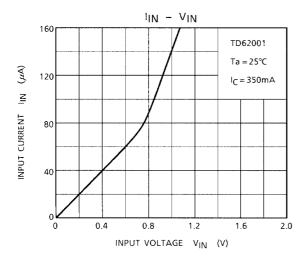
Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

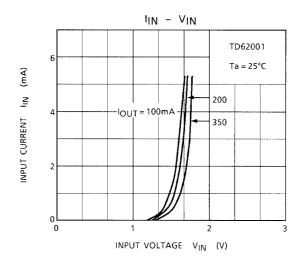


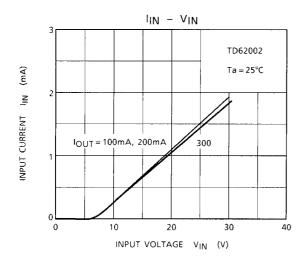


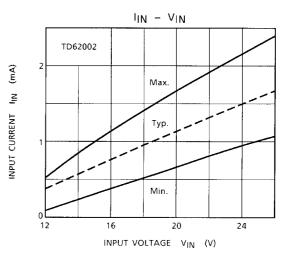


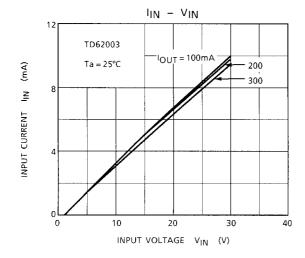


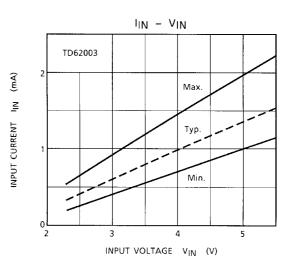


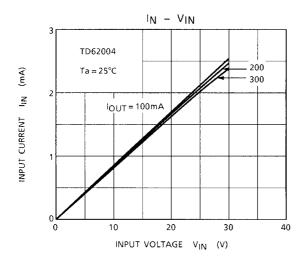


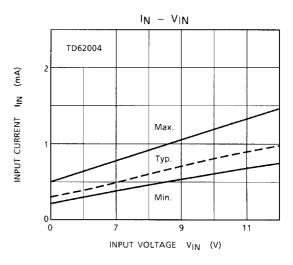


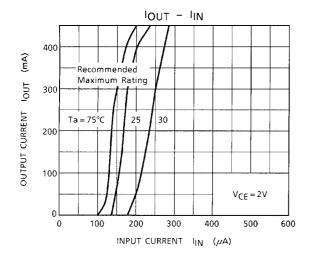


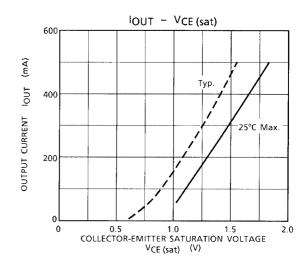


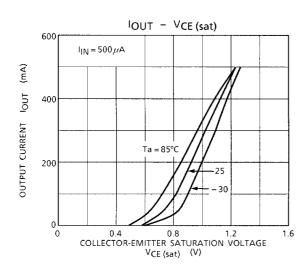


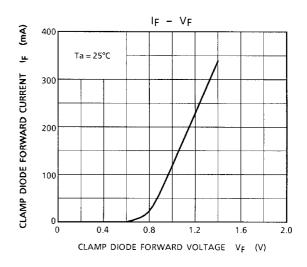




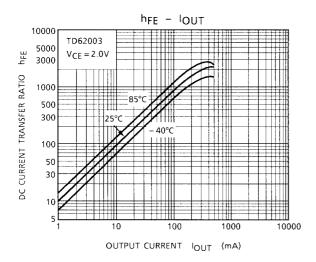


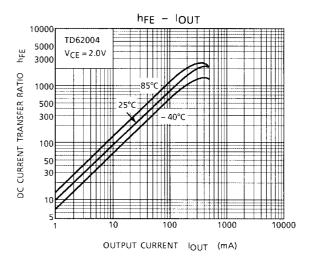


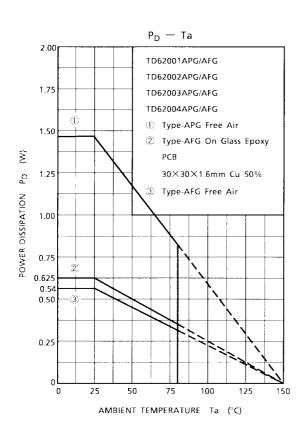




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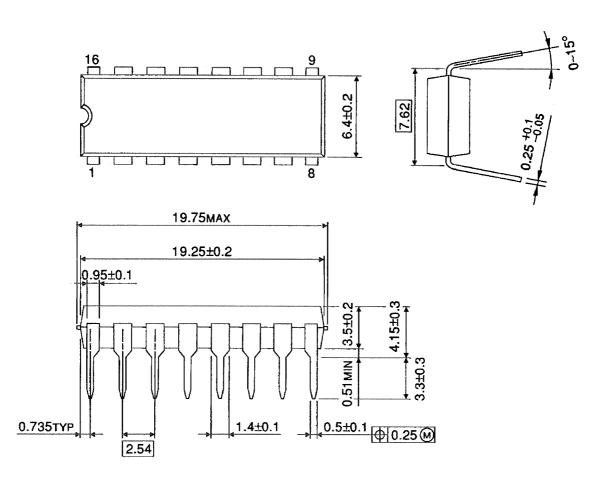




# **TOSHIBA**

## **Package Dimensions**

DIP16-P-300-2.54A Unit: mm

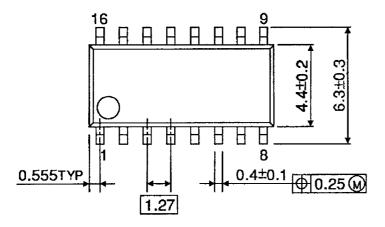


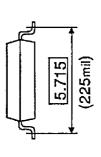
Weight: 1.11 g (Typ.)

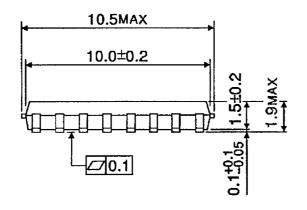
Unit: mm

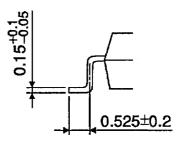
## **Package Dimensions**

SOP16-P-225-1.27









Weight: 0.16 g (Typ.)

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