

TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOS VI)

TK16H60C

Switching Regulator Applications

• Low drain-source ON resistance : $R_{DS}(ON) = 0.32\Omega$ (typ.)

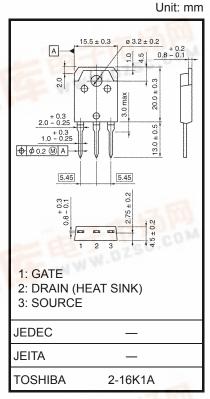
• High forward transfer admittance : $|Y_{fs}| = 11S$ (typ.)

• Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 600 \text{ V)}$

• Enhancement mode : $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V}, \text{ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stic	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	600	V	
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	600	V	
Gate-source voltage		V_{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	16	Α	
	Pulse (Note 1)	I _{DP}	64	Α	
Drain power dissipation	(Tc = 25°C)	PD	150	W	
Single-pulse avalanche	e energy (Note 2)	E _{AS}	979	mJ	
Avalanche current		I _{AR}	16	Α	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	inge	T _{stg}	-55~150	°C	



Weight: 3.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

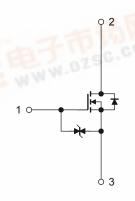
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 6.69 mH, $R_{G} = 25 \Omega$, $I_{AR} = 16 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.





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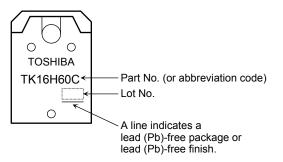
Electrical Characteristics (Ta = 25°C)

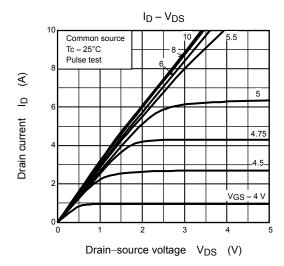
Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cutoff curr	ent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold v	oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source Ol	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 8 A	_	0.32	0.4	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 8 A	3.0	11	_	S
Input capacitano	e	C _{iss}			3100	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	20	_	
Output capacitance		Coss		_	270	_	
Switching time	Rise time	t _r	V_{GS} $\stackrel{10}{0}$ $\stackrel{V}{\bigvee}$ $\stackrel{I_{D}}{\bigvee}$ $\stackrel{8A}{\bigvee}$ $\stackrel{U}{\bigvee}$	_	60	_	- ns
	Turn-on time	t _{on}		_	110	_	
	Fall time	t _f			50	_	
	Turn-off time	t _{off}			215	_	
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 16 A	_	62	_	nC
Gate-source charge		Q _{gs}		_	40	_	
Gate-drain ("Miller") charge		Q_{gd}			22	_	

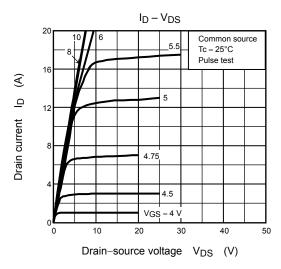
Source-Drain Ratings and Characteristics (Ta = 25°C)

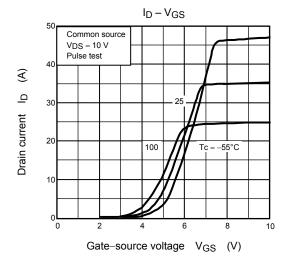
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	16	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	64	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 16 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 16 A, V _{GS} = 0 V		1050	_	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs	_	15	_	μC

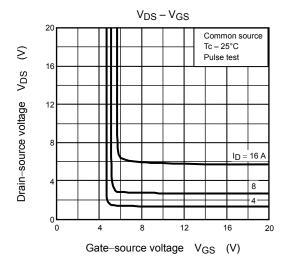
Marking

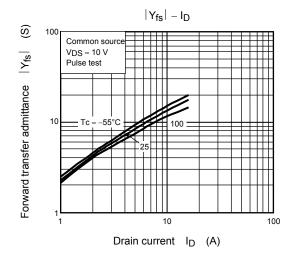


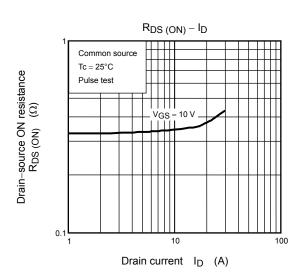


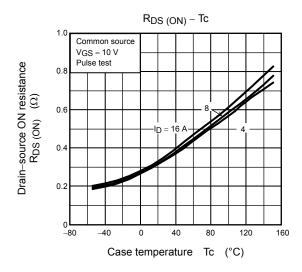


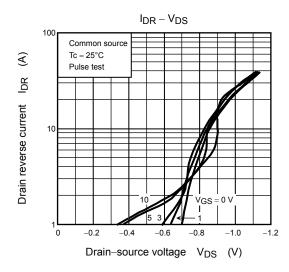


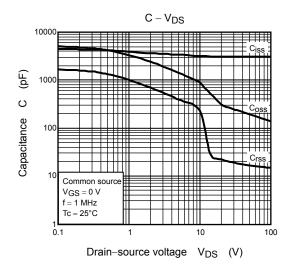


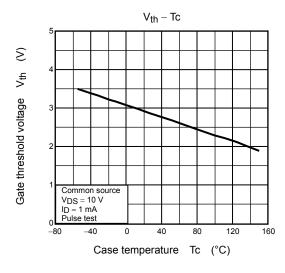


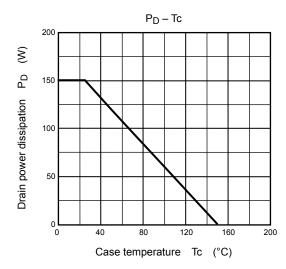


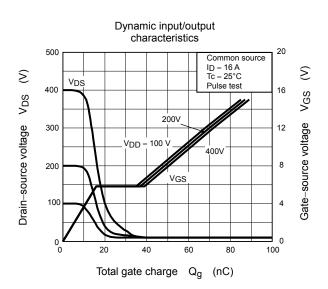


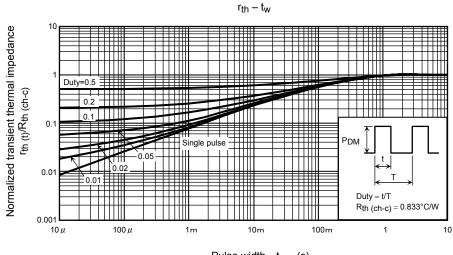




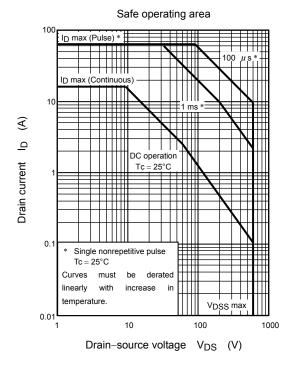


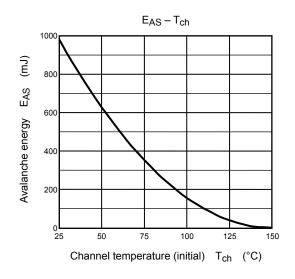


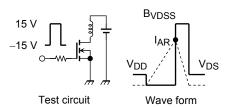




Pulse width t_w (s)







$$R_G = 25 \Omega$$

 $V_{DD} = 90 \text{ V, L} = 6.69 \text{ mH}$ $E_{AS} = \frac{1}{2} \cdot \text{L} \cdot \text{I}^2 \cdot \left(\frac{\text{BVDSS}}{\text{BVDSS} - \text{V}_{DD}}\right)$

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