查询TLP179D供应商 TOSHIBA

<u>Å</u>_Å

0.4±0.1

 2.54 ± 0.25

JEDEC

TOSHIBA

Weight: 0.1 g

EIAJ

1.4土0.25

TLP179D

TOSHIBA PHOTOCOUPLER PHOTO RELAY

TLP179D

MEASUREMENT INSTRUMENTS LOGIC IC TESTERS / MEMORY TESTERS **BOARD TESTERS / SCANNERS**

The TOSHIBA TLP179D Mini-flat photorelay is a small-outline photorelay, suitable for surface-mount assembly. The TLP179D consists of a GaAs infrared-emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

Its characteristics include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measurement instruments.

FEATURES

- 4 pin SOP (2.54SOP4) •
- 1-Form-A

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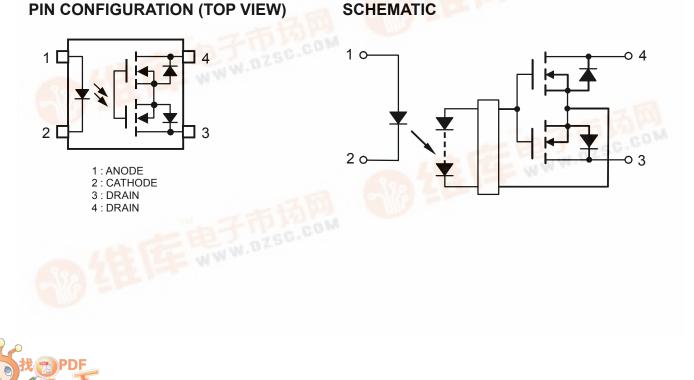
- : 200 V (min) • Peak Off-State Voltage
- Trigger LED Current : 3 mA (max) •
- On-State Current
 - **On-State Resistance** $: 50 \Omega (max)$
 - Output Capacitance
- Isolation Voltage
- : 20 pF (max)

: 2.1 mm high, 2.54 mm pitch

: 1500 Vrms (min)

: 50 mA (max)





Unit: mm

 7.0 ± 0.4

0.6±0.3

11-5H1

Absolute MAXIMUM RATINGS (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	RATING	UNIT
	Forward Current	١ _F	50	mA
Q	Forward Current Derating (Ta \ge 25°C)	∆I _F /°C	-0.5	mA/°C
LED	Reverse Voltage	V _R	5	V
Junction Temperature		Tj	125	°C
ъ	Off-State Output Terminal Voltage	V _{OFF}	200	V
DETECTOR	On-State Current	I _{ON}	50	mA
ETE	On-State Current Derating (Ta \ge 25°C)	∆l _{ON} /°C	-0.5	mA/°C
	Junction Temperature	Tj	125	°C
Storage Temperature Range		T _{stg}	-55~125	°C
Operating Temperature Range		T _{opr}	-40~85	°C
Lead Soldering Temperature (10 s)		T _{sol}	260	°C
Isolat	tion Voltage (AC, 1 minute, R.H. \leq 60%) (NOTE1)	BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(NOTE1): Device considered a two-terminal device : LED side pins shorted together, and DETECTOR side pins shorted together.

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	Min	Тур.	Max	UNIT
Supply Voltage	V _{DD}	_	_	160	V
Forward Current	١ _F	5	7.5	15	mA
On-State Current	I _{ON}	_	_	50	mA
Operating Temperature	T _{opr}	-20		60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

INDIVIDUAL ELECTRICAL CHARACTERISTICS (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
	Forward Voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse Current	I _R	$V_R = 5 V$	_	_	10	μA
	Capacitance	CT	V = 0, f = 1 MHz	_	30	_	pF
CTOR	Off-State Current	IOFF	V _{OFF} = 160 V	_	_	1	nA
DETECT	Capacitance	C _{OFF}	V = 0, f = 1 MHz		15	20	pF

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COUPLED ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Trigger LED Current	I _{FT}	I _{ON} = 50 mA	_	1	3	mA
Return LED Current	I _{FC}	I _{OFF} = 100 μA	0.1	_	_	mA
On-State Resistance	R _{ON}	$I_{ON} = 50 \text{ mA}, I_F = 5 \text{ mA}$	_	40	50	Ω

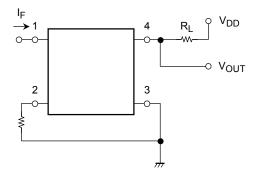
ISOLATION CHARACTERISTICS (Ta = 25°C)

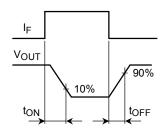
CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Capacitance Input to Output	CS	$V_S = 0 V$, f = 1 MHz	_	0.8	_	pF
Isolation Resistance	R _S	$V_S = 500 \text{ V}, \text{ R.H.} \leq 60\%$	5×10^{10}	10 ¹⁴	_	Ω
		AC, 1 minute	1500	_	_	Vrms
Isolation Voltage	BVS	AC, 1 second (in oil)	_	3000	_	VIIIS
		DC, 1 minute (in oil)		3000	_	Vdc

SWITCHING CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Turn-on Time	t _{ON}	$R_L = 200 \Omega$ (NOT	E 2) —	0.03	0.5	ms
Turn-off Time	tOFF	V _{DD} = 10 V, I _F = 5 mA	—	0.07	0.2	1115

(NOTE 2) : SWITCHING TIME TEST CIRCUIT





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RESTRICTIONS ON PRODUCT USE

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 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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