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捷多邦,专业PCB打样工厂,24小时加急出货 TL499A WIDE-RANGE POWER-SUPPLY CONTROLLERS

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- Internal Series-Pass and Step-Up Switching Regulator
- Output Adjustable From 2.9 V to 30 V
- 1-V to 10-V Input for Switching Regulator
- 4.5-V to 32-V Input for Series Regulator
- Externally Controlled Switching Current
- No External Rectifier Required



description

The TL499A is an integrated circuit designed to provide a wide range of adjustable regulated supply voltages. The regulated output voltage can be varied from 2.9 V to 30 V by adjusting two external resistors. When the TL499A is ac-coupled to line power through a step-down transformer, it operates as a series dc voltage regulator to maintain the regulated output voltage. With the addition of a battery from 1.1 V to 10 V, an inductor, a filter capacitor, and two resistors, the TL499A operates as a step-up switching regulator during an ac-line failure.

The adjustable regulated output voltage makes the TL499A useful for a wide range of applications. Providing backup power during an ac-line failure makes the TL499A extremely useful in microprocessor memory applications.

The TL499AC is characterized for operation from –20°C to 85°C.

AVAILABLE OPTIONS

TA	PLASTIC DIP (P)	PLASTIC SMALL-OUTLINE (PS)	CHIP FORM (Y)
–20°C to 85°C	TL499ACP	TL499ACPS	TL499AY

The PS package is available taped and reeled. Add the suffix R to device type (e.g., TL499ACPSR). Chip forms are tested at 25°C.



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functional block diagram





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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Output voltage, V _O (see Note 1)	35 V
Input voltage, series regulator, V ₁ 1	35 V
Input voltage, switching regulator, V ₁ 2	10 V
Blocking-diode reverse voltage	35 V
Blocking-diode forward current	1 A
Power switch current (SW IN)	1 A
Package thermal impedance, θ_{IA} (see Notes 2 and 3): P package	85°C/W
PS package	95°C/W
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C
Storage temperature range, T _{stg}	. −65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values are with respect to network ground terminal.
 - 2. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
 - 3. The package thermal impedance is calculated in accordance with JESD 51.

recommended operating conditions

	MIN	NOM	MAX	UNIT
Output voltage, VO	2.9		30	V
Input voltage, VI1 (SERIES IN1)	4.5		32	V
Input voltage, V _I 2 (SW REG IN2)	1.1		10	V
Output-to-input differential voltage, switching regulator, $V_O - V_I 2$ (see Note 4)	1.2		28.9	V
Continuous output current, IO			100	mA
Power switch current (at SW IN)			500	mA
Current-limiting resistor, R _{CL}	150		1000	Ω
Filter capacitor	100		470	μF
Pass capacitor		0.1		μF
Inductor, L (dcr \leq 0.1 Ω)	50		150	μH
Operating free-air temperature, T _A	-20		85	°C

NOTE 4: When operating temperature range is $T_A \le 70^{\circ}$ C, minimum $V_O - V_12$ is ≥ 1.2 V. When operating temperature range is $T_A \le 85^{\circ}$ C, minimum $V_O - V_12$ is ≥ 1.9 V.



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electrical characteristics over recommended operating conditions (unless otherwise noted)

DADAM	TED		TEST	ONDITIONS		т	L499AC	;	
PARAINI	EIER		TEST CO	UNDITIONS		MIN	TYP	MAX	UNIT
Voltage deviation (see I	Note 5)						20	30	mV/V
	Switching regulator	$T_A = -20^\circ C t$	o 70°C					1.2	
Dropout voltage	Switching regulator							1.9	V
	Series regulator	V _I 1 = 15 V,	l _O = 50 mA					1.8	
Reference voltage (inte	VI2 = 5 V,	V _O = 3 V,	$I_{O} = 1 \text{ mA}$		1.2	1.26	1.32	V	
Reference-voltage char	nge with temperature						5	10	mV/V
Output regulation (of re	ference voltage)	$I_{O} = 1 \text{ mA to}$	50 mA				10	30	mV/V
		V _I 2 = 1.1 V,	V _O = 12 V,	R _{CL} = 150 Ω,	T _A = 25°C	10			
Output current	Switching regulator	V _I 2 = 1.5 V,	V _O = 15 V,	R _{CL} = 150 Ω,	T _A = 25°C	15			~^^
(see Figure 1)		VI2 = 6 V,	V _O = 30 V,	R _{CL} = 150 Ω,	T _A = 25°C	65			ША
	Series regulator							100	
Standby current	Switching regulator	V _I 2 = 3 V,	V _O = 9 V,	$T_A = 25^{\circ}C$			15	80	μA
Stanuby current	Series regulator	V _I 1 = 15 V,	V _O = 9 V,	$R_E 2 = 4.7 \text{ k}\Omega$			0.8	1.2	mA

NOTE 5: Voltage deviation is the output voltage difference that occurs in a change from series regulation to switching regulation: Voltage deviation = V_0 (series regulation) - V_0 (switching regulation)

electrical characteristics over recommended operating conditions, T_{A} = 25°C (unless otherwise noted)

DADAM	TED		TEST CONDITION		Т	TL499AY		
PARAINI	LIEK		TEST CONDITION	JN3	MIN	TYP	MAX	UNIT
Voltage deviation (see I	Note 5)					20	30	mV/V
	Switching regulator	$T_A = -20^{\circ}C$ to	70°C				1.2	
Dropout voltage	Switching regulator	$T_A = -20^{\circ}C$ to	85°C			1.9	V	
	Series regulator	V _I 1 = 15 V,	I _O = 50 mA			1.8		
Reference voltage (inte	rnal)	V _I 2 = 5 V,	V _O = 3 V,	I _O = 1 mA	1.2	1.26	1.32	V
Reference-voltage char	nge with temperature	$T_A = -20^{\circ}C$ to	85°C		5	10	mV/V	
Output regulation (of re	ference voltage)	$I_{O} = 1 \text{ mA to } 50$		10	30	mV/V		
		V _I 2 = 1.1 V,	V _O = 12 V,	R _{CL} = 150 Ω	10			
Output current	Switching regulator	Vj2 = 1.5 V,	V _O = 15 V,	R _{CL} = 150 Ω	15			m A
(see Figure 1)		V 2 = 6 V,	V _O = 30 V,	R _{CL} = 150 Ω	65			MA
	Series regulator						100	
Standby ourrant	Switching regulator	$V_{1}2 = 3 V_{2}$	VO = 9 V			15	80	μA
Stanuby current	Series regulator	V _I 1 = 15 V,	V _O = 9 V,	$R_E 2 = 4.7 \text{ k}\Omega$		0.8	1.2	mA

NOTE 5: Voltage deviation is the output voltage difference that occurs in a change from series regulation to switching regulation: Voltage deviation = $V_O(series regulation) - V_O(switching regulation)$



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Figure 1. TL499A Basic Configuration

OUTPUT	SWITCHING REGULATOR INPUT VOLTAGE (SW REG IN2) (V)													
VOLTAGE	1.1	1.2	1.3	1.5	1.7	2	2.5	3	5	6	9			
(V)	(V) OUTPUT CURRENT (mA)													
30										65	90			
25									50	80	100			
20						20	25	30	80	100	100			
15				15	20	30	45	55	100	100	100			
12	10	15	20	25	30	40	55	70	100	100	100			
10	15	20	25	30	35	45	65	80	100	100				
9	20	25	25	35	40	50	70	90	100	100				
6	30	35	40	45	55	75	95	100						
5	35	40	45	55	70	85	100	100	Circuit of Figure 1, except:					
4.5	35	45	50	60	75	95	100	100†	R _{CL} = 150 Ω					
3	55	65†	75†	95†	100†				C _F = 330 μF					
2.9	60†	70†	75†	100†	100†				C	P = 0.1 µ	F			

Table 1. Maximum Output Current vs Input and Output Voltages for Step-Up Switching Regulator With R_{CL} = 150 Ω

[†] The difference between the output and input voltage for these combinations is greater than the minimum output-to-input differential-voltage specification at 70°C (1.2 V), but less than the minimum at 85°C (1.9 V).



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Table 2. Maximum Output Current vs Input and Output Voltages for Step-Up Switching Regulator With R_{CL} = 200 Ω

OUTPUT		SWITCHING REGULATOR INPUT VOLTAGE (SW REG IN2) (V)													
VOLTAGE	1.1	1.2	1.3	1.5	1.7	2	2.5	3	5	6	9				
(V)	(V) OUTPUT CURRENT (mA)														
30										50	100				
25									50	70	100				
20						15	25	30	70	90	100				
15				10	15	25	35	45	90	100	100				
12	10	10	15	20	25	35	45	60	100	100	100				
10	15	20	20	25	30	40	55	70	100	100					
9	20	20	25	30	35	45	60	80	100						
6	25	30	35	45	50	65	90	100							
5	30	35	40	55	60	75	100	100	Circuit of	Circuit of Figure 1, except:					
4.5	35	40	45	55	65	85	100	100†	R _{CL} = 200 Ω						
3	50	55†	65†	80†	90†				C _F = 330 μF						
2.9	50†	60†	65†	85†	100†				С	P = 0.1 µ	۱F				

[†] The difference between the output and input voltage for these combinations is greater than the minimum output-to-input differential-voltage specification at 70°C (1.2 V), but less than the minimum at 85°C (1.9 V).

Table 3. Maximum	Output Current vs Input and Output Voltages
for Step-Up	Switching Regulator With R _{CL} = 300 Ω

OUTPUT		SWITCHING REGULATOR INPUT VOLTAGE (SW REG IN2) (V)													
VOLTAGE	1.1	1.2	1.3	1.5	1.7	2	2.5	3	5	6	9				
(V)	OUTPUT CURRENT (mA)														
30										40	70				
25									40	55	100				
20						10	15	20	55	70	100				
15				10	10	20	30	35	75	95	100				
12	10	10	10	15	20	25	35	45	95	100	100				
10	15	15	15	20	25	30	45	55	100	100					
9	15	15	20	25	30	35	50	60	100	100					
6	25	25	30	35	45	55	70	90							
5	30	30	35	45	50	65	85	100	Circuit of	Circuit of Figure 1, except:					
4.5	30	35	40	45	55	70	95	100†	R _{CL} = 300 Ω						
3	45	50†	55†	70†	90†				C _F = 330 μF						
2.9	45†	50†	60†	75†	95†				С	P = 0.1 µ	۱F				

[†] The difference between the output and input voltage for these combinations is greater than the minimum output-to-input differential-voltage specification at 70°C (1.2 V), but less than the minimum at 85°C (1.9 V).



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Table 4. Maximum Output Current vs Input and Output Voltages for Step-Up Switching Regulator With R_{CL} = 510 Ω

OUTPUT		SWITCHING REGULATOR INPUT VOLTAGE (SW REG IN2) (V)													
VOLTAGE	1.1	1.2	1.3	1.5	1.7	2	2.5	3	5	6	9				
(V)				OUTPUT CURRENT (mA)											
30										30	50				
25									25	40	75				
20									40	55	90				
15							15	20	55	70	100				
12					10	15	25	35	65	80	100				
10				10	20	25	30	40	70	85					
9	10	10	10	15	20	25	35	45	75	100					
6	15	20	20	25	30	35	50	60							
5	20	20	25	30	35	45	55	70	Circuit of Figure 1, except:						
4.5	20	25	30	35	40	50	65	90†	R _{CL} = 510 Ω						
3	35	35†	40†	50†	75†				C _F = 330 μF						
2.9	35†	35†	40†	55†	80†				C	P = 0.1 µ	۱F				

[†] The difference between the output and input voltage for these combinations is greater than the minimum output-to-input differential-voltage specification at 70°C (1.2 V), but less than the minimum at 85°C (1.9 V).

Table 5. Maximum Output Current vs Input and Output Voltages
for Step-Up Switching Regulator With R _{CL} = 1 k Ω

OUTPUT		SWITCHING REGULATOR INPUT VOLTAGE (SW REG IN2) (V)													
VOLTAGE	1.1	1.2	1.3	1.5	1.7	2	2.5	3	5	6	9				
(V)			-												
30											35				
25										35	50				
20										35	60				
15								10	30	45	65				
12								20	40	45	85				
10							15	25	40	55					
9				10	10	15	25	30	45	60					
6	10	10	10	15	20	20	30	35							
5	10	10	15	20	20	25	35	40	Circuit of	Circuit of Figure 1, except:					
4.5	15	15	15	20	25	30	40	45†	$R_{CL} = 1 k\Omega$						
3	20	25†	25†	30†	35†				C _F = 330 μF						
2.9	20†	25†	25†	30†	45†				c c	P = 0.1 µ	F				

[†] The difference between the output and input voltage for these combinations is greater than the minimum output-to-input differential-voltage specification at 70°C (1.2 V), but less than the minimum at 85°C (1.9 V).



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