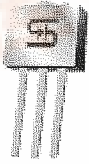


		TSM2N60 N-Channel Power Enhancement Mode MOSFET	
TO-252 	TO-251 	Pin assignment: 1. Gate 2. Source 3. Drain	$V_{DS} = 600V$ $I_D = 2A$ $R_{DS(on)}, V_{GS} @ 10V, I_{DS} @ 1.0A = 4.4\Omega$

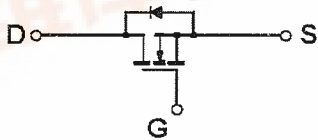
General Description

The TSM2N60 is used an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain- to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

Features

- Robust high voltage termination
- Avalanche energy specified
- Diode is characterized for use in bridge circuits
- Source to Drain diode recovery time comparable to a discrete fast recovery diode.
- I_{DSS} and $V_{DS(on)}$ specified at elevated temperature

Block Diagram



Ordering Information

Part No.	Packing	Package
TSM2N60CP	Tape & Reel	TO-252
TSM2N60CH	Tube	TO-251

Absolute Maximum Rating (Ta = 25 °C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	600V	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	2	A
Pulsed Drain Current	I_{DM}	9	A
Maximum Power Dissipation	P_D	50	W
		0.4	W/°C
Operating Junction Temperature	T_J	+150	°C
Operating Junction and Storage Temperature Range	T_J, T_{STG}	- 55 to +150	°C
Single Pulse Drain to Source Avalanche Energy ($V_{DD} = 100V, V_{GS}=10V, I_{AS}=2A, L=10mH, R_G=25\Omega$)	E_{AS}	20	mJ

Thermal Performance

Parameter	Symbol	Limit	Unit
Lead Temperature (1/8" from case)	T_L	10	S
Junction to Ambient Thermal Resistance (PCB mounted) Note: Surface mounted on FR4 board t<=10sec.	$R_{\theta JA}$	62.5	°C/W



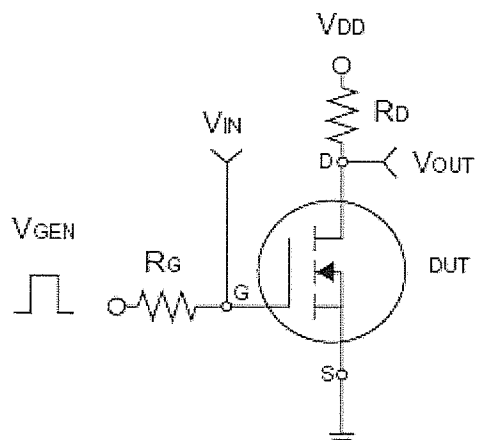
Electrical Characteristics

T_j = 25 °C, unless otherwise noted

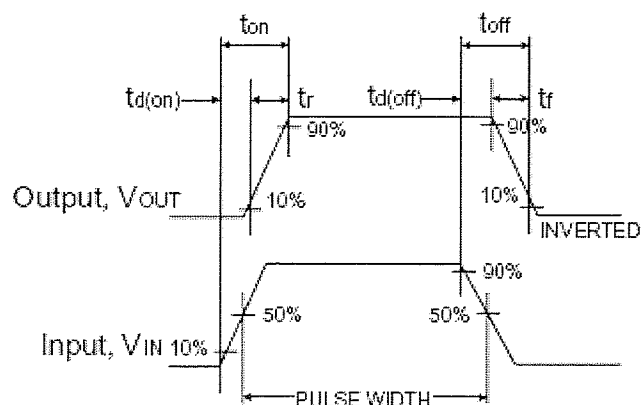
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV_{DSS}	600	--	--	V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 0.6A$	$R_{DS(ON)}$	--	--	8.0	Ω
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(TH)}$	2.0	--	4.0	V
Zero Gate Voltage Drain Current	$V_{DS} = 600V, V_{GS} = 0V$	I_{DSS}	--	--	250	μA
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I_{GSS}	--	--	± 100	nA
Forward Transconductance	$V_{DS} \geq 50V, I_D = 1.0A$	g_{fs}	1.0	--	--	S
Dynamic						
Total Gate Charge	$V_{DS} = 400V, I_D = 2.0A, V_{GS} = 10V$	Q_g	--	13	22	nC
Gate-Source Charge		Q_{gs}	--	2	--	
Gate-Drain Charge		Q_{gd}	--	6	--	
Turn-On Delay Time	$V_{DD} = 300V, I_D = 2A, V_{GEN} = 10V, R_G = 18\Omega$	$t_{d(on)}$	--	12		nS
Turn-On Rise Time		t_r	--	21		
Turn-Off Delay Time		$t_{d(off)}$	--	30		
Turn-Off Fall Time		t_f	--	24		
Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	C_{iss}	--	435	--	pF
Output Capacitance		C_{oss}	--	56	--	
Reverse Transfer Capacitance		C_{rss}	--	9.2	--	
Source-Drain Diode						
Max. Diode Forward Current		I_S	--	--	2.0	A
Diode Forward Voltage	$I_S = 2.0A, V_{GS} = 0V$	V_{SD}	--	--	1.6	V

Note: 1. pulse test: pulse width ≤ 300uS, duty cycle ≤ 2%

2. Negligible, Dominated by circuit inductance.

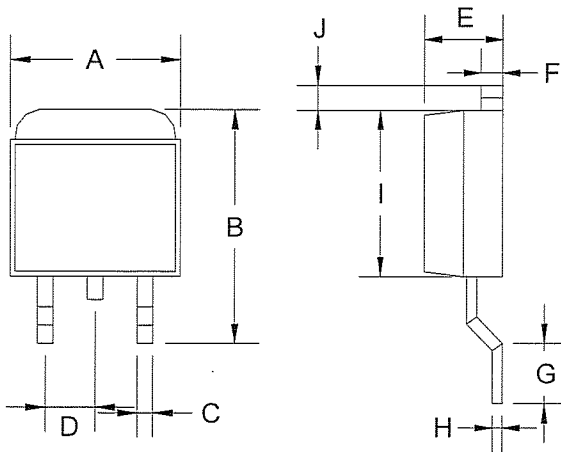


Switching Test Circuit



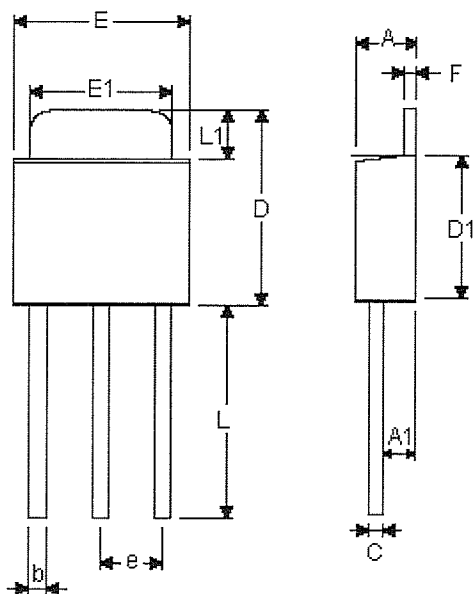
Switchin Waveforms

TO-252 Mechanical Drawing



TO-252 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	6.570	6.840	0.259	0.269
B	9.250	10.400	0.364	0.409
C	0.550	0.700	0.022	0.028
D	2.560	2.670	0.101	0.105
E	2.300	2.390	0.090	0.094
F	0.490	0.570	0.019	0.022
G	1.460	1.580	0.057	0.062
H	0.520	0.570	0.020	0.022
I	5.340	5.550	0.210	0.219
J	1.460	1.640	0.057	0.065

TO-252 Mechanical Drawing



TO-252 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.20	2.4	0.087	0.095
A1	1.10	1.30	0.043	0.051
b	0.40	0.80	0.016	0.032
C	0.40	0.60	0.016	0.024
D	6.70	7.30	0.264	0.287
D1	5.40	5.65	0.213	0.222
E	6.40	6.65	0.252	0.262
e	2.10	2.50	0.083	0.098
F	0.40	0.60	0.016	0.024
L	7.00	8.00	0.276	0.315
L1	1.60	1.86	0.063	0.073