

N		Datenblatt / Data Sheet	power electronics in motion 
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Netz-Thyristor-Modul Phase Control Thyristor Module	TT106N	
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	TT106N	TD106N	DT106N
	TD106N..K..-A	TD106N..K..-K	

Elektrische Eigenschaften / Electrical properties

Höchstzulässige Werte / Maximum rated values

Periodische Vorwärts- und Rückwärts-Spitzenperrspannung repetitive peak forward off-state and reverse voltages	$T_{vj} = -40^{\circ}\text{C} \dots T_{vj\ max}$	V_{DRM}, V_{RRM}	1200 1600	1400 1800	V V
Vorwärts-Stoßspitzensperrspannung non-repetitive peak forward off-state voltage	$T_{vj} = -40^{\circ}\text{C} \dots T_{vj\ max}$	V_{DSM}	1200 1600	1400 1800	V V
Rückwärts-Stoßspitzensperrspannung non-repetitive peak reverse voltage	$T_{vj} = +25^{\circ}\text{C} \dots T_{vj\ max}$	V_{RSM}	1300 1700	1500 1900	V V
Durchlaßstrom-Grenzeffektivwert maximum RMS on-state current		I_{TRMSM}		180	A
Dauergrenzstrom average on-state current	$T_C = 85^{\circ}\text{C}$ $T_C = 78^{\circ}\text{C}$	I_{TAVM}		106 115	A A
Stoßstrom-Grenzwert surge current	$T_{vj} = 25^{\circ}\text{C}, t_p = 10\ \text{ms}$ $T_{vj} = T_{vj\ max}, t_p = 10\ \text{ms}$	I_{TSM}		2250 2000	A A
Grenzlastintegral I^2t -value	$T_{vj} = 25^{\circ}\text{C}, t_p = 10\ \text{ms}$ $T_{vj} = T_{vj\ max}, t_p = 10\ \text{ms}$	I^2t		25300 20000	A ² s A ² s
Kritische Stromsteilheit critical rate of rise of on-state current	DIN IEC 747-6 $f = 50\ \text{Hz}$, $i_{GM} = 0,6\text{A}, di_G/dt = 0,6\text{A}/\mu\text{s}$	$(di_T/dt)_{cr}$		150	A/ μs
Kritische Spannungssteilheit critical rate of rise of off-state voltage	$T_{vj} = T_{vj\ max}, V_D = 0,67 V_{DRM}$ 6.Kennbuchstabe / 6 th letter F	$(dv_D/dt)_{cr}$		1000	V/ μs

Charakteristische Werte / Characteristic values

Durchlaßspannung on-state voltage	$T_{vj} = T_{vj\ max}, i_T = 300\ \text{A}$	v_T		max. 1,78	V
Schleusenspannung threshold voltage	$T_{vj} = T_{vj\ max}$	$V_{(TO)}$		0,9	V
Ersatzwiderstand slope resistance	$T_{vj} = T_{vj\ max}$	r_T		2,6	m Ω
Zündstrom gate trigger current	$T_{vj} = 25^{\circ}\text{C}, V_D = 6\ \text{V}$	I_{GT}		max. 150	mA
Zündspannung gate trigger voltage	$T_{vj} = 25^{\circ}\text{C}, V_D = 6\ \text{V}$	V_{GT}		max. 1,4	V
Nicht zündender Steuerstrom gate non-trigger current	$T_{vj} = T_{vj\ max}, V_D = 6\ \text{V}$ $T_{vj} = T_{vj\ max}, V_D = 0,5 V_{DRM}$	I_{GD}		max. 5,0 max. 2,5	mA mA
Nicht zündende Steuerspannung gate non-trigger voltage	$T_{vj} = T_{vj\ max}, V_D = 0,5 V_{DRM}$	V_{GD}		max. 0,2	V
Haltestrom holding current	$T_{vj} = 25^{\circ}\text{C}, V_D = 6\ \text{V}, R_A = 5\ \Omega$	I_H		max. 200	mA
Einraststrom latching current	$T_{vj} = 25^{\circ}\text{C}, V_D = 6\ \text{V}, R_{GK} \geq 10\ \Omega$ $i_{GM} = 0,6\text{A}, di_G/dt = 0,6\text{A}/\mu\text{s}$, $t_g = 20\ \mu\text{s}$	I_L		max. 620	mA
Vorwärts- und Rückwärts-Sperrstrom forward off-state and reverse current	$T_{vj} = T_{vj\ max}$ $V_D = V_{DRM}, V_R = V_{RRM}$	i_D, i_R		max. 30	mA
Zündverzug gate controlled delay time	DIN IEC 747-6 $T_{vj} = 25^{\circ}\text{C}$, $i_{GM} = 0,6\text{A}, di_G/dt = 0,6\text{A}/\mu\text{s}$	t_{gd}		max. 3	μs

prepared by:	C.Drilling	date of publication:	16.05.02
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N**Datenblatt / Data Sheet**

power electronics in motion

eupec**Netz-Thyristor-Modul**
Phase Control Thyristor Module**TT106N****Elektrische Eigenschaften / Electrical properties**


Charakteristische Werte / Characteristic values

Freiwerdezeit circuit commutated turn-off time	$T_{vj} = T_{vj\ max}, i_{TM} = I_{TAVM}$ $V_{RM} = 100\ V, v_{DM} = 0,67\ V_{DRM}$ $dv_D/dt = 20\ V/\mu s, -di_T/dt = 10\ A/\mu s$ 5.Kennbuchstabe / 5 th letter O	t_q	typ.	150	μs
Isolations-Prüfspannung insulation test voltage	RMS, f = 50 Hz, t = 1 min RMS, f = 50 Hz, t = 1 sec	V_{ISOL}		3,0 3,6	kV kV

Thermische Eigenschaften / Thermal properties

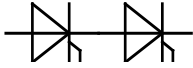
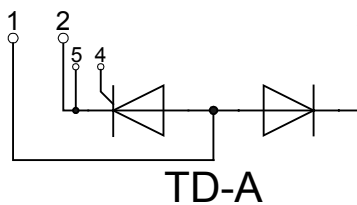
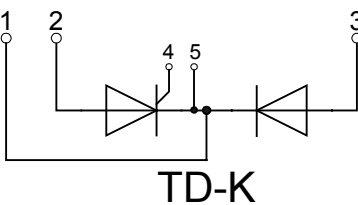
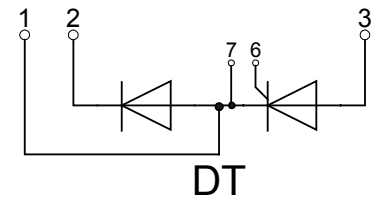
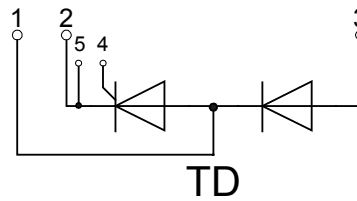
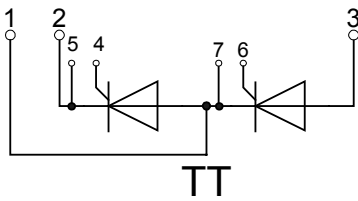
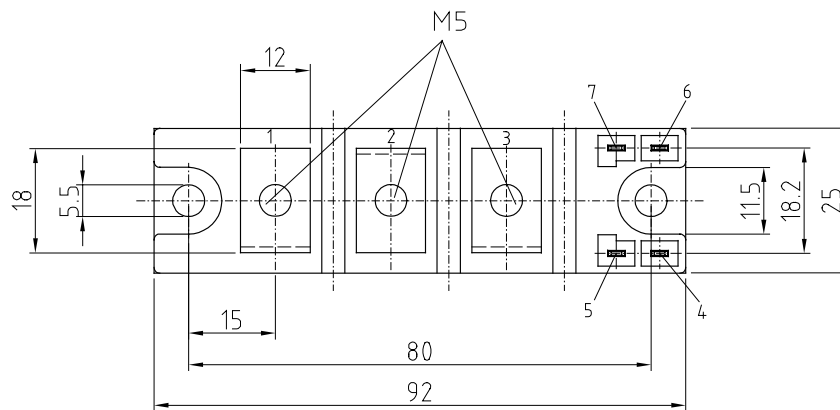
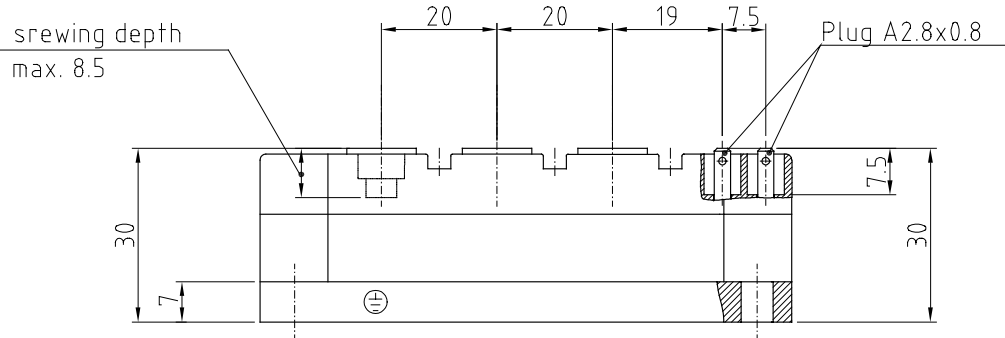
Innerer Wärmewiderstand thermal resistance, junction to case	pro Modul / per Module, $\Theta = 180^\circ\ sin$ pro Zweig / per arm, $\Theta = 180^\circ\ sin$ pro Modul / per Module, DC pro Zweig / per arm, DC	R_{thJC}	max.	0,165	$^\circ C/W$
			max.	0,330	$^\circ C/W$
			max.	0,155	$^\circ C/W$
			max.	0,310	$^\circ C/W$
Übergangs-Wärmewiderstand thermal resistance, case to heatsink	pro Modul / per Module pro Zweig / per arm	R_{thCH}	max.	0,04	$^\circ C/W$
			max.	0,08	$^\circ C/W$
Höchstzulässige Sperrschichttemperatur maximum junction temperature		$T_{vj\ max}$		140	$^\circ C$
Betriebstemperatur operating temperature		$T_{c\ op}$		-40...+140	$^\circ C$
Lagertemperatur storage temperature		T_{stg}		-40...+140	$^\circ C$

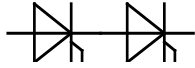
Mechanische Eigenschaften / Mechanical properties

Gehäuse, siehe Anlage case, see annex				Seite 3 Page 3	
Si-Element mit Druckkontakt Si-pellet with pressure contact					
Innere Isolation internal insulation				AIN	
Anzugsdrehmoment für mechanische Anschlüsse mounting torque	Toleranz / Tolerance $\pm 15\%$	M1		4	Nm
Anzugsdrehmoment für elektrische Anschlüsse terminal connection torque	Toleranz / Tolerance $\pm 10\%$	M2		4	Nm
Steueranschlüsse control terminals	DIN 46 244			A 2,8 x 0,8	
Gewicht weight		G	typ.	250	g
Kriechstrecke creepage distance				15	mm
Schwingfestigkeit vibration resistance	f = 50 Hz			50	m/s ²
	file-No.			E 83336	

Mit diesem Datenblatt werden Halbleiterbauelemente spezifiziert, jedoch keine Eigenschaften zugesichert. Es gilt in Verbindung mit den zugehörigen technischen Erläuterungen.

This data sheet specifies semiconductor devices, but promises no characteristics. It is valid in combination with the belonging technical notes.

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eupec**Netz-Thyristor-Modul**
Phase Control Thyristor Module**TT106N**



Netz-Thyristor-Modul
Phase Control Thyristor Module

TT106N

Analytische Elemente des transienten Wärmewiderstandes Z_{thJC} für DC
Analytical elements of transient thermal impedance Z_{thJC} for DC

Pos. n	1	2	3	4	5	6	7
R_{thn} [°C/W]	0,0127	0,03	0,049	0,1682	0,0315		
τ_n [s]	0,001	0,0092	0,074	0,57	3,51		

Analytische Funktion / Analytical function:
$$Z_{thJC} = \sum_{n=1}^{n_{max}} R_{thn} \left(1 - e^{-\frac{t}{\tau_n}} \right)$$

Luftselbstkühlung / Natural cooling
3 Module pro Kühlkörper / 3 modules per heatsink
Kühlkörper / Heatsink type: KM14 (50W)

Analytische Elemente des transienten Wärmewiderstandes Z_{thCA}
Analytical elements of transient thermal impedance Z_{thCA}

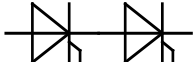
Pos. n	1	2	3	4	5	6	7
R_{thn} [°C/W]	0,048	0,202	2,05				
τ_n [s]	3,71	40	984				

Verstärkte Kühlung / Forced cooling
3 Module pro Kühlkörper / 3 modules per heatsink
Kühlkörper / Heatsink type: KM 14 (Papst 4650N)

Analytische Elemente des transienten Wärmewiderstandes Z_{thCA}
Analytical elements of transient thermal impedance Z_{thCA}

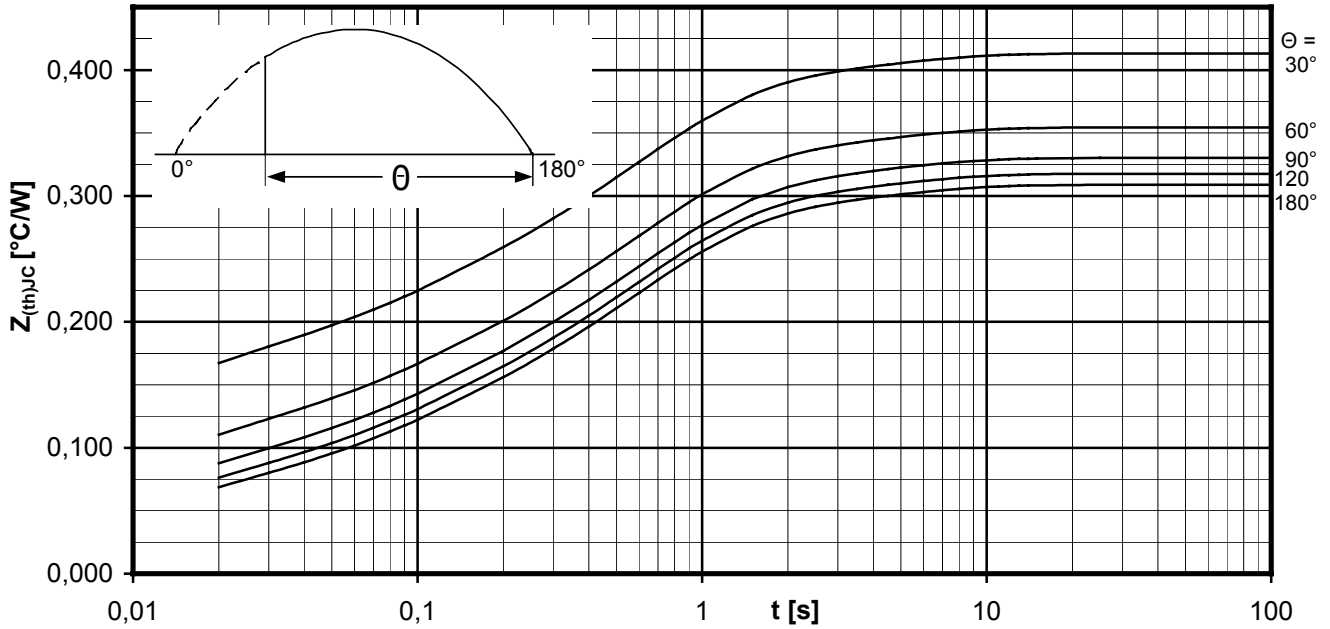
Pos. n	1	2	3	4	5	6	7
R_{thn} [°C/W]	0,048	0,202	0,53				
τ_n [s]	3,71	40	254				

Analytische Funktion / Analytical function:
$$Z_{thCA} = \sum_{n=1}^{n_{max}} R_{thn} \left(1 - e^{-\frac{t}{\tau_n}} \right)$$



Netz-Thyristor-Modul
Phase Control Thyristor Module

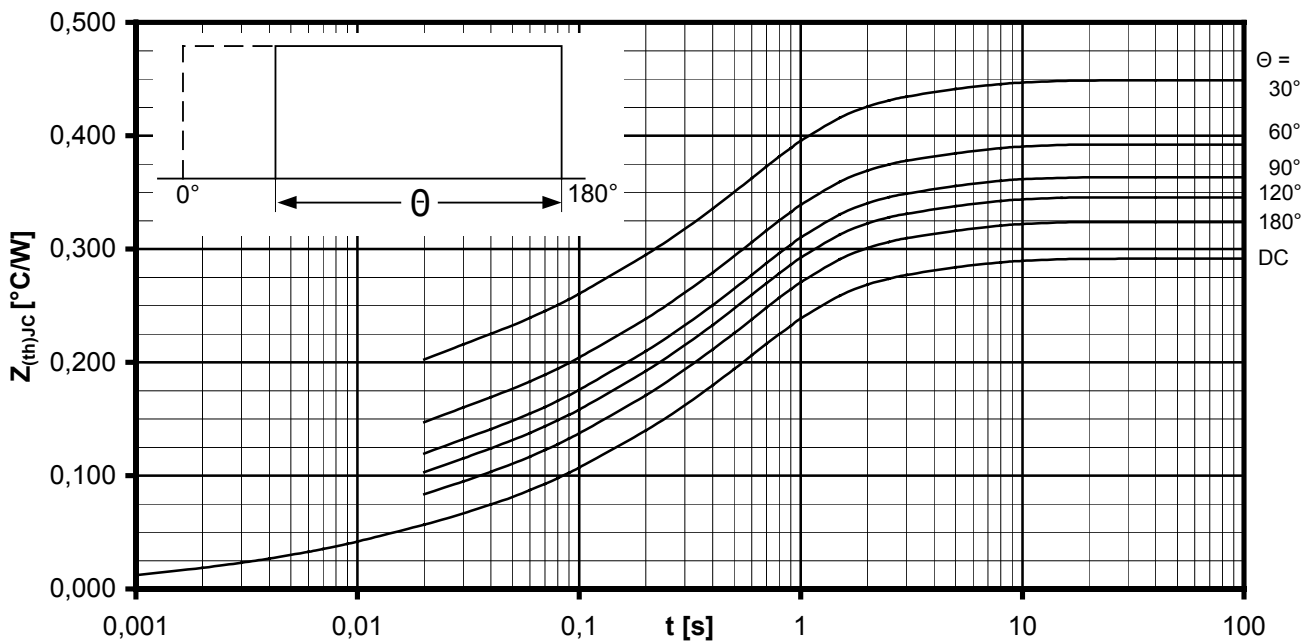
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Transienter innerer Wärmewiderstand je Zweig / Transient thermal impedance per arm $Z_{thJc} = f(t)$

Sinusförmiger Strom / Sinusoidal current

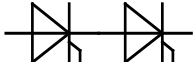
Parameter: Stromflußwinkel Θ / Current conduction angle Θ



Transienter innerer Wärmewiderstand je Zweig / Transient thermal impedance per arm $Z_{thJc} = f(t)$

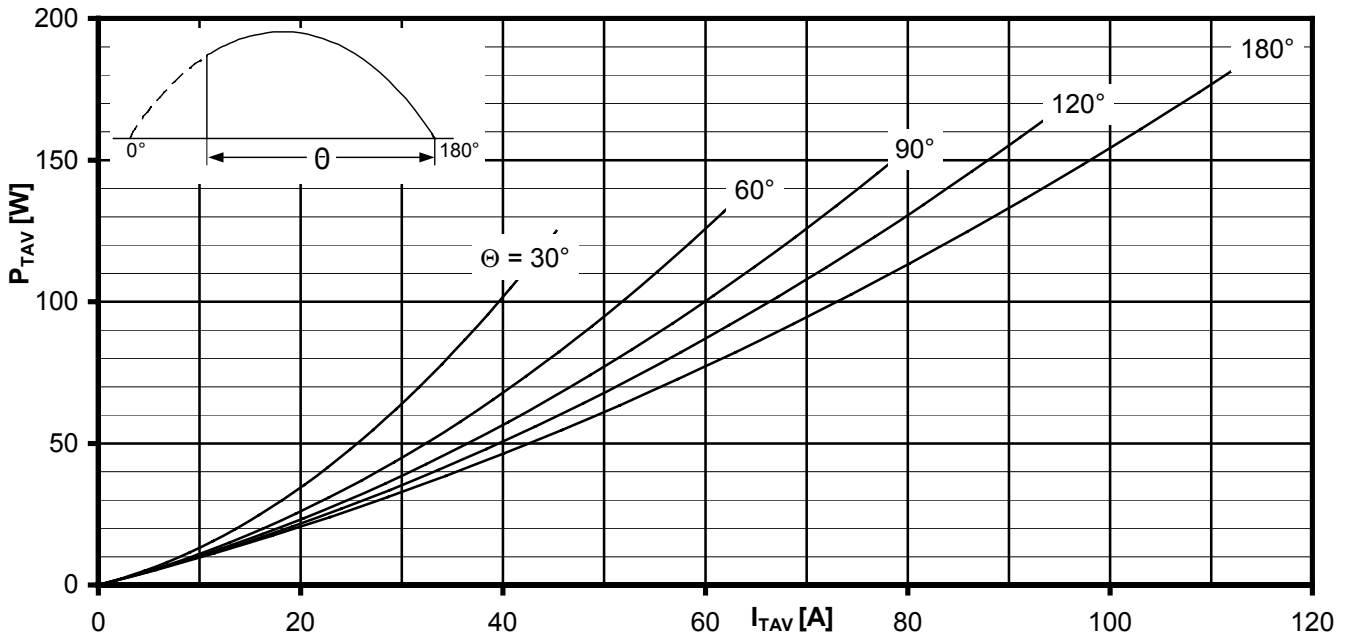
Rechteckförmiger Strom / Rectangular current

Parameter: Stromflußwinkel Θ / Current conduction angle Θ



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Phase Control Thyristor Module

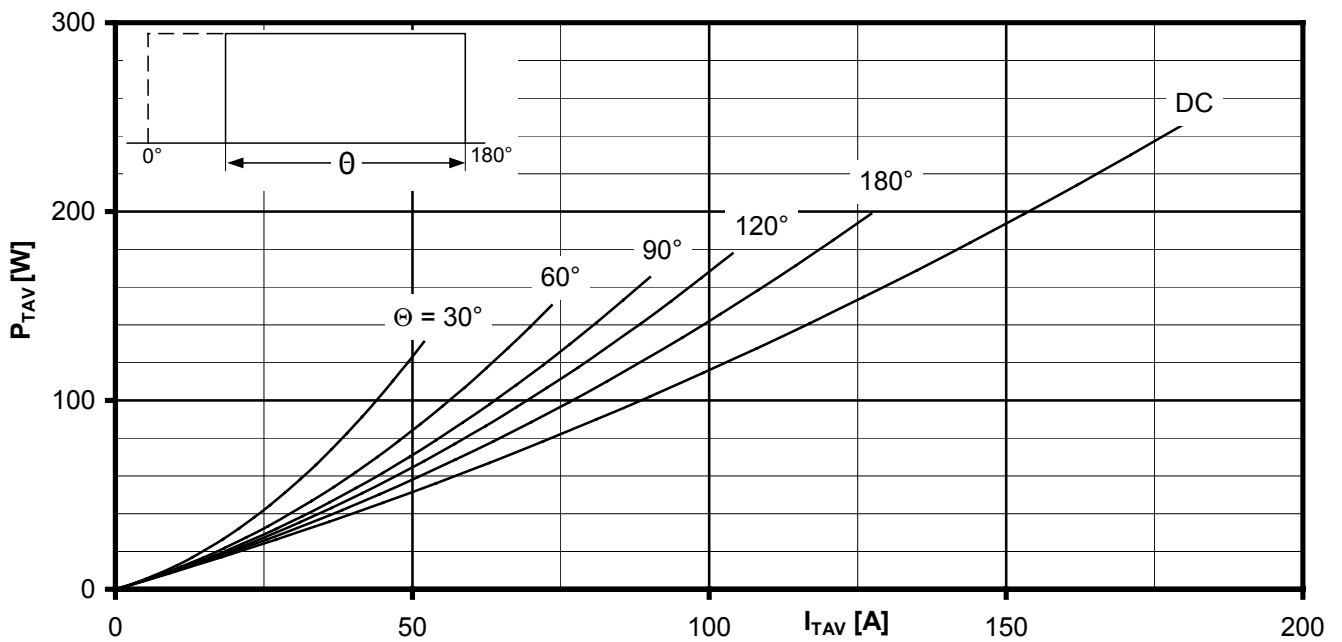
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Durchlassverlustleistung je Zweig / On-state power loss per arm $P_{TAV} = f(I_{TAV})$

Sinusförmiger Strom / Sinusoidal current Strombelastung je Zweig / Current load per arm

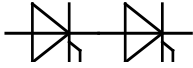
Parameter: Stromflußwinkel / Current conduction angle Θ



Durchlassverlustleistung je Zweig / On-state power loss per arm $P_{TAV} = f(I_{TAV})$

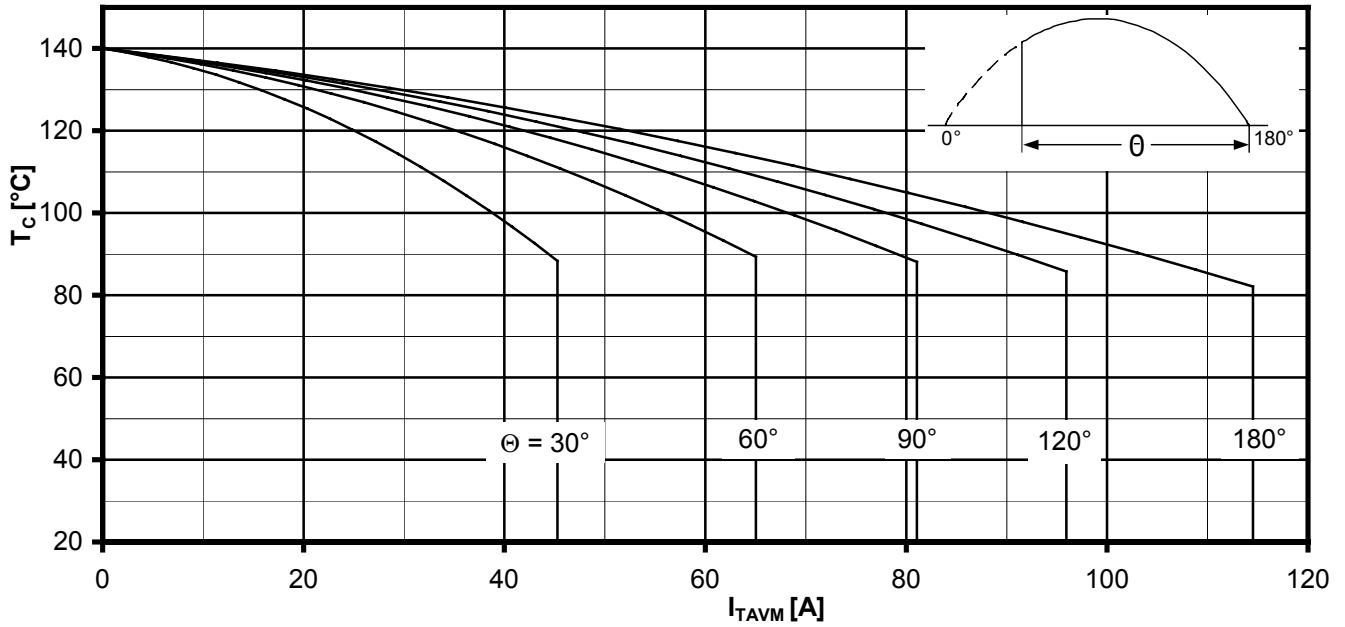
Rechteckförmiger Strom / Rectangular current Strombelastung je Zweig / Current load per arm

Parameter: Stromflußwinkel / Current conduction angle Θ



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Phase Control Thyristor Module

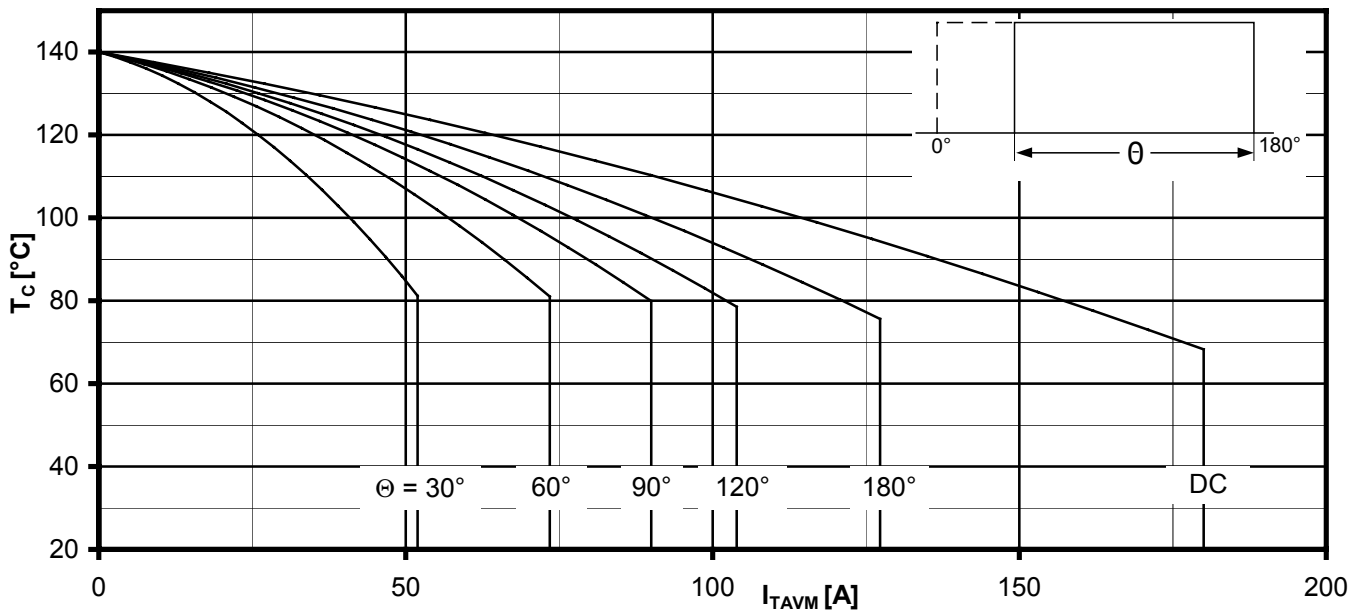
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Höchstzulässige Gehäusetemperatur / Maximum allowable case temperature $T_C = f(I_{TAVM})$

Sinusförmiger Strom / Sinusoidal current Strombelastung je Zweig / Current load per arm

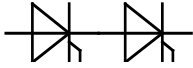
Parameter: Stromflußwinkel Θ / Current conduction angle Θ



Höchstzulässige Gehäusetemperatur / Maximum allowable case temperature $T_C = f(I_{TAVM})$

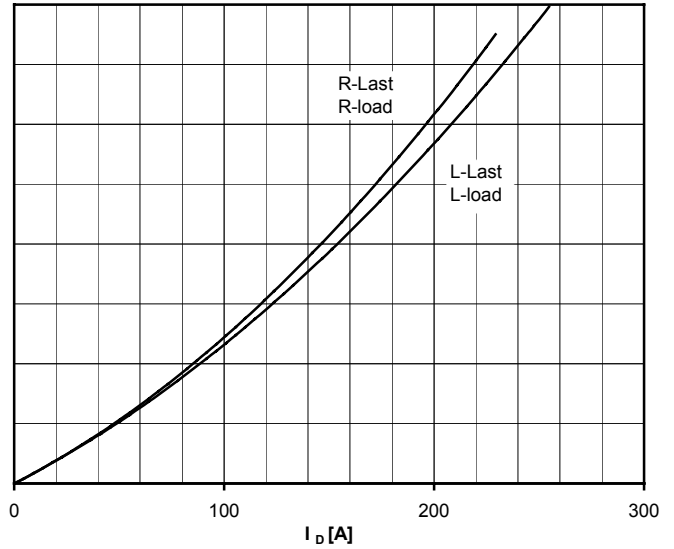
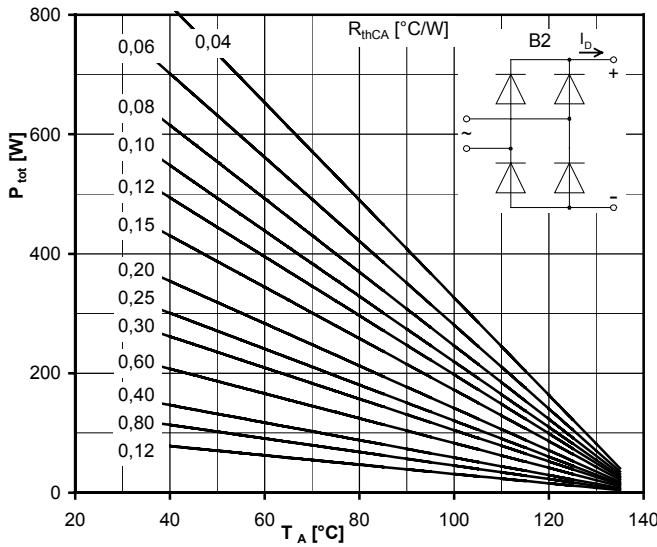
Rechteckförmiger Strom / Rectangular current Strombelastung je Zweig / Current load per arm

Parameter: Stromflußwinkel Θ / Current conduction angle Θ



**Netz-Thyristor-Modul
Phase Control Thyristor Module**

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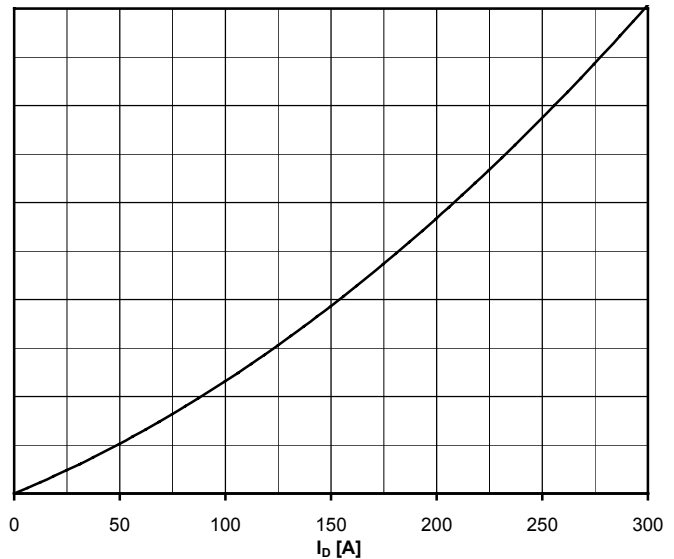
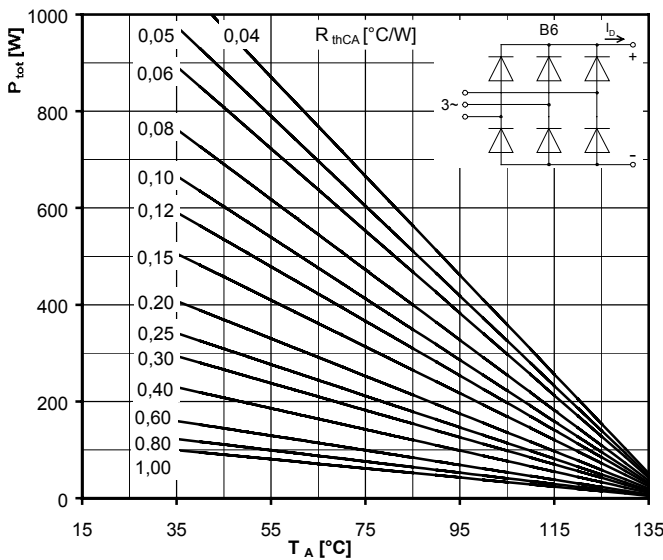
Höchstzulässiger Ausgangsstrom / Maximum rated output current I_D

B2- Zweipuls-Brückenschaltung / Two-pulse bridge circuit

Gesamtverlustleistung der Schaltung / Total power dissipation at circuit P_{tot}

Parameter:

Wärmewiderstand zwischen den Gehäusen und Umgebung / Thermal resistance cases to ambient R_{thCA}



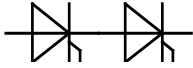
Höchstzulässiger Ausgangsstrom / Maximum rated output current I_D

B6- Sechspuls-Brückenschaltung / Six-pulse bridge circuit

Gesamtverlustleistung der Schaltung / Total power dissipation at circuit P_{tot}

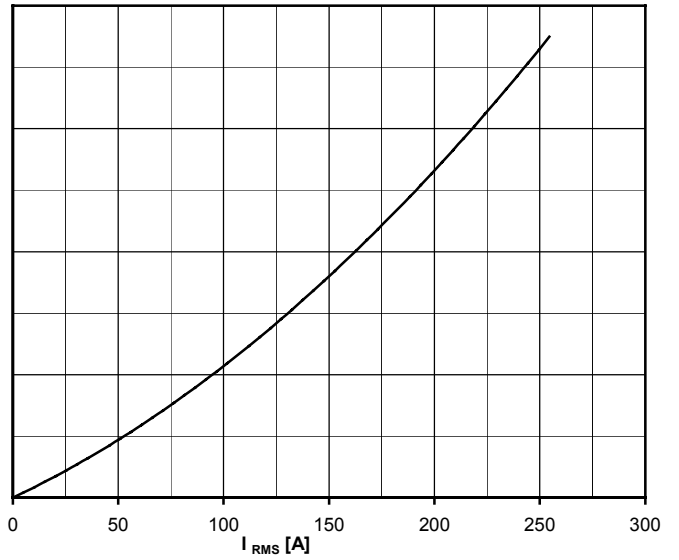
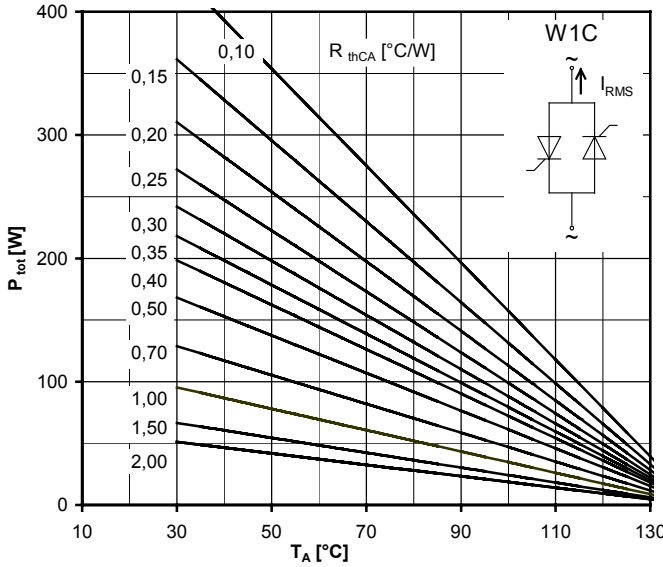
Parameter:

Wärmewiderstand zwischen den Gehäusen und Umgebung / Thermal resistance cases to ambient R_{thCA}



**Netz-Thyristor-Modul
Phase Control Thyristor Module**

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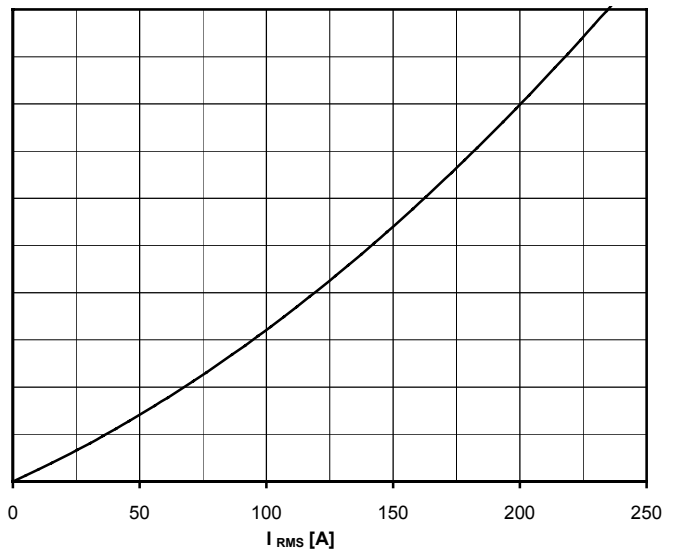
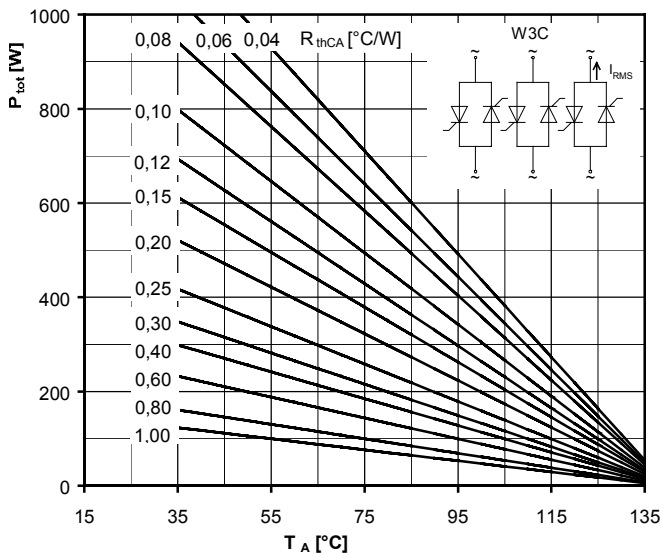
Höchstzulässiger Effektivstrom / Maximum rated RMS current I_{RMS}

W1C - Einphasen-Wechselwgschaltung / Single-phase inverse parallel circuit

Gesamtverlustleistung der Schaltung / Total power dissipation at circuit P_{tot}

Parameter:

Wärmewiderstand zwischen den Gehäusen und Umgebung / Thermal resistance case to ambient R_{thCA}



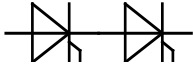
Höchstzulässiger Effektivstrom / Maximum rated RMS current I_{RMS}

W3C - Dreiphasen-Wechselwgschaltung / Three-phase inverse parallel circuit

Gesamtverlustleistung der Schaltung / Total power dissipation at circuit P_{tot}

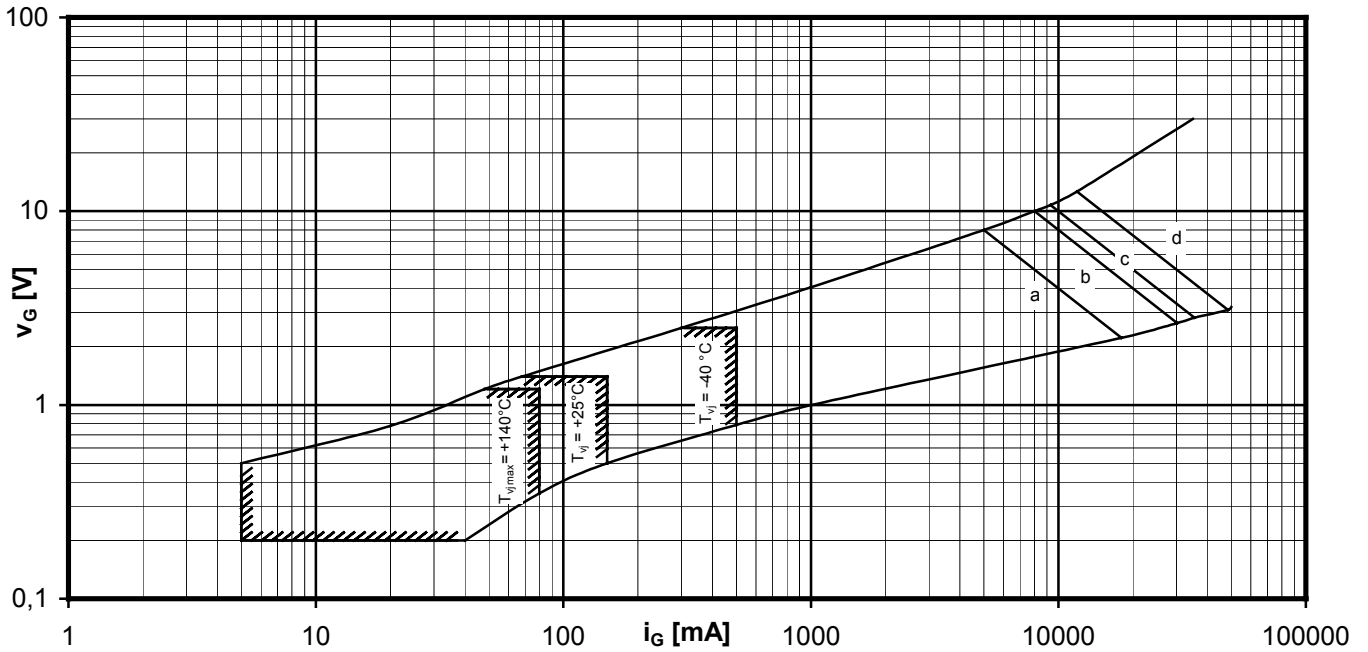
Parameter:

Wärmewiderstand zwischen den Gehäusen und Umgebung / Thermal resistance cases to ambient R_{thCA}



Netz-Thyristor-Modul
Phase Control Thyristor Module

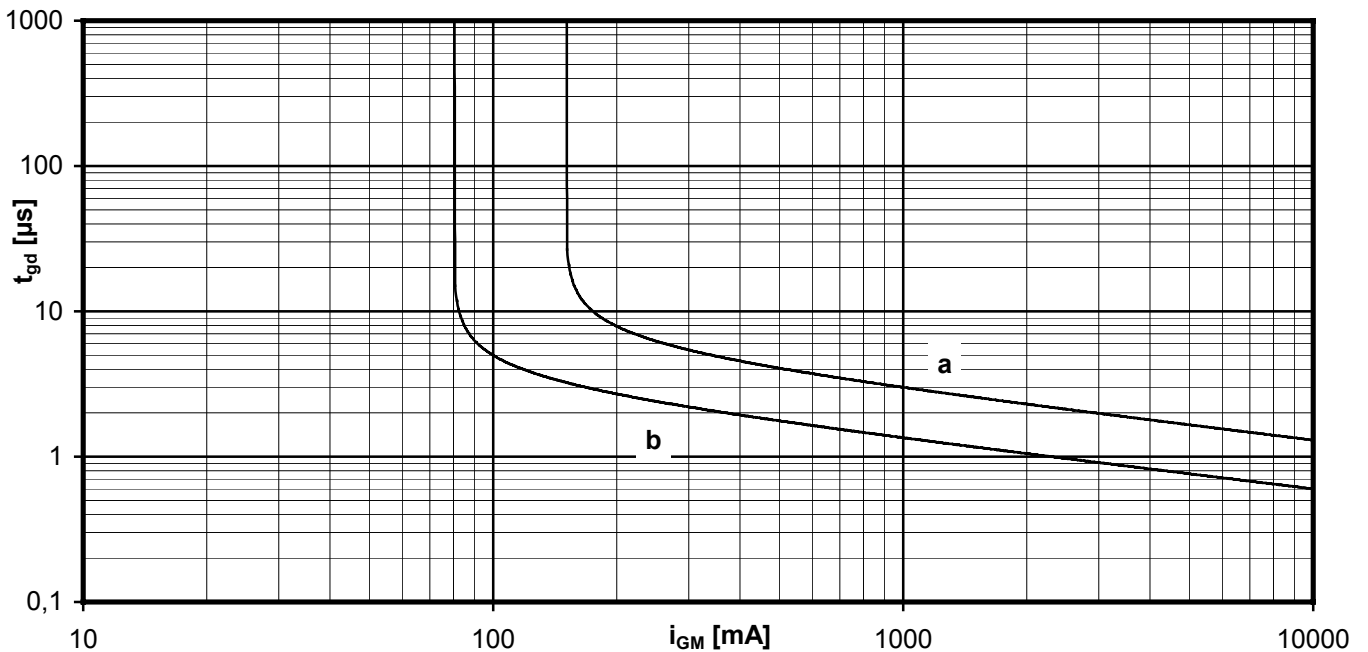
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Steuercharakteristik $v_G = f(i_G)$ mit Zündbereichen für $V_D = 6\text{ V}$
Gate characteristic $v_G = f(i_G)$ with triggering area for $V_D = 6\text{ V}$

Höchstzulässige Spitzensteuerverlustleistung / Maximum rated peak gate power dissipation $P_{GM} = f(t_g)$:

a - 40 W/10ms b - 80 W/1ms c - 100 W/0,5ms d - 150W/0,1ms

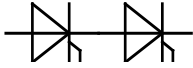


Zündverzögerung / Gate controlled delay time $t_{gd} = f(i_G)$

$T_{vj} = 25^\circ\text{C}$, $di_G/dt = i_{GM}/1\mu\text{s}$

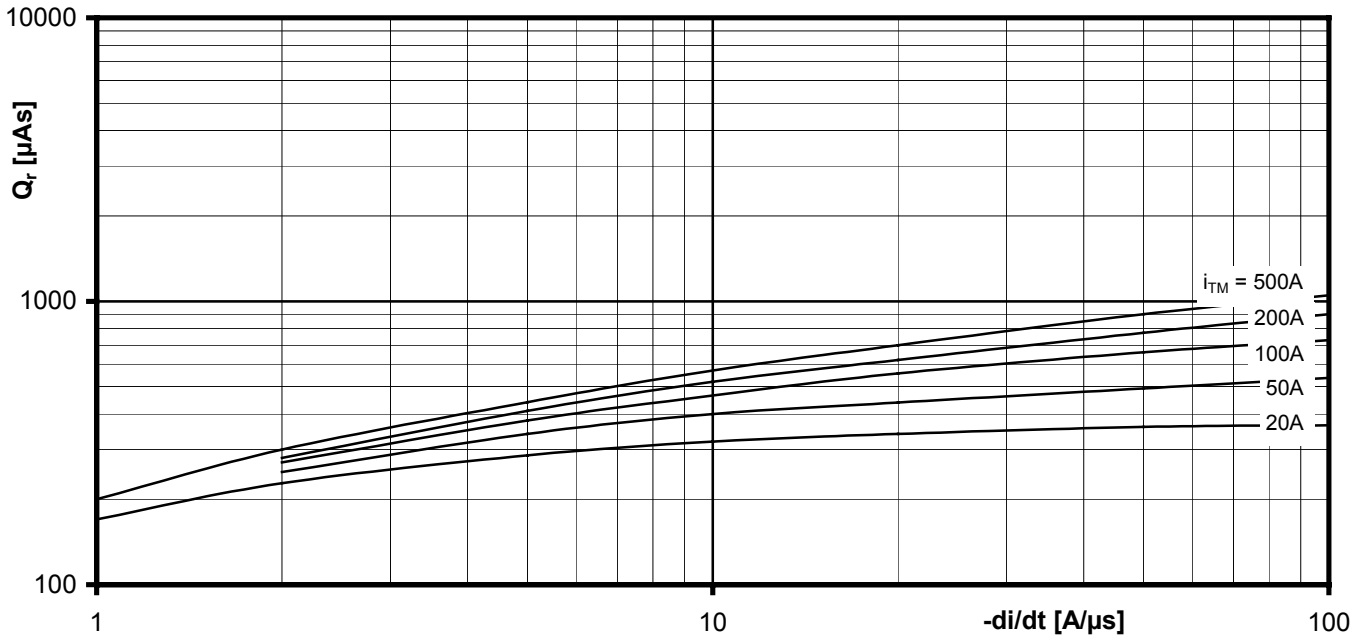
a - maximaler Verlauf / Limiting characteristic

b - typischer Verlauf / Typical characteristic



Netz-Thyristor-Modul
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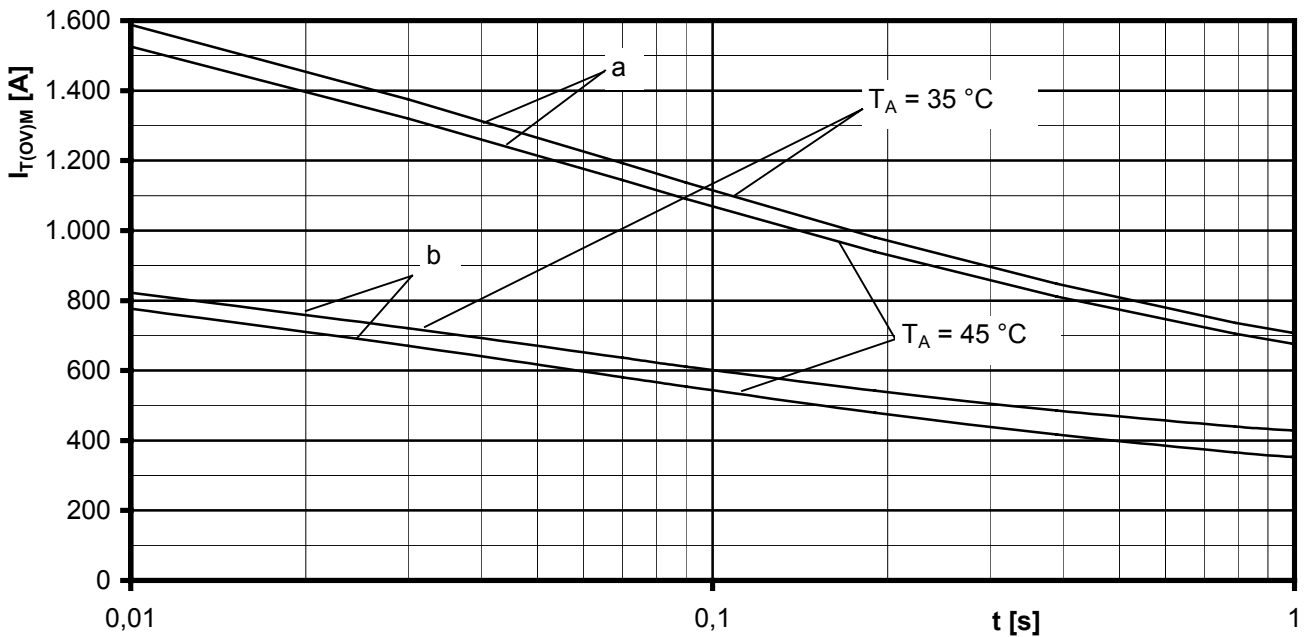
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Sperrverzögerungsladung / Recovered charge $Q_r = f(-di/dt)$

$$T_{vj} = T_{vjmax}, V_R \leq 0,5 V_{RRM}, V_{RM} = 0,8 V_{RRM}$$

Parameter: Durchlaßstrom / On-state current i_{TM}

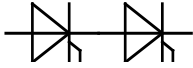


Grenzstrom / Maximum overload on-state current $I_{T(OV)M} = f(t), V_{RM} = 0,8 V_{RRM}$

Leerlauf / No-load conditions

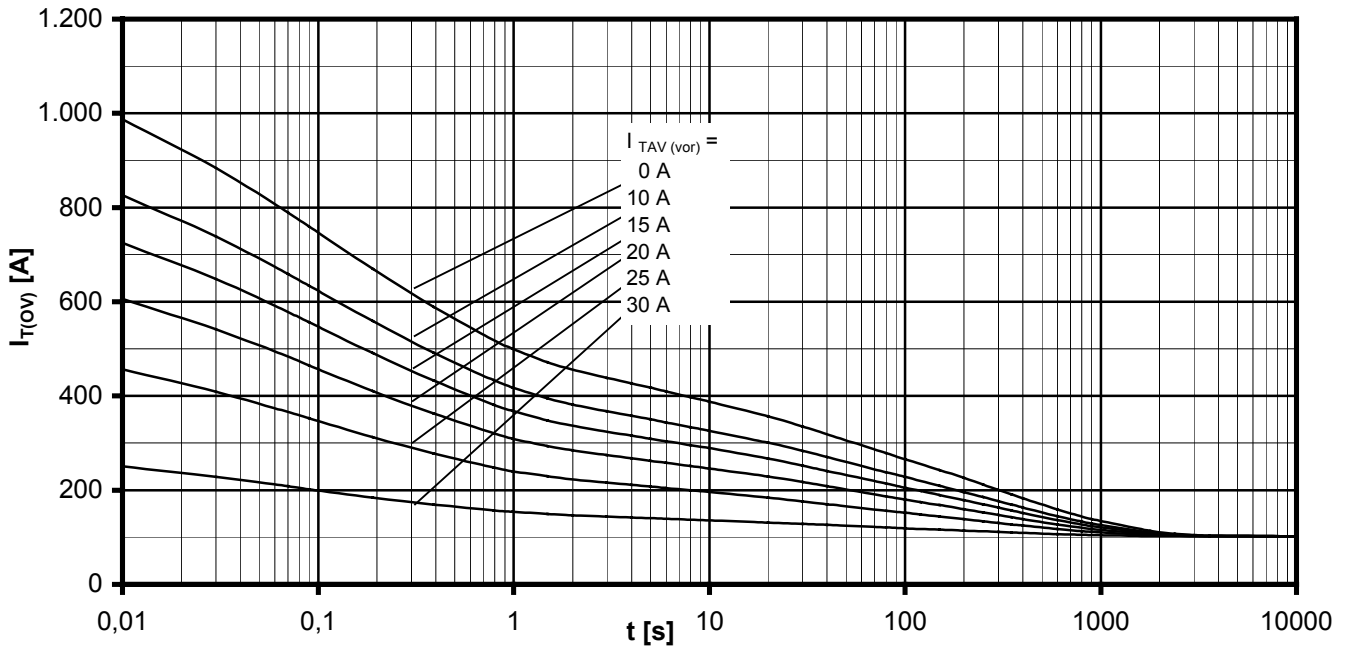
a: $T_A = 35^\circ\text{C}$, verstärkte Luftkühlung / Forced air cooling

b: $T_A = 45^\circ\text{C}$, Luftselbstkühlung / Natural air cooling



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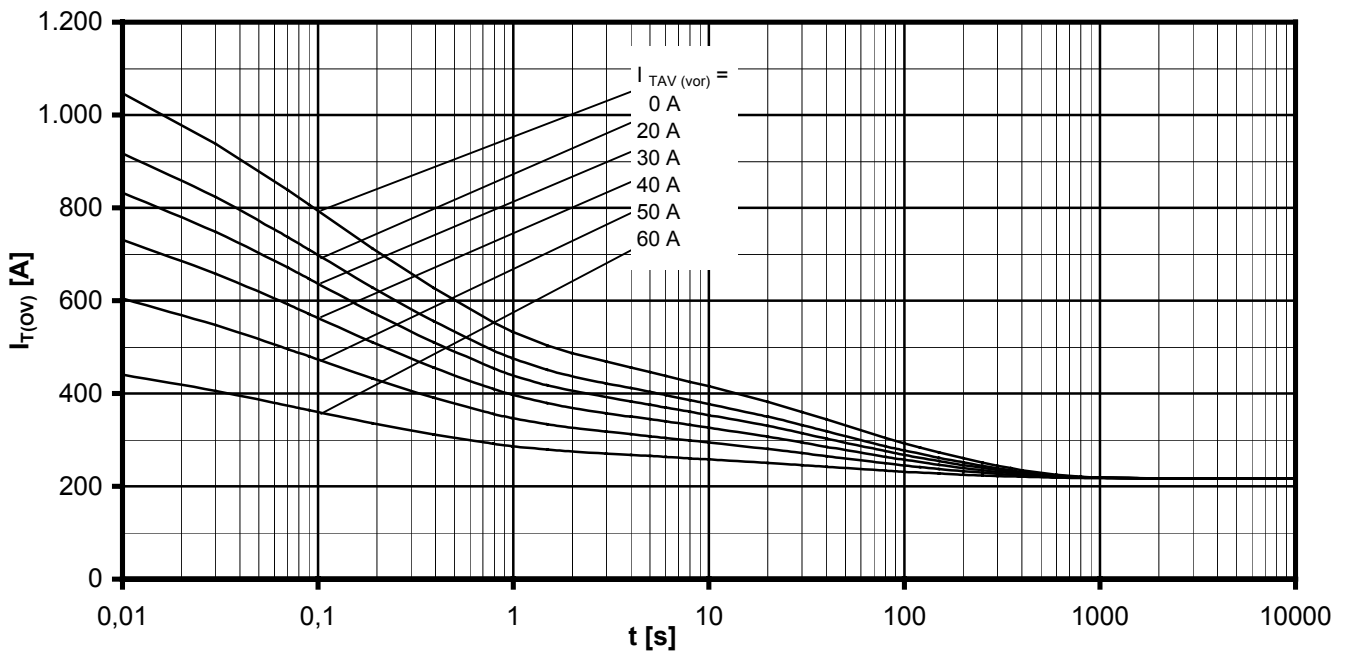


Überstrom je Zweig / Overload on-state current $I_{T(OV)}$

B6- Sechspuls-Brückenschaltung, 120° Rechteck / Six-pulse bridge circuit, 120° rectangular

Kühlkörper / Heatsink type KM14 (50W) Luftselbstkühlung bei / Natural cooling at $T_A = 45^\circ\text{C}$

Parameter: Vorlaststrom je Zweig / Pre-load current per arm $I_{TAV(vor)}$



Überstrom je Zweig / Overload on-state current $I_{T(OV)}$

B6- Sechspuls-Brückenschaltung, 120° Rechteck / Six-pulse bridge circuit, 120° rectangular

Kühlkörper / Heatsink type KM 14 (Papst 4650N) Verstärkte Kühlung bei / Forced cooling at $T_A = 35^\circ\text{C}$

Parameter: Vorlaststrom je Zweig / Pre-load current per arm $I_{TAV(vor)}$

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Warning

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