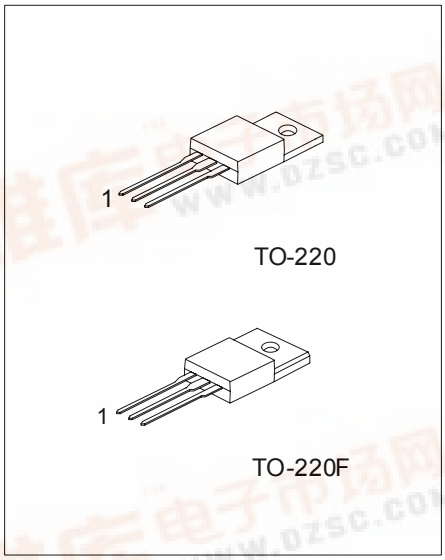
UF730

MOSFET

5.5A, 400V, 1.0 OHM, N-CHANNEL POWER MOSFET



*Pb-free plating product number: UF730L

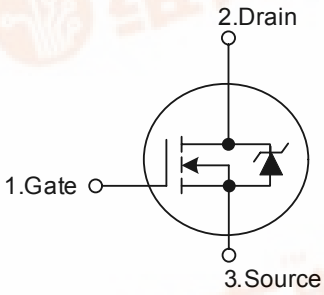
■ DESCRIPTION

The UF730 power MOSFET is designed for high voltage, high speed power switching applications such as switching power suppliess, switching adaptors.

■ FEATURES

* 5.5A, 400V, Low $R_{DS(ON)}$(1.0Ω)
* Single Pulse Avalanche Energy Rated
* Rugged - SOA is Power Dissipation Limited
* Fast Switching

■ SYMBOL



■ ORDERING INFORMATION

Order Number		Package	Pin Assignment			Packing
Normal	Lead Free Plating		1	2	3	
UF730-TA3-T	UF730L-TA3-T	TO-220	G	D	S	Tube
UF730-TF3-T	UF730L-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: GATE D: DRAIN S: SOURCE

UF730L-TA3-T (1)Packing Type (2)Package Type (3)Lead Plating	(1) T: Tube (2) TA3: TO-220, TF3: TO-220F (3) L: Lead Free Plating, Blank: Pb/Sn

■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless Otherwise Specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain to Source Voltage (T_J =25°C~125°C)		V_{DS}	400	V
Drain to Gate Voltage (R_{GS} = 20kΩ) (T_J =25°C~125°C)		V_{DGR}	400	V
Gate to Source Voltage		V_{GS}	±20	V
Drain Current	Continuous	I_D	6.5	A
	T_C = 100°C	I_D	3.5	A
	Pulsed	I_{DM}	22	A
Maximum Power Dissipation Derating above 25°C		P_D	93 0.6	W W/°C
Single Pulse Avalanche Energy Rating (V_{DD}=50V, starting T_J =25°C, L=17mH, R_G=25Ω, peak I_{AS} = 5.5A)		E_{AS}	300	mJ
Operating Temperature Range		T_{OPR}	-55 ~ +150	°C
Storage Temperature Range		T_{STG}	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Thermal Resistance Junction-Ambient	θ_{JA}	80	°C/W
Thermal Resistance Junction-Case	θ_{Jc}	1.67	

■ ELECTRICAL CHARACTERISTICS (T_C =25°C, Unless Otherwise Specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Drain to Source Breakdown Voltage	BV_{DSS}	I_D = 250μA, V_{GS} = 0V	400			V
Gate to Threshold Voltage	$V_{GS(THR)}$	V_{DS} = V_{GS}, I_D = 250μA	2.0		4.0	V
On-State Drain Current (Note 1)	$I_{D(ON)}$	V_{DS} > $I_{D(ON)}$ x $R_{DS(ON)MAX}$, V_{GS} = 10V	5.5			A
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = Rated BV_{DSS}, V_{GS} = 0V			25	μA
		V_{DS} = 0.8 x Rated BV_{DSS}, V_{GS} = 0V, T_J = 125°C			250	μA
Gate to Source Leakage Current	I_{GSS}	V_{GS} = ±20V			±100	nA
Drain to Source On Resistance (Note 1)	$R_{DS(ON)}$	I_D = 3.0A, V_{GS} = 10V		0.8	1.0	Ω
Forward Transconductance (Note 1)	g_{FS}	V_{DS} ≥ 10V, I_D = 3.3A	2.9	4.4		S
Turn-On Delay Time	$t_{DLY(ON)}$	V_{DD} = 200V, I_D ≈ 5.5A, R_{GS} = 12Ω, R_L = 35Ω MOSFET Switching Times are Essentially Independent of Operating Temperature		10	17	ns
Rise Time	t_R			20	29	ns
Turn-Off Delay Time	$t_{DLY(OFF)}$			35	56	ns
Fall Time	t_F			15	24	ns
Total Gate Charge (Gate to Source + Gate to Drain)	$Q_{G(TOT)}$	V_{GS} = 10V, I_D = 5.5A, V_{DS} = 0.8 x Rated BV_{DSS} $I_{G(REF)}$ = 1.5mA Gate Charge is Essentially Independent of Operating Temperature		20	35	nC
Gate to Source Charge	Q_{GS}			3.0		nC
Gate to Drain “Miller” Charge	Q_{GD}			10		nC
Input Capacitance	C_{ISS}	V_{DS} = 25V, V_{GS} = 0V,f = 1MHz		600		pF
Output Capacitance	C_{OSS}			150		pF
Reverse - Transfer Capacitance	C_{RSS}			40		pF

■ ELECTRICAL CHARACTERISTICS(Cont.)

SOURCE TO DRAIN DIODE SPECIFICATIONS						
Source to Drain Diode Voltage (Note 1)	V_{SD}	T_J = 25°C, I_{SD} = 5.5A, V_{GS} = 0V			1.6	V
Continuous Source to Drain Current	I_S				5.5	A
Pulse Source to Drain Current (Note 2)	I_{SM}				22	A
Reverse Recovery Time	t_{RR}	T_J = 25°C, I_{SD} = 5.5A, dI_{SD}/dt = 100A/μs	140	300	660	ns
Reverse Recovery Charge	Q_{RR}	T_J = 25°C, I_{SD} = 5.5A, dI_{SD}/dt = 100A/μs	0.93	2.1	4.3	μC

Notes: 1. Pulse Test: Pulse width≤≤300μs, Duty Cycle≤≤2%.
2. Repetitive rating: Pulse width limited by maximum junction temperature.
3. V_{DD} = 50V, starting T_J = 25°C, L = 17mH, R_G = 25Ω, peak I_{AS} = 5.5A.

■ TEST CIRCUITS AND WAVEFORMS

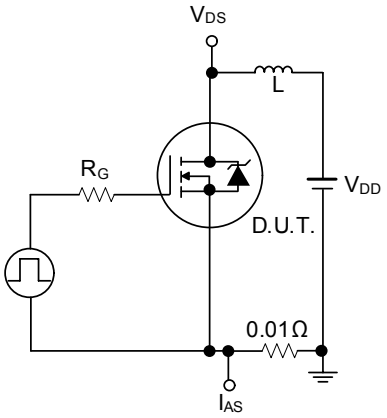


Figure 1A. Unclamped Energy Test Circuit

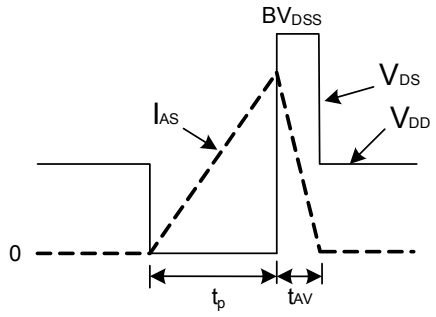


Figure 1B. Unclamped Energy Waveforms

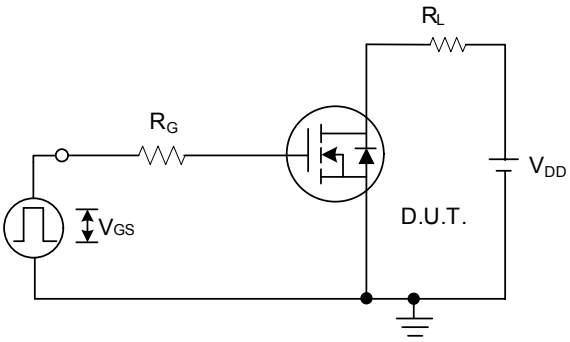


Figure 2A. Switching Time Test Circuit

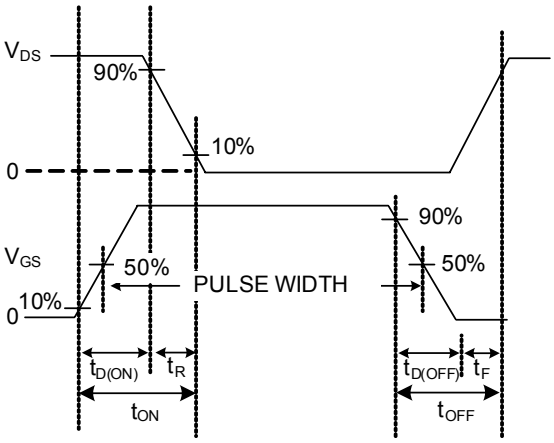


Figure 2B. Resistive Switching Waveforms

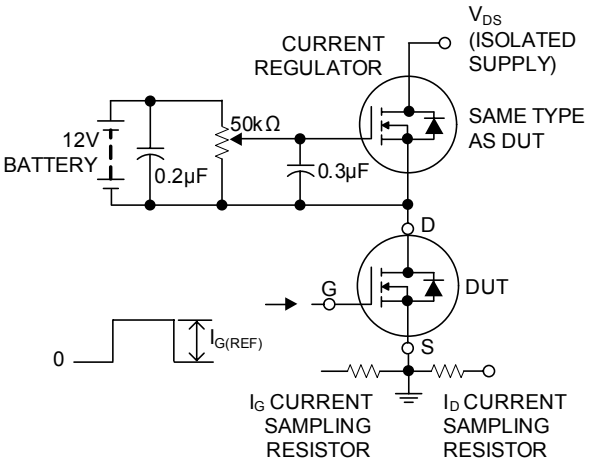


Figure 3A. Gate Charge Test Circuit

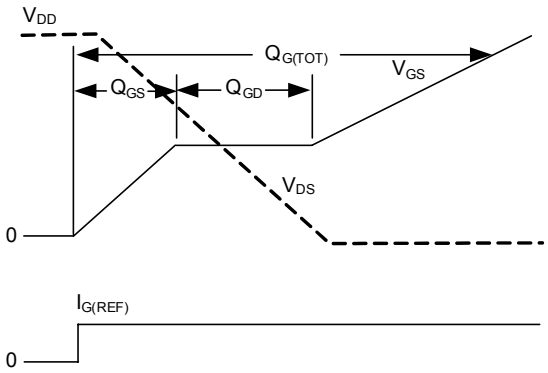
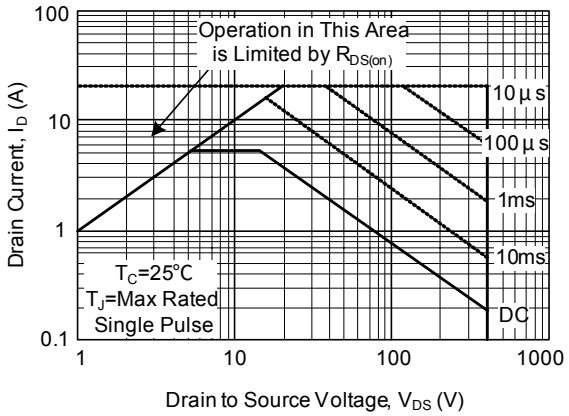


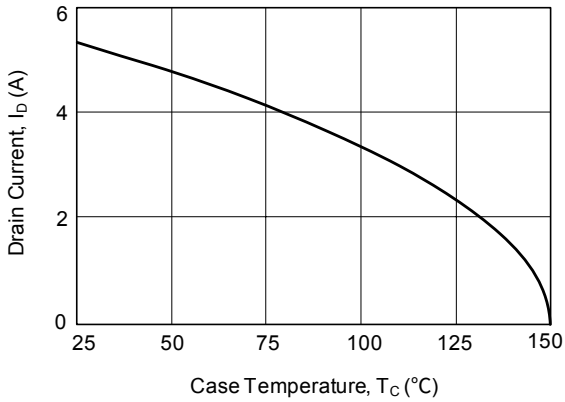
Figure 3B. Gate Charge Waveforms

■ TYPICAL PERFORMANCE CUVES (Unless Otherwise Specified)

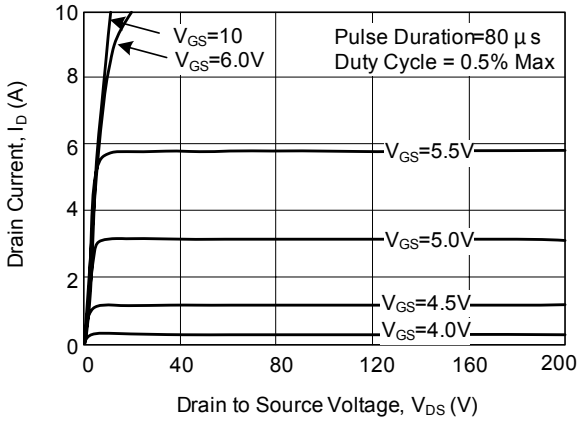
Forward Bias Safe Operating Area



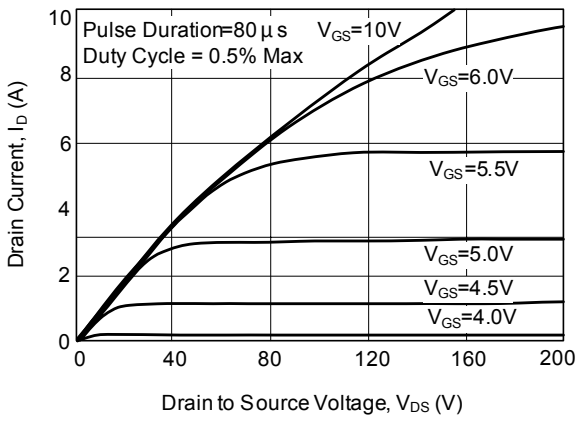
Maximum Contionuous Drain Current vs. Case Temperature



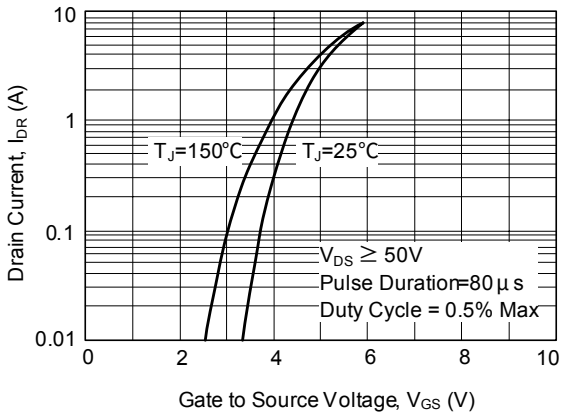
Output Characteristics



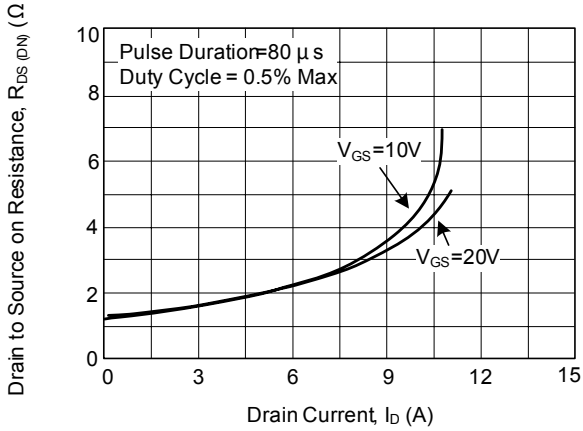
Sturation Characteristics



Transfer Characteristics

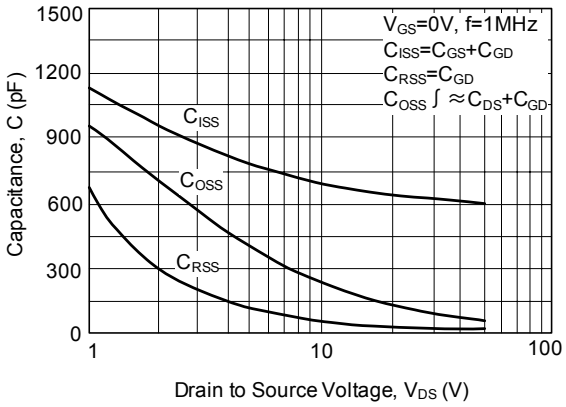


Drain to Source on Resistance vs. Gate Voltage and Drain Current

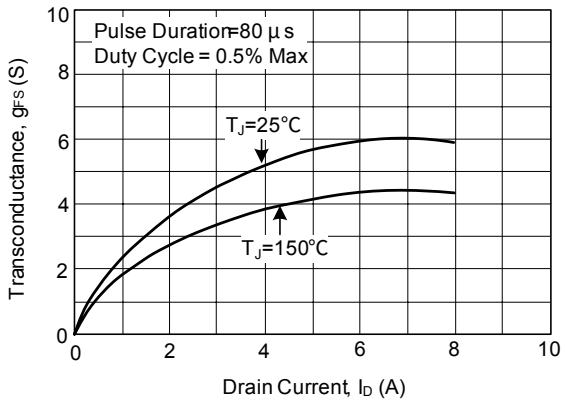


■ TYPICAL PERFORMANCE CUVES

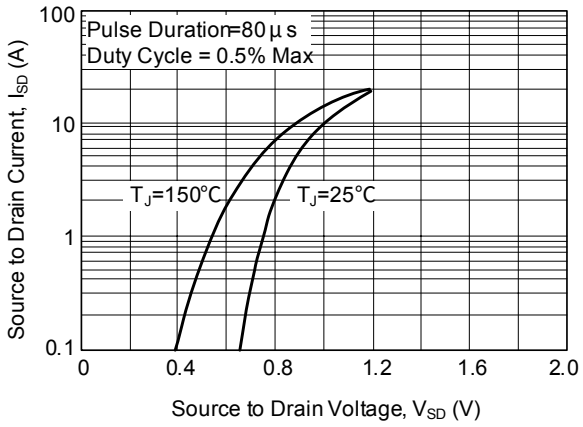
Capacitance vs. Drain to Source Voltage



Transconductance vs. Drain Current



Source to Drain Diode Voltage



Gate to Source Voltage vs. Gate Charge