



NPN SILICON HIGH FREQUENCY TRANSISTOR

UPA812T

FEATURES

- **SMALL PACKAGE STYLE:**
2 NE681 Die in a 2 mm x 1.25 mm package
- **LOW NOISE FIGURE:**
NF = 1.4 dB TYP at 1 GHz
- **HIGH GAIN:**
 $IS_{21EI}^2 = 12$ dB TYP at 1 GHz
- **HIGH GAIN BANDWIDTH:** $f_T = 7$ GHz
- **LOW CURRENT OPERATION**

DESCRIPTION

NEC's UPA812T is two NPN high frequency silicon epitaxial transistors encapsulated in an ultra small 6 pin SMT package. Each transistor is independently mounted and easily configured for either dual transistor or cascode operation. The high f_T , low voltage bias and small size make this device suited for various hand-held wireless applications.

ABSOLUTE MAXIMUM RATINGS¹ ($T_A = 25^\circ\text{C}$)

SYMBOLS	PARAMETERS	UNITS	RATINGS
V_{CBO}	Collector to Base Voltage	V	20
V_{CEO}	Collector to Emitter Voltage	V	10
V_{EBO}	Emitter to Base Voltage	V	1.5
I_C	Collector Current	mA	65
PT	Total Power Dissipation		
	1 Die	mW	110
	2 Die	mW	200
T_J	Junction Temperature	$^\circ\text{C}$	150
T_{STG}	Storage Temperature	$^\circ\text{C}$	-65 to +150

Note: 1. Operation in excess of any one of these parameters may result in permanent damage.

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

PART NUMBER PACKAGE OUTLINE			UPA812T S06		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
I_{CBO}	Collector Cutoff Current at $V_{CB} = 10$ V, $I_E = 0$	μA			0.8
I_{EBO}	Emitter Cutoff Current at $V_{EB} = 1$ V, $I_C = 0$	μA			0.8
h_{FE}^1	Forward Current Gain at $V_{CE} = 3$ V, $I_C = 7$ mA		70	100	240
f_T	Gain Bandwidth at $V_{CE} = 3$ V, $I_C = 7$ mA, $f = 1$ GHz	GHz	4.5	7.0	
C_{re}^2	Feedback Capacitance at $V_{CB} = 3$ V, $I_E = 0$, $f = 1$ MHz	pF			0.9
IS_{21EI}^2	Insertion Power Gain at $V_{CE} = 3$ V, $I_C = 7$ mA, $f = 1$ GHz	dB	10	12	
NF	Noise Figure at $V_{CE} = 3$ V, $I_C = 7$ mA, $f = 1$ GHz	dB		1.4	1.7
h_{FE1}/h_{FE2}	h_{FE} Ratio: h_{FE1} = Smaller Value of Q1, or Q2 h_{FE2} = Larger Value of Q1 or Q2		0.85		

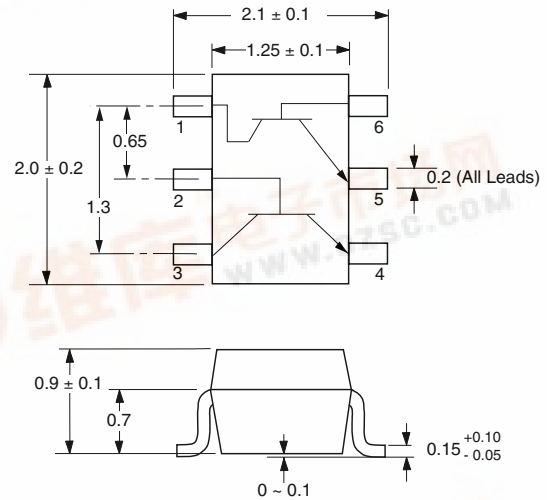
Notes: 1. Pulsed measurement, pulse width ≤ 350 μs , duty cycle $\leq 2\%$.

2. The emitter terminal should be connected to the ground terminal of the 3 terminal capacitance bridge.

For Tape and Reel version use part number UPA812T-T1, 3K per reel.

OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S06
(Top View)



PIN OUT

1. Collector Transistor 1
2. Base Transistor 2
3. Collector Transistor 2
4. Emitter Transistor 2
5. Emitter Transistor 1
6. Base Transistor 1

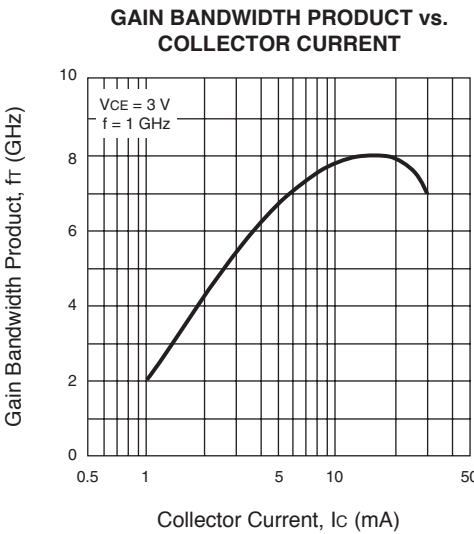
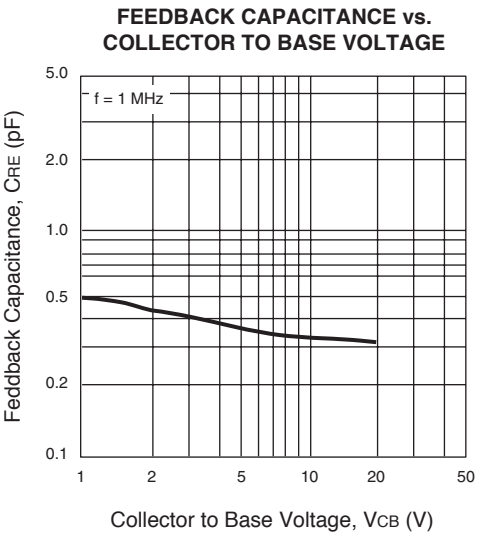
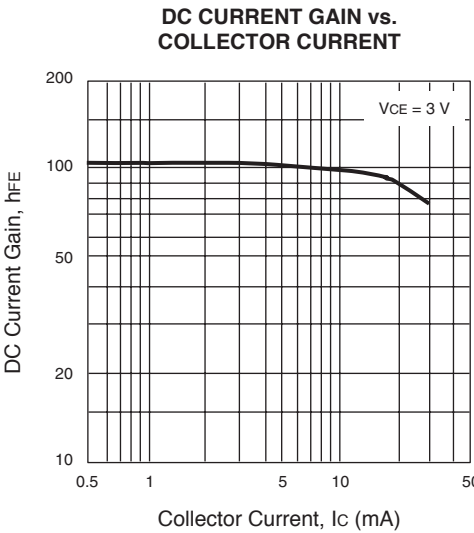
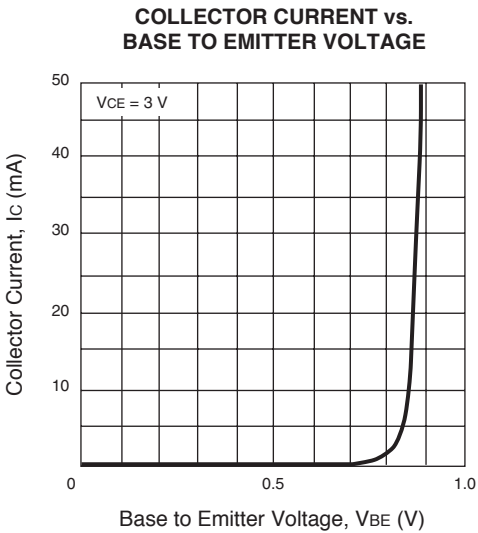
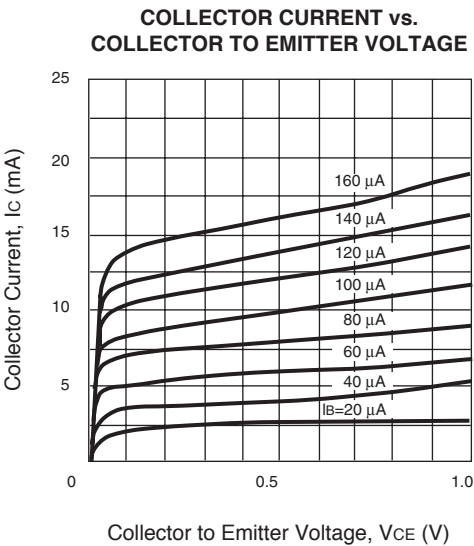
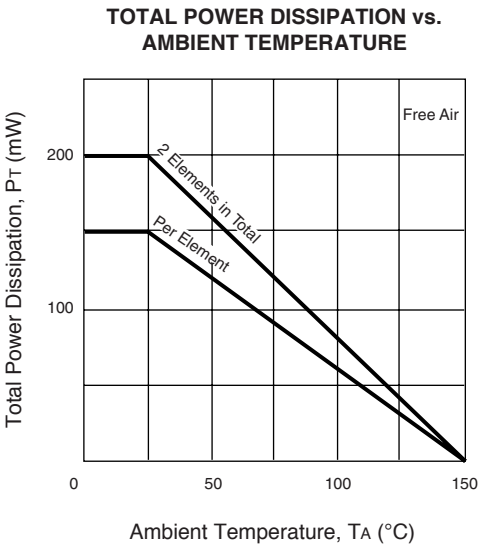
Note:

Pin 3 is identified with a circle on the bottom of the package.

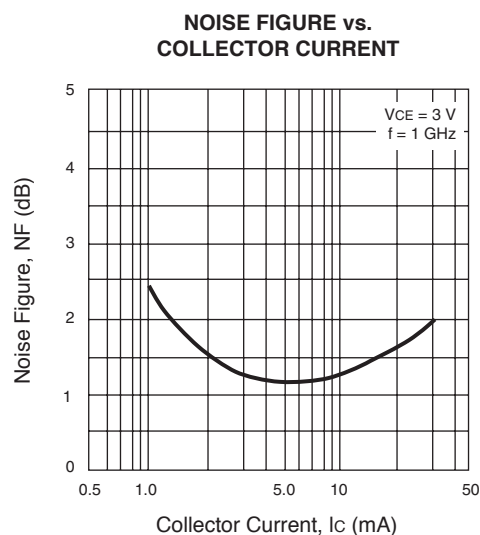
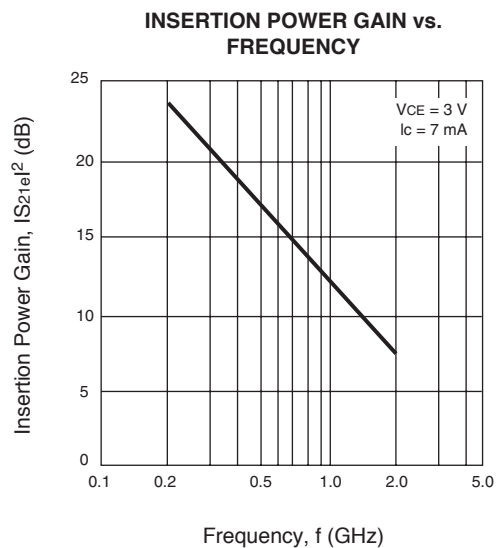
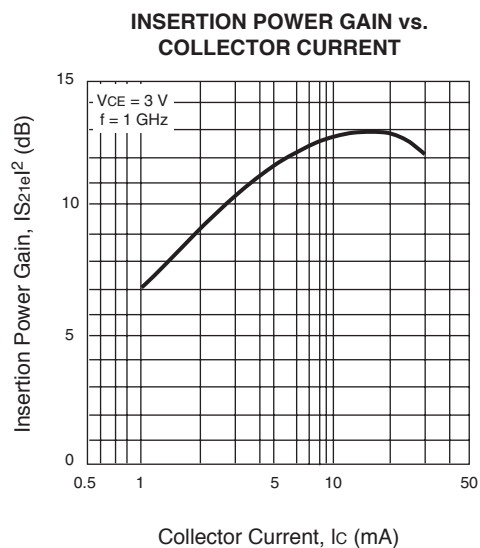


UPA812T

TYPICAL PERFORMANCE CURVES (T_A = 25°C)



TYPICAL PERFORMANCE CURVES ($T_A = 25^\circ\text{C}$)



ORDERING INFORMATION

PART NUMBER	QUANTITY	PACKAGING
UPA812T-T1-A	3000	Tape & Reel

Life Support Applications

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CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices	
		-A	-AZ
Lead (Pb)	< 1000 PPM	Not Detected	(*)
Mercury	< 1000 PPM	Not Detected	
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not Detected	
PBB	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not Detected	

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