

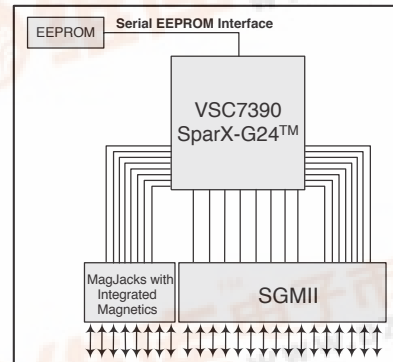
VSC7390

VITESSE®

24-Port Integrated Layer-2 Gigabit Ethernet Smart Switch with Transceivers



24-PORT WEB-MANAGED SMB SWITCH:



FEATURES:

- ▶ 24 Gigabit Ethernet Ports with Non-blocking Wire-speed Performance
- ▶ 8 Integrated Tri-speed (10/100/1000 Mbps) Copper Transceivers (IEEE 802.3ab™ compliant) and 16 Tri-speed SGMII Ports
- ▶ 500 kb On-chip Frame Buffer
- ▶ Per-port Data Rate and Shaper Policer with a Granularity of 64 kbps for Both Ingress and Egress Direction
- ▶ Selection Between On-chip V-Core CPU or Off-chip 8-bit or 16-bit CPU for Fully Managed Applications
- ▶ DSCP Remarking for Both IPv4 and IPv6 Frames
- ▶ Jumbo Frame Support on all Ports
- ▶ Programmable Classifier for QoS, Including IEEE Std 802.1p™ and DSCP for IPv4 and IPv6, with 4 Classes of Service
- ▶ 8 k MAC Addresses and 4 k VLANs (IEEE Std 802.1Q™)
- ▶ Flexible Link Aggregation Compliant to IEEE Std 802.3ad™
- ▶ Full-duplex Flow Control (IEEE Std 802.3x™) and Half-duplex Back Pressure, Symmetric and Asymmetric
- ▶ Multicast and Broadcast Storm Control as well as Flooding Control
- ▶ Vitesse VeriPHY™ Cable Diagnostics
- ▶ Multiple Protocol Support: IEEE Std 802.1D™, IEEE Std 802.1w™, IEEE Std 802.1s™ and IEEE Std 802.1X™

BENEFITS:

- ▶ Ensures Maximum Data Throughput on Even Highly Loaded Networks
- ▶ Reduces 8 External PHYs Thereby Significantly Reducing the Overall Systems Cost – by Only Needing 2 External Octal PHYs
- ▶ Eliminates the Need for External Buffer Memory While Maintaining High Peak Load Performance
- ▶ Enables Pre-arranged Bandwidth Control and Billing
- ▶ Enables Advanced Switch Management Using Run-time Protocols, in Addition to Low Cost Unmanaged and Web-managed Operation
- ▶ Enables LAN Edge Products to Generate Standard Priority which can be Used in the Core Network
- ▶ Provides the Support of Frame Sizes up to 12.2 k Sized Jumbo Frames
- ▶ Optimizes Network Performance to Support any Digital Stream, from Internet Browsing to VoIP and Video
- ▶ Stores a Large Amount of Stations at Wire Speed in the On-chip MAC Table, Ensuring Optimal Network Performance
- ▶ Provides High Bandwidth Uplinking Abilities
- ▶ Ensures Loss-less Data Transmission while Supporting Existing Legacy Ethernet Equipment
- ▶ Eliminates Unwanted Flooding of Ports and Thereby Increases Overall Network Performance
- ▶ Enables Sophisticated Real Time Cable Diagnostics
- ▶ Supports Complex Network Topologies with Fast Failure Recovery and Access Control

APPLICATIONS:

- ▶ 24-Port Unmanaged Gigabit Ethernet SMB/SME/SOHO Switch
- ▶ 24-Port Web-Managed Smart Gigabit Ethernet SMB/SME/SOHO Switch



VSC7390

24-Port Integrated Layer-2 Gigabit Ethernet Smart Switch with Transceivers

GENERAL DESCRIPTION:



VSC7390 SparX-G24™ is the highly integrated, single-chip Gigabit Ethernet switch with 8 Integrated GbE Copper PHY ports. As part of the Vitesse SparX™ Family of Switches, SparX-G24 delivers high performance with easy migration paths, and comprehensive system solutions that enable ultra-short time-to-market and market-leading system cost.

A Vitesse Switch Family Member

SparX-G24 is a part of the Vitesse SparX™ Family of Unmanaged or Web-Managed Switches and takes the family into a new generation of integration. With its 8 tri-speed Gigabit Ethernet copper ports and 16 SGMII ports, it is specifically targeted at SMB, SME and SOHO unmanaged applications, and with its on-chip V-Core CPU and parallel CPU interface it enables web-managed and managed switch applications.

Being a member of the SparX™ switch family provides a number of unique advantages when designing with the device, including software compatibility between members. Software written for SparX-G24 can therefore easily be ported from or used with other switch configurations in the family, and vice-versa.

Additionally, building a product family is easy when designing with Vitesse switches. Upgrading designs to higher port counts, or migrating to other configurations or future technology is achievable with a minimum of effort and time.

The Vitesse SparX Family Of Switches

SparX-G24, as part of the Vitesse SparX Family of Switches, shares the unique traits of the family.

The highly integrated switches require no external memory, and the integration of copper PHY as well as internal oscillator circuits simplifies the design effort. The requirement of needing only 2 power supply rails reduces the additional DC/DC converter cost. Further trimming down system cost is the persistently low power dissipation of the family. In addition, the value-added features in SparX-G24 enables the customer to provide advanced features without the need of enterprise class silicon.

SPECIFICATIONS:

- ▶ 25-MHz, 125-MHz LVTTTL Reference Clock
- ▶ 3.3 V Interface Power Supply
- ▶ 1.2 V Core Power Supply
- ▶ Thermally Enhanced PBGA Package

The switches provide non-blocking, wire-speed performance designs with wire-speed MAC address learning or CPU-based address learning.

Designing both unmanaged and managed solutions is straightforward. The devices offer both an internal V-Core CPU and serial and parallel CPU interfaces, and support a host of management protocols such as GxRP, SNMP, and three flavors of Spanning Tree: Standard STP, Multiple STP, and Rapid STP.

Controlling networks operated by the Vitesse switches is simple; advanced classification with IPv4 and IPv6 support, along with port DSCP remarking and 802.1x authentication support, provide flexible QoS operation. This is backed up by Layer 2-4 link aggregation and VLAN support, enabling managed switches to be deployed.

With its unique low power dissipation, SparX-G24 reduces thermal management cost for system design and thereby provides for higher system reliability to the benefit of the user.

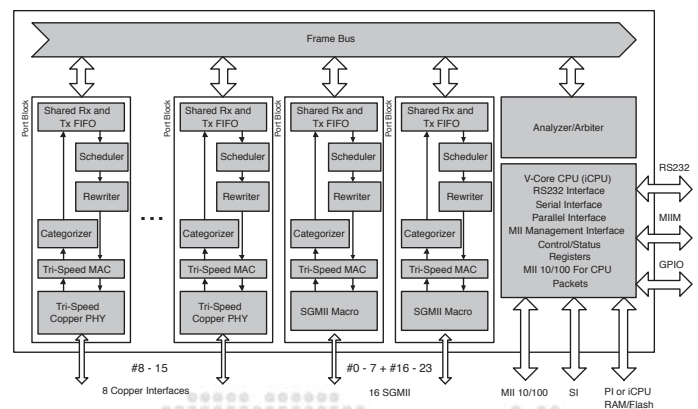
Designing with Vitesse SparX Family of Switches means designing with the whole Vitesse Solution:

The outstanding Vitesse web-managed switch software system that runs on the on-chip V-Core CPU is provided. This is in addition to the unmanaged and smart managed software systems for an external 8051, which are also included. For homegrown software development our full platform and OS independent switch API is supplied.

Accompanying the software systems are the Vitesse Switch Reference Systems. These are production-ready hardware designs that also serve as evaluation platforms for our switches.

The Vitesse Solution is supplied with a comprehensive collateral package, which includes software source code, full documentation, and an in-depth qualification report for easy pre-evaluation.

BLOCK DIAGRAM:



Trademarks™

Vitesse, ASIC-Friendly, FibreTimer, TimeStream, Snoop Loop, Super FEC, FOCUSConnect, Meigs-II, Meigs-IIIe, Lansing, Campbell-I, Barrington, PaceMaker, HOVCAT48, HOVCAT48e, HOVCAT192, HOVCAT192e, Micro PHY, FOCUS32, FOCUS16, IQ2200, VersaCAT, GigaStream, HawX, SparX, StaX, VstaX, SimpliPHY, VeriPHY, ActiPHY, XFP PRO, SFP PRO, Smart-LINK, OctalMAC, EQ Technology are trademarks in the United States and/or other jurisdictions of Vitesse Semiconductor Corporation. All other trademarks or registered trademarks mentioned herein are the property of their respective holders.

Copyright © 2005

Vitesse Semiconductor Corporation ("Vitesse") retains the right to make changes to its products or specifications to improve performance, reliability or manufacturability. All information in this document, including descriptions of features, functions, performance, technical specifications and availability, is subject to change without notice at any time. While the information furnished herein is held to be accurate and reliable, no responsibility will be assumed by Vitesse for its use. Furthermore, the information contained

741 Calle Plano
Camarillo, CA 93012, USA

Tel: +1 805.388.3700

Fax: +1 805.987.5896

www.vitesse.com

sales@vitesse.com