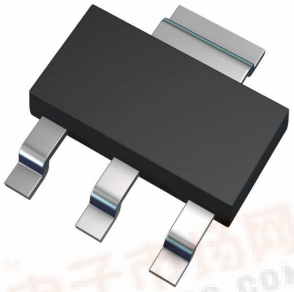
ZXTP19060CG
60V PNP medium transistor in SOT223



Summary

BV_{CEO} > -60V

BV_{ECO} > -7V

$I_{C(cont)}$ = 5A

$V_{CE(sat)}$ < -80mV @ -1A

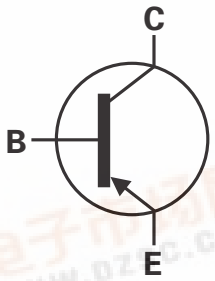
$R_{CE(sat)}$ = 50mΩ

P_D = 3.0W

Complementary part number ZXTN19060CG

Description

Packaged in the SOT223 outline this new low saturation PNP transistor offers extremely low on state losses making it ideal for use in DC-DC circuits and various driving and power management functions.

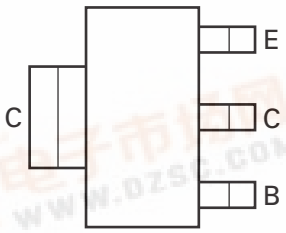


Features

- High Gain
- Low saturation voltage
- High peak current
- 7V reverse blocking voltage

Applications

- High side driver
- Motor drive
- Load disconnect switch



Pinout - top view

Ordering information

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTP19060CGTA	7	12	1000

Device marking

ZXTP19060C

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Collector-Base voltage	V_{CBO}	-60	V
Collector-Emitter voltage	V_{CEO}	-60	V
Emitter-Collector voltage (reverse blocking)	V_{ECX}	-7	V
Emitter-Base voltage	V_{EBO}	-7	V
Continuous Collector current[c]	I_C	-5	A
Base current	I_B	-1	A
Peak pulse current	I_{CM}	-7	A
Power dissipation at T_A =25°C[a] Linear derating factor	P_D	1.2 9.6	W mW/°C
Power dissipation at T_A =25°C[b] Linear derating factor	P_D	1.6 12.8	W mW/°C
Power dissipation at T_A =25°C[c] Linear derating factor	P_D	3.0 24	W mW/°C
Power dissipation at T_A =25°C[d] Linear derating factor	P_D	5.3 42	W mW/°C
Power dissipation at T_C =25°C[e] Linear derating factor	P_D	10.2 81	W mW/°C
Operating and storage temperature range	T_j, T_{stg}	-55 to 150	°C

Thermal resistance

Parameter	Symbol	Limit	Unit
Junction to ambient[a]	$R_{\Theta JA}$	104	°C/W
Junction to ambient[b]	$R_{\Theta JA}$	78	°C/W
Junction to ambient[c]	$R_{\Theta JA}$	42	°C/W
Junction to ambient[d]	$R_{\Theta JA}$	23.5	°C/W
Junction to case[e]	$R_{\Theta JC}$	12.3	°C/W

NOTES:

(a) For a device surface mounted on 15mm x 15mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

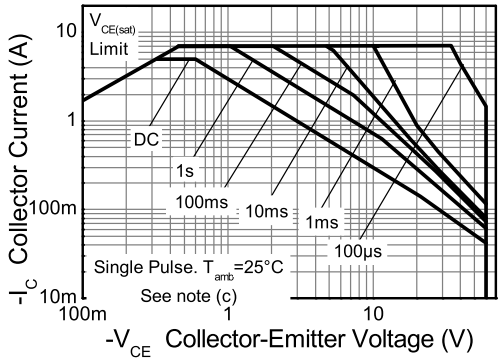
(b) Mounted on 25mm x 25mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

(c) Mounted on 50mm x 50mm x 0.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.

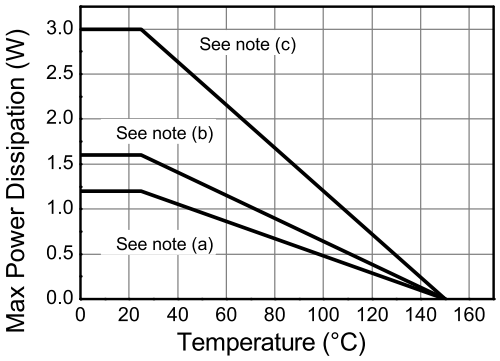
(d) As (c) above measured at t<5 seconds.

(e) Junction to case (collector tab). Typical

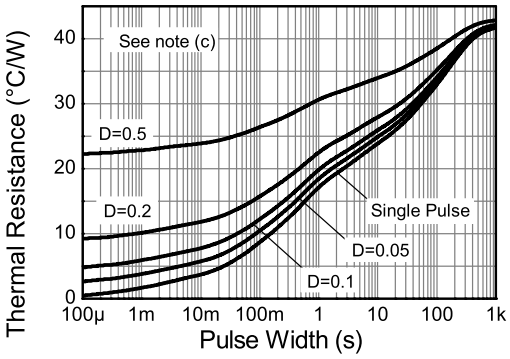
Thermal characteristics



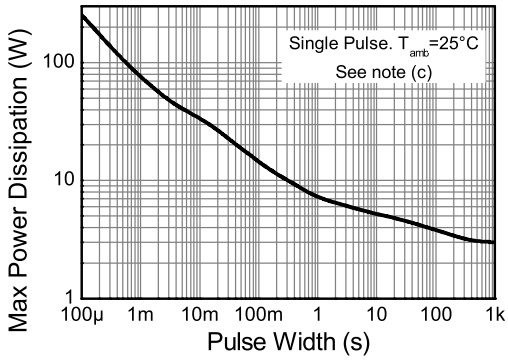
Safe Operating Area



Derating Curve



Transient Thermal Impedance



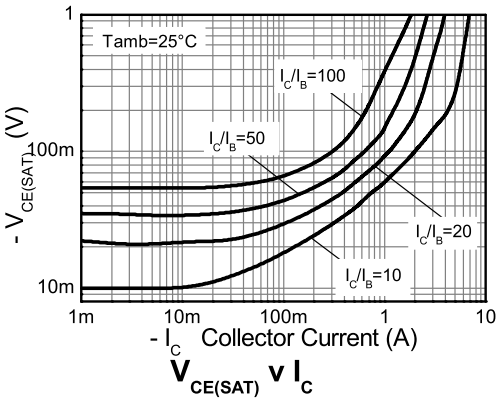
Pulse Power Dissipation

Electrical characteristics (at T_{amb} = 25°C unless otherwise stated)

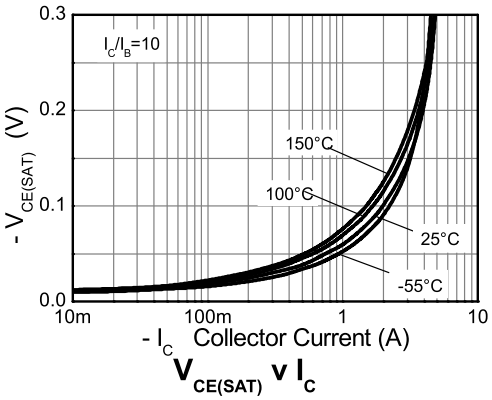
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-Base breakdown voltage	BV_{CBO}	-60	-110		V	I_C = -100μA
Collector-Emitter breakdown voltage	BV_{CEO}	-260	-90		V	I_C= -10mA (*)
Emitter-Collector breakdown voltage (reverse blocking)	BV_{ECX}	-7	-8.4		V	I_E = -100μA, R_{BC} < 1kΩ or 0.25V > V_{BC} > -0.25V
Emitter-Collector breakdown voltage (reverse blocking)	BV_{ECO}	-7	-8.8		V	I_E = -100μA
Emitter-Base breakdown voltage	BV_{EBO}	-7	-8.4		V	I_E = -100μA
Collector-Base cut-off current	I_{CBO}		<1	-50 -0.5	nA μA	V_{CB} = -60V V_{CB} = -60V, T_{amb}=100°C
Emitter cut-off current	I_{EBO}		<1	-50	nA	V_{EB} = -5.6V
Collector-Emitter saturation voltage	$V_{CE(sat)}$		-62 -145 -500 -105 -145 -300	-80 -205 -750 -165 -200 -500	mV mV mV mV mV mV	I_C = -1A, I_B = -100mA(*) I_C = -1A, I_B = -20mA(*) I_C = -2A, I_B = -40mA(*) I_C = -2A, I_B = -200mA(*) I_C = -3A, I_B = -300mA(*) I_C = -5A, I_B = -500mA(*)
Base-Emitter saturation voltage	$V_{BE(sat)}$		-975	-1050	mV	I_C = -5A, I_B = -500mA(*)
Base-Emitter turn-on voltage	$V_{BE(on)}$		-890	-1000	mV	I_C = -5A, V_{CE} = -2V(*)
Static forward current transfer ratio	h_{FE}	200 160 20	330 260 40	500		I_C = -100mA, V_{CE} = -2V(*) I_C = -1A, V_{CE} = -2V(*) I_C = -5A, V_{CE} = -2V(*)
Transition frequency	f_T		180		MHz	I_C = -50mA, V_{CE} = -10V f = 50MHz
Input capacitance	C_{ibo}		280	400	pF	V_{EB} = -0.5V, f = 1MHz(*)
Output capacitance	C_{obo}		29.5	40	pF	V_{CB} = -10V, f = 1MHz(*)
Delay time	t_d		24.3		ns	I_C = -500mA, V_{CC} = -10V, I_{B1} = -I_{B2} = -50mA
Rise time	t_r		13.2		ns	
Storage time	t_s		456		ns	
Fall time	t_f		68.2		ns	

NOTES:
(*) Measured under pulsed conditions. Pulse width ≤ 300μs; duty cycle ≤ 2%.

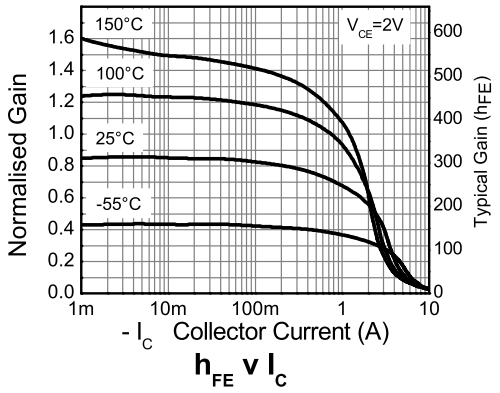
Typical characteristics



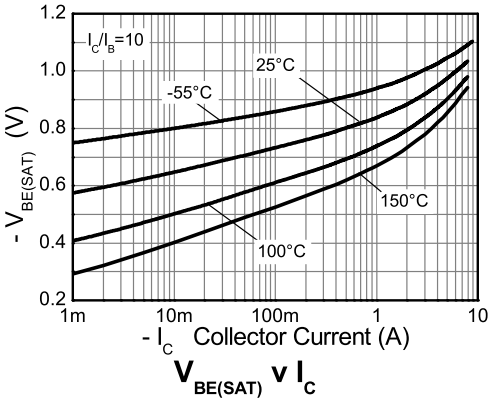
$V_{CE(SAT)}$ v I_C



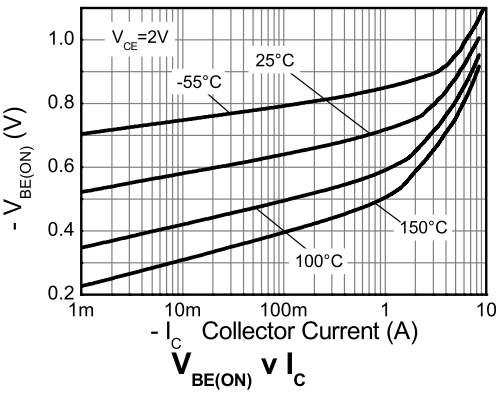
$V_{CE(SAT)}$ v I_C



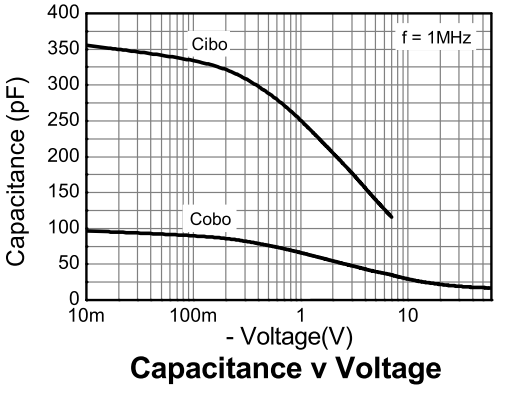
h_{FE} v I_C



$V_{BE(SAT)}$ v I_C

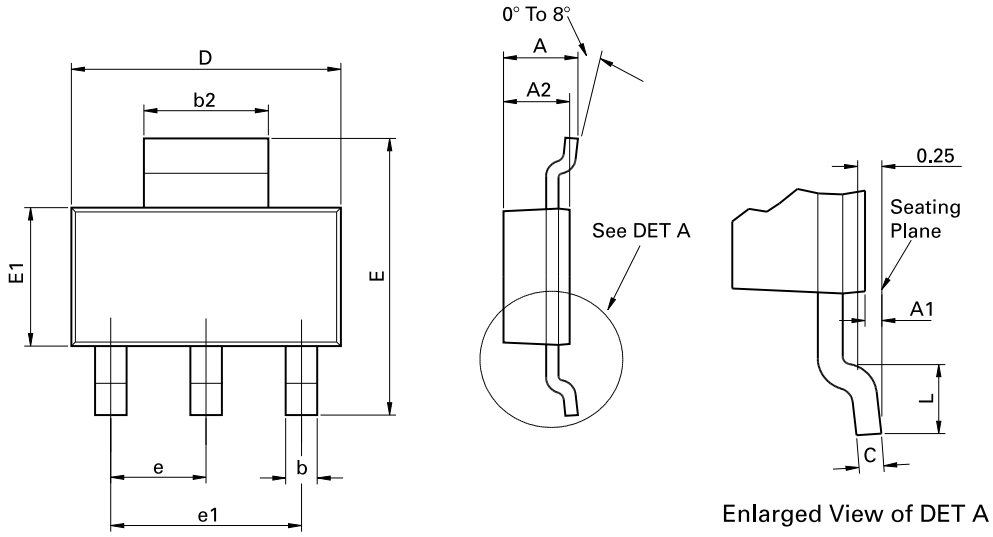


$V_{BE(ON)}$ v I_C



Capacitance v Voltage

Package outline - SOT223



Conforms to JEDEC TO-261 AA Issue B

Dim.	Millimeters		Inches		Dim.	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	-	1.80	-	0.071	D	6.30	6.70	0.248	0.264
A1	0.02	0.10	0.0008	0.004	e	2.30 BSC		0.0905 BSC	
A2	1.55	1.65	0.0610	0.0649	e1	4.60 BSC		0.181 BSC	
b	0.66	0.84	0.026	0.033	E	6.70	7.30	0.264	0.287
b2	2.90	3.10	0.114	0.122	E1	3.30	3.70	0.130	0.146
C	0.23	0.33	0.009	0.013	L	0.90	-	0.355	-

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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