



捷多邦,专业PCB打样工厂,24小时加急出货

SURFACE MOUNT SCHOTTKY BARRIER DIODE

DZSG.COM

SOD-123

Min

3.55

2.55

1.40

0.45

0.25

_

0°

All Dimensions in mm

0.55 Typical

0.11 Typical

Max

3.85

2.85

1.70

1.35

0.65

0.10

8°

Dim

Α

В

С

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Features

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Fast Switching Time
- Low Reverse Capacitance
- Surface Mount Package Ideally Suited for Automatic Insertion
- Lead Free/RoHS Compliant (Note 3)

Mechanical Data

- Case: SOD-123
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe)
- Polarity: Cathode Band
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.01 grams (approximate)

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{rrm} V _{rwm} V _r	60	SC.CVM		
RMS Reverse Voltage	V _{R(RMS)}	42	V		
Forward Continuous Current	IF.	15	mA		
Non-Repetitive Peak Forward Surge Current @ t \leq 1.0s @ t = 10ms	I _{FSM}	50 2.0	mA A		
Power Dissipation (Note 1)	PD	333	mW		
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{ ext{ heta}JA}$	300	°C/W		
Operating Temperature Range	Tj	-55 to +125	°C		
Storage Temperature Range	T _{STG}	-55 to +150	°C		

F- TE

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
Reverse Breakdown Voltage (Note 2)	V _{(BR)R}	60	-	-	V	$I_R = 10 \mu A$	
Reverse Leakage Current (Note 2)	I _{RM}	_	-	200	nA	$V_R = 50V$	
Forward Voltage Drop	V _{FM}	372		0.41	V	$I_F = 1.0 \text{mA}$	
Torward Voltage Drop	V FM			1.0		$I_F = 15 mA$	
Total Capacitance	Ст			2.2	pF	$V_{R} = 0V, f = 1.0MHz$	
Reverse Recovery Time				1.0		$I_{F} = I_{R} = 5.0 \text{mA}$	
	L'IL		_	1.0	ns	$I_{rr} = 0.1 \times I_{R}, R_{L} = 100\Omega$	

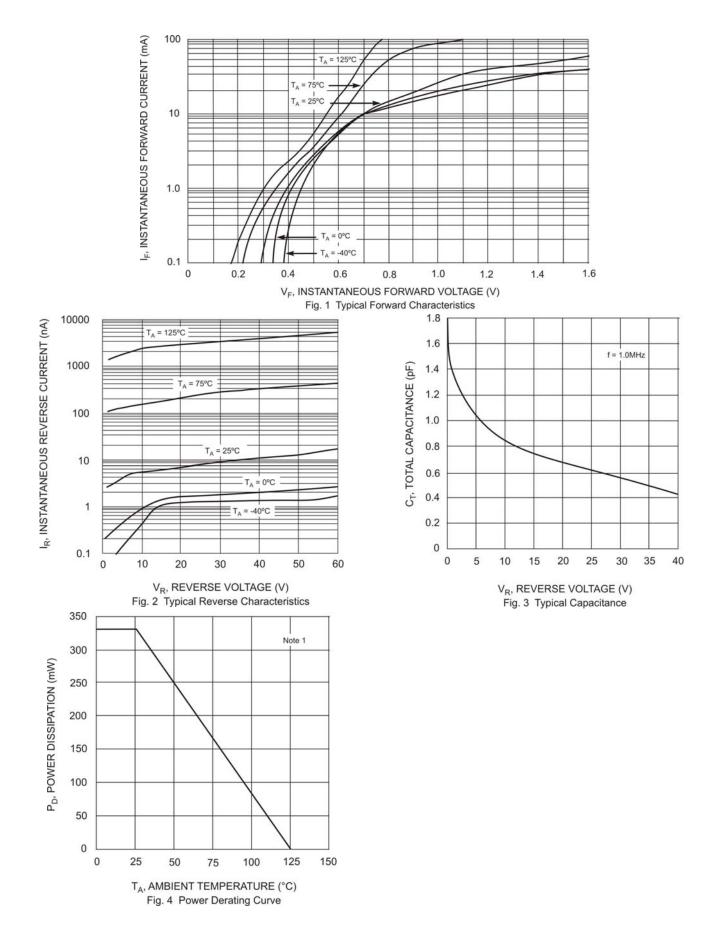


Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf. Short duration test pulse used to minimize self-heating effect.

3. No purposefully added lead.









Ordering Information (Note 4)

Device	Packaging	Shipping
1N6263W-7-F	SOD-123	3000/Tape and Reel

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



 $\begin{array}{l} \mathsf{SB} = \mathsf{Product} \ \mathsf{Type} \ \mathsf{Marking} \ \mathsf{Code} \\ \mathsf{YM} = \mathsf{Date} \ \mathsf{Code} \ \mathsf{Marking} \\ \mathsf{Y} = \mathsf{Year} \ (\mathsf{ex:} \ \mathsf{T} = 2006) \\ \mathsf{M} = \mathsf{Month} \ (\mathsf{ex:} \ 9 = \mathsf{September}) \end{array}$

Date Code Key

	,														
Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	J	К	L	М	Ν	Р	R	S	Т	U	V	W	Х	Y	Z
N	lonth		Jan	Feb	Mar	Apr	Мау	Jun	Jul	Αι	Ig S	Sep	Oct	Nov	Dec
Code			1	2	3	4	5	6	7	8	;	9	0	Ν	D

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