

TOSHIBA Diode Silicon Epitaxial Planar Type

1SS181

Ultra High Speed Switching Application

Small package : SC-59

Low forward voltage $V_{F(3)} = 0.92V (T_{yp.})$ Fast reverse recovery time: $t_{rr} = 1.6ns$ (Typ.) $: C_T = 2.2pF (Typ.)$ Small total capacitance

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V_{RM}	85	V	
Reverse voltage	V _R	80	V	
Maximum (peak) forward current	I _{FM}	300 (*)	mA	
Average forward current	Io	100 (*)	mA	
Surge current (10ms)	I _{FSM}	2 (*)	Α	
Power dissipation	Р	150	mW	
Junction temperature	Tj	125	°C	
Storage temperature	T _{stg}	-55~125	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Unit rating. Total rating = Unit rating × 1.5.

Unit: mm CATHODE **CATHODE** S-MINI **ANODE JEDEC** TO-236MOD EIAJ SC-59 **TOSHIBA** 1-3G1E Weight: 0.012g

Electrical Characteristics

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F (1)}	_	I _F = 1mA	_	0.61	-	157
	V _{F (2)}	_	I _F = 10mA	10-	0.74	6-37	V
	V _{F (3)}	_	I _F = 100mAs		0.92	1.20	Co.
Reverse current	I _{R (1)}	_	V _R = 30V	-41	42.	0.1	
	I _{R (2)}		V _R = 80V	_	_	0.5	μА
Total capacitance	CT	14	V _R = 0, f = 1MHz	_	2.2	4.0	pF
Reverse recovery time	t _{rr}	10.0	I _F = 10mA (Fig.1)	_	1.6	4.0	ns

Marking



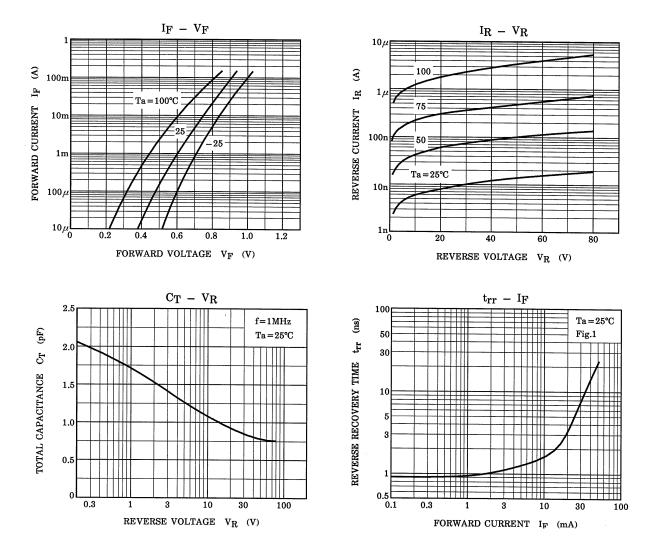
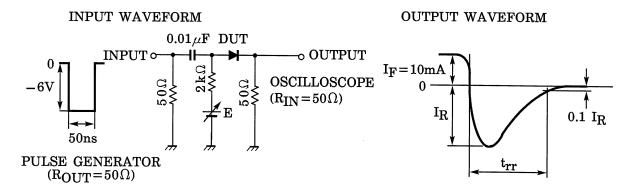


Fig.1 Reverse recovery time (t_{rr}) test circuit



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20070701-EN GENERAL

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