查询188344_07供应商 **TOSHIBA**

1SS344

TOSHIBA Diode Silicon Epitaxial Schottky Planar Type



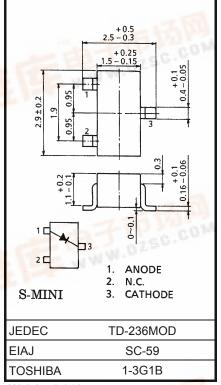
Ultra High Speed Switching Application

Unit: mm

- Low forward voltage $: V_{F(3)} = 0.50V (typ.)$
- Fast reverse recovery time : t_{rr} = 20ns (typ.)
- High average forward current : $I_O = 0.5A$ (max)

Absolute Maximum Ratings (Ta = 25°C)

| Characteristic | Symbol | Rating | Unit | |
|--------------------------------|------------------|---------|------|--|
| Maximum (peak) reverse voltage | V _{RM} | 25 | V | |
| Reverse voltage | V _R | 20 | V | |
| Maximum (peak) forward current | I _{FM} | 1500 | mA | |
| Average forward current | Ι _Ο | 500 | mA | |
| Surge current (10ms) | I _{FSM} | 5 | А | |
| Power dissipation | Р | 200 | mW | |
| Junction temperature | Tj | 125 | °C | |
| Storage temperature | T _{stg} | -55~125 | °C | |
| Operating Temperature | T _{opr} | -40~100 | °C | |



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

Weight: 0.012g

temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

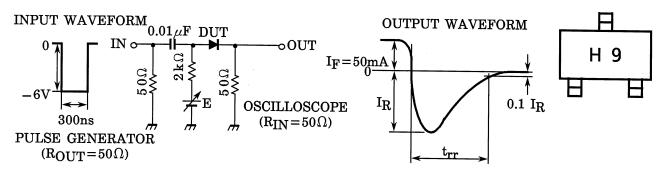
Electrical Characteristics (Ta = 25°C)

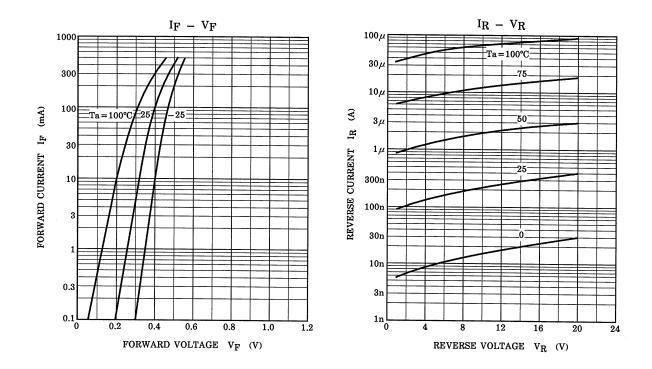
| Characteristic | Symbol | Test Circuit | Test Condition | Min | Тур. | Max | Unit |
|-----------------------|--------------------|-----------------|--------------------------------|------|------|------|------|
| Forward voltage | V _{F (1)} | — | I _F = 10mA | _ | 0.30 | -75 | V |
| | V _{F (2)} | _ | I _F = 100mA | 11 | 0.38 | 2 | |
| | V _{F (3)} | _ | I _F = 500mA | 1 15 | 0.50 | 0.55 | |
| Reverse current | I _{R (1)} | _ | V _R = 10V | - 24 | _ | 20 | μA |
| | I _{R (2)} | 1 | V _R = 20V | _ | _ | 100 | |
| Total capacitance | CT | | V _R = 0, f = 1MHz | _ | 120 | — | pF |
| Reverse recovery time | trsG | 50.0 | I _F = 50mA, (Fig.1) | | 20 | _ | ns |



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Fig.1 Reverse Recovery Time (trr) Test Circuit Marking





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20070701-EN GENERAL

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