Unit: mm



Low forward voltage

TOSHIBA Diode Silicon Epitaxial Schottky Barrier Type

1SS418

High Speed Switching Application

 $: V_{F(3)} = 0.23V (typ.)@ I_{F} = 5mA$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V _{RM}	35	V	
Reverse voltage	V_{R}	30	V	
Maximum (peak) forward current	I _{FM}	200	mA	
Average forward current	IO	100	mA	
Surge current (10ms)	I _{FSM}	1	Α	
Power dissipation	P*	100	mW	
Junction temperature	Tj	125	°C	
Storage temperature range	T _{stg}	− 55 ~ 125	°C	
Operating temperature range	T _{opr}	−40 ~ 100	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

Weight: 0.0011g(Typ.)

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

* Mounted on a glass epoxy circuit board of 20 × 20mm, pad dimension of 4 × 4mm.

Electrical Characteristics (Ta = 25°C)

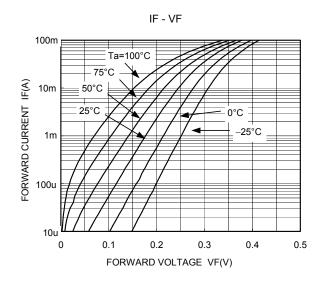
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F (1)}	_	I _F = 1mA	- 49	0.18	Ne c	COM
	V _{F (2)}	_	I _F = 5mA		0.23	-	V
	V _{F (3)}		I _F = 100mA	_	0.38	0.50	
Reverse current	I _R	100	V _R = 10V	_	_	20	μA
Reverse current	I _R	1400	V _R = 30V	_	_	50	μA
Total capacitance	CT		V _R = 0, f = 1MH _z	_	15	_	pF

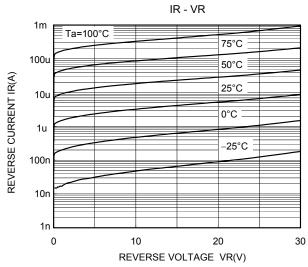
Equivalent Circuit (Top View)

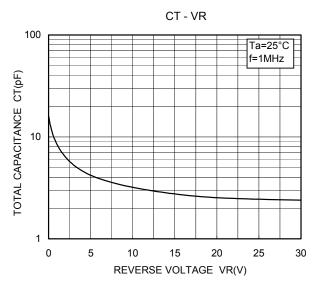
Marking

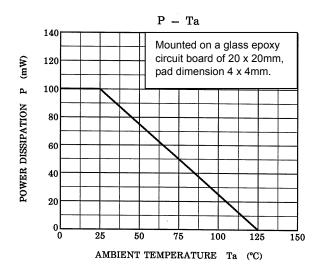












2007-11-01

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20070701-EN GENERAL

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 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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