

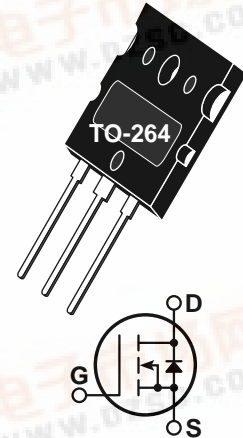


# APT10050LVR

1000V 21A 0.500Ω

## POWER MOS V<sup>®</sup>

Power MOS V<sup>®</sup> is a new generation of high voltage N-Channel enhancement mode power MOSFETs. This new technology minimizes the JFET effect, increases packing density and reduces the on-resistance. Power MOS V<sup>®</sup> also achieves faster switching speeds through optimized gate layout.



- Faster Switching
- Lower Leakage
- 100% Avalanche Tested
- Popular TO-264 Package

### MAXIMUM RATINGS

All Ratings: T<sub>C</sub> = 25°C unless otherwise specified.

Symbol	Parameter	APT10050LVR	UNIT
V <sub>DSS</sub>	Drain-Source Voltage	1000	Volts
I <sub>D</sub>	Continuous Drain Current @ T <sub>C</sub> = 25°C	21	Amps
I <sub>DM</sub>	Pulsed Drain Current <sup>①</sup>	84	
V <sub>GS</sub>	Gate-Source Voltage Continuous	±30	Volts
V <sub>GSM</sub>	Gate-Source Voltage Transient	±40	
P <sub>D</sub>	Total Power Dissipation @ T <sub>C</sub> = 25°C	520	Watts
	Linear Derating Factor	4.16	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C
T <sub>L</sub>	Lead Temperature: 0.063" from Case for 10 Sec.	300	
I <sub>AR</sub>	Avalanche Current <sup>①</sup> (Repetitive and Non-Repetitive)	21	Amps
E <sub>AR</sub>	Repetitive Avalanche Energy <sup>①</sup>	50	mJ
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>④</sup>	2500	

### STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage (V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA)	1000			Volts
I <sub>D(on)</sub>	On State Drain Current <sup>②</sup> (V <sub>DS</sub> > I <sub>D(on)</sub> × R <sub>DS(on)</sub> Max, V <sub>GS</sub> = 10V)	21			Amps
R <sub>DS(on)</sub>	Drain-Source On-State Resistance <sup>②</sup> (V <sub>GS</sub> = 10V, 0.5 I <sub>D(Cont.)</sub> )			0.500	Ohms
I <sub>DSS</sub>	Zero Gate Voltage Drain Current (V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0V)			25	μA
	Zero Gate Voltage Drain Current (V <sub>DS</sub> = 0.8 V <sub>DSS</sub> , V <sub>GS</sub> = 0V, T <sub>C</sub> = 125°C)			250	
I <sub>GSS</sub>	Gate-Source Leakage Current (V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V)			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 2.5mA)	2		4	Volts

**CAUTION:** These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

APT Website - <http://www.advancedpower.com>

## DYNAMIC CHARACTERISTICS

APT10050LVR

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1 \text{ MHz}$		6600	7900	pF
$C_{oss}$	Output Capacitance			595	830	
$C_{rss}$	Reverse Transfer Capacitance			290	430	
$Q_g$	Total Gate Charge <sup>③</sup>	$V_{GS} = 10V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_{D[Cont.]} @ 25^\circ C$		335	500	nC
$Q_{gs}$	Gate-Source Charge			29	45	
$Q_{gd}$	Gate-Drain ("Miller") Charge			165	250	
$t_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_{D[Cont.]} @ 25^\circ C$ $R_G = 0.6\Omega$		16	32	ns
$t_r$	Rise Time			13	26	
$t_{d(off)}$	Turn-off Delay Time			59	90	
$t_f$	Fall Time			8	16	

## SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
$I_S$	Continuous Source Current (Body Diode)			21	Amps
$I_{SM}$	Pulsed Source Current <sup>①</sup> (Body Diode)			84	
$V_{SD}$	Diode Forward Voltage <sup>②</sup> ( $V_{GS} = 0V, I_S = -I_{D[Cont.]}$ )			1.3	Volts
$t_{rr}$	Reverse Recovery Time ( $I_S = -I_{D[Cont.]}, di_S/dt = 100A/\mu s$ )		960		ns
$Q_{rr}$	Reverse Recovery Charge ( $I_S = -I_{D[Cont.]}, di_S/dt = 100A/\mu s$ )		22		$\mu C$

## THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.24	$^\circ C/W$
$R_{\theta JA}$	Junction to Ambient			40	

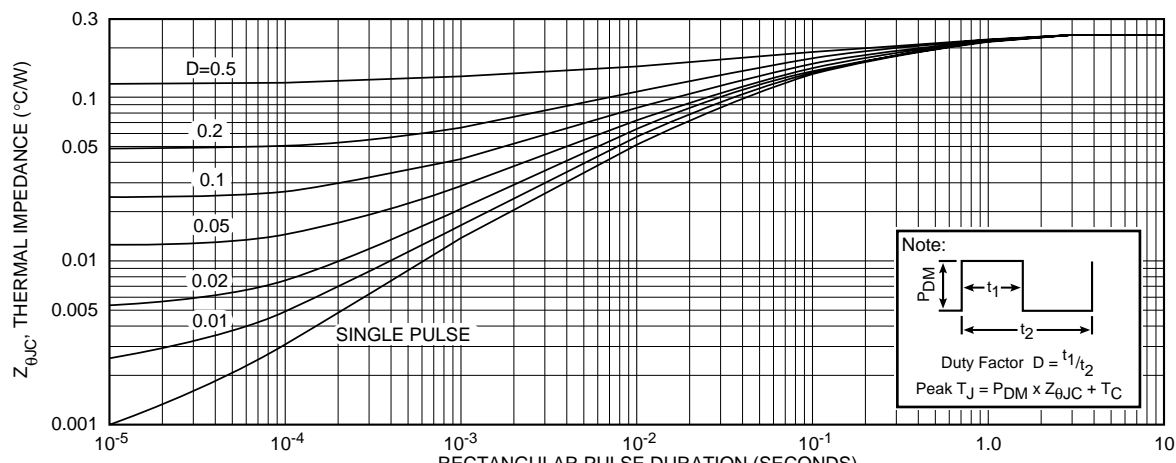
① Repetitive Rating: Pulse width limited by maximum junction temperature.

③ See MIL-STD-750 Method 3471

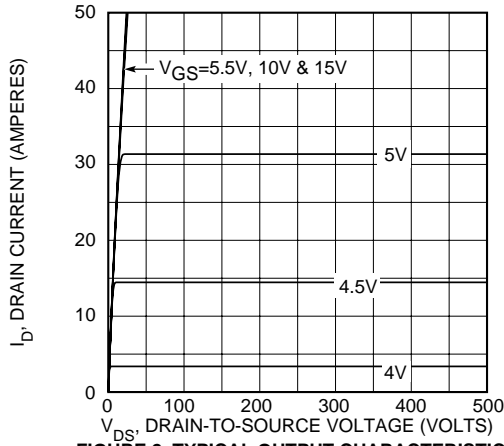
② Pulse Test: Pulse width < 380  $\mu s$ , Duty Cycle < 2%

④ Starting  $T_j = +25^\circ C$ ,  $L = 11.34mH$ ,  $R_G = 25\Omega$ , Peak  $I_L = 21A$

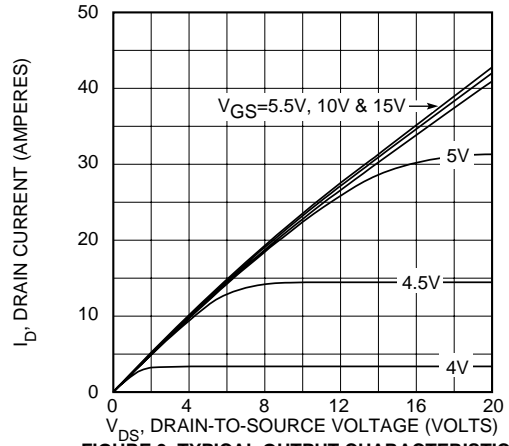
APT Reserves the right to change, without notice, the specifications and information contained herein.



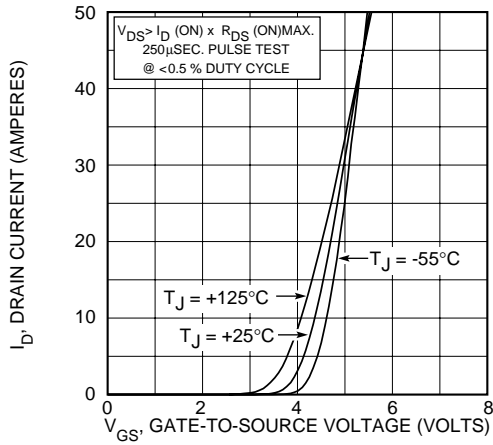
**APT10050LVR**



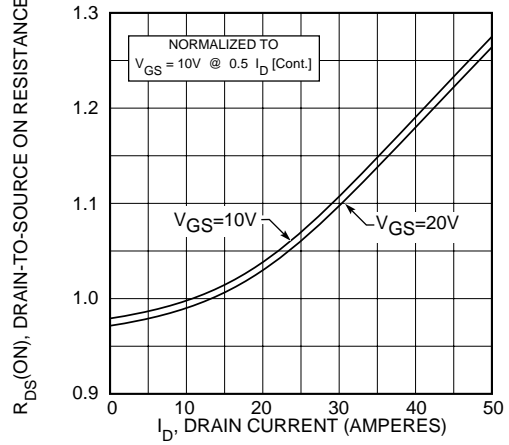
**FIGURE 2, TYPICAL OUTPUT CHARACTERISTICS**



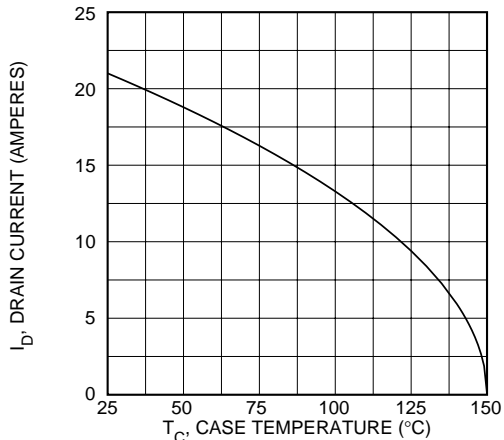
**FIGURE 3, TYPICAL OUTPUT CHARACTERISTICS**



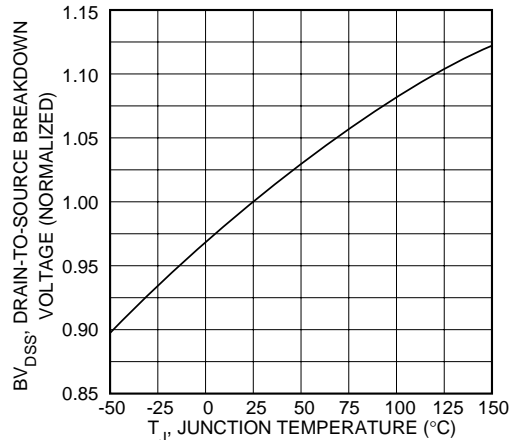
**FIGURE 4, TYPICAL TRANSFER CHARACTERISTICS**



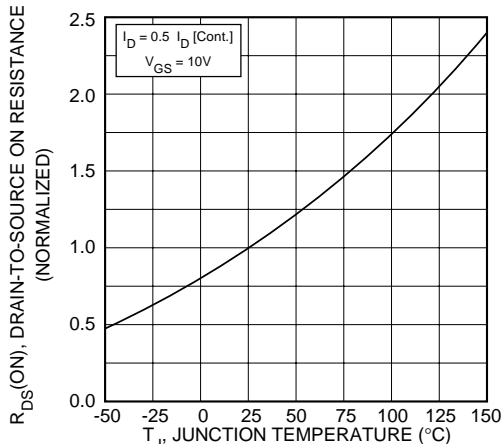
**FIGURE 5,  $R_{DS(ON)}$  vs DRAIN CURRENT**



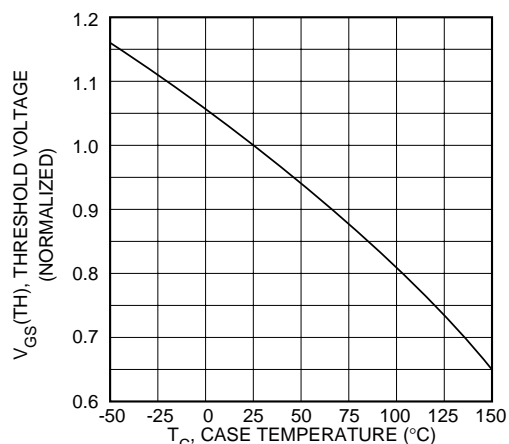
**FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE**



**FIGURE 7, BREAKDOWN VOLTAGE vs TEMPERATURE**



**FIGURE 8, ON-RESISTANCE vs. TEMPERATURE**



**FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE**

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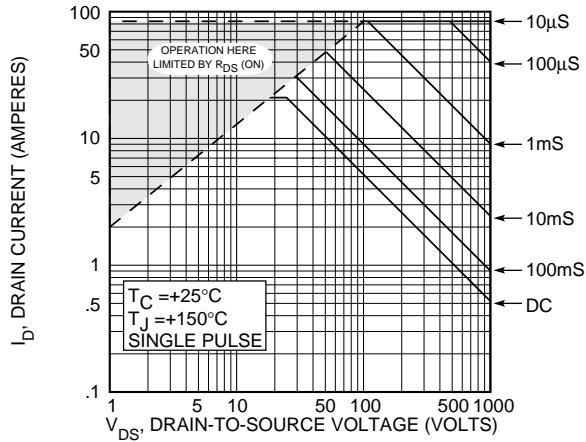


FIGURE 10, MAXIMUM SAFE OPERATING AREA

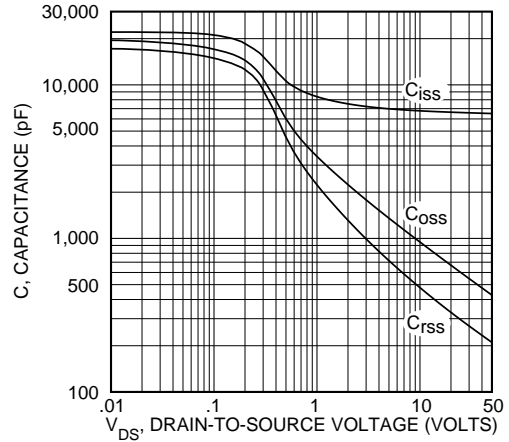


FIGURE 11, TYPICAL CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

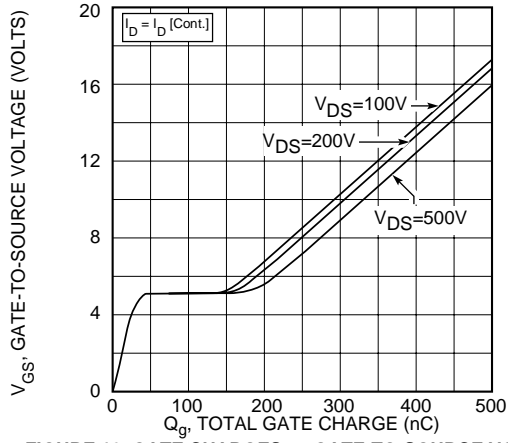


FIGURE 12, GATE CHARGES vs GATE-TO-SOURCE VOLTAGE

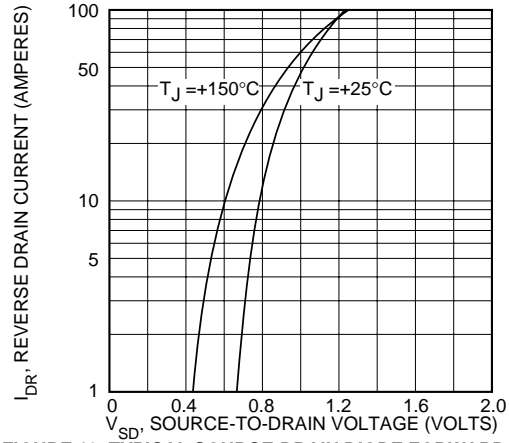


FIGURE 13, TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE

## TO-264 Package Outline

