

2N5679 2N5680

MECHANICAL DATA

Dimensions in mm (inches)

8.89 (0.35) 9.40 (0.37) 7.75 (0.305) 8.51 (0.335) 0.66 (0.026) 12.70 (0.500) min. 7.75 (0.305) 8.51 (0.335) 0.66 (0.026) 0.71 (0.028) 0.75 (0.305) 0.77 (0.035) 0.77 (0.035)

PNP SILICON TRANSISTORS

DESCRIPTION

The 2N5679 and 2N5680 are silicon expitaxial planar PNP transistors in jedec TO-39 metal case intended for use as drivers for high power transistors in general purpose, amplifier and switching circuit

The complementary NPN types are the 2N5681 and 2N5682 respectively

TO-39

Pin 1 – Emitter Pin 2 – Base Pin 3 – Collector

ABSOLUTE MAXIMUM RATINGS

$T_{CASE} = 25^{\circ} c$	unless otherwise stated	2N5679	2N5680	
V_{CBO}	Collector – Base Voltage	-100V	-120V	
V_{CEO}	Collector – Emitter Voltage (I _B = 0)	-100V	-120V	
V_{EBO}	Emitter – Base Voltage (I _C = 0)	-4V		
I _C	Continuous Collector Current	-1A		
l _B	Base Current	-0.5A		
P _{tot}	Total Dissipation at T _{case} ≤ 25°C	10W		
	T _{amb} ≤ 25°C		1W	
T _{stg}	Operating and Storage Temperature Range	−65 to +200°C		
找PDF	Junction temperature	200°C		



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THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-case	Max	17.5	°C/W
R _{thj-amb}	Thermal Resistance Junction-ambient	Max	175	°C/W

ELECTRICAL CHARACTERISTICS (T_{case} = 25°C unless otherwise stated)

	Parameter		Test Conditions		Тур.	Max.	Unit	
		I _E = 0						
I _{CBO}	Collector Cut Off Current	for 2N5679	$V_{CB} = -100V$			-1	μΑ	
		for 2N5680	$V_{CB} = -120V$			-1		
	Collector Cut Off Current	V _{BE} = 1.5						
I _{CEV}		for 2N5679	$V_{CE} = -100V$			-1	μΑ	
		for 2N5680	$V_{CE} = -120V$			-1		
		T _{case} = 150°0	C					
		for 2N5679	$V_{CE} = -100V$			-1	mA	
		for 2n5680	$V_{CE} = -120V$			-1		
	Collector Cut Off Current	I _B = 0						
I _{CEO}		for 2N5679	$V_{CE} = -70V$			-10	μA	
		for 2N5680	$V_{CE} = -80V$			-10		
I _{EBO}	Emitter Cut Off Current	I _C = 0	V _{EB} = -4V			-1		
		I _B = 0	I _C = -10mA					
V _{CEO(sus)*}	Collector Emitter Sustaining Voltage	for 2N5679		-100				
		for 2N5680		-120				
	Collector Emitter Saturation Voltage	$I_{C} = -250 \text{mA}$	$I_B = -25 \text{mA}$			-0.6	V	
V _{CE(sat)*}		$I_{C} = -500 \text{mA}$	$I_B = -50 \text{mA}$			-1		
		I _C = -1A	I _B = -200mA			-2		
V _{BE*}	Base Emitter Voltage	$I_{C} = -250 \text{mA}$	V _{CE} = -2V			-1	-	
h _{FE*}	DC Current Gain	$I_{C} = -250 \text{mA}$	V _{CE} = -2V	40		150		
		I _C = -1A	$V_{CE} = -2V$	5				
f _T	Transistion Frequency	$I_{C} = -100 \text{mA}$	V _{CE} = -10V	30			MHz	
		f = 10MHz						
C _{CBO}	Collector Base Capacitance	I _E = 0	V _{CB} = -20V			50	pF	
		f = 1MHz						
h _{fe}	Concil Circuit Coin	$I_{C} = -0.2A$	V _{CE} = -1.5V	40				
	Small Signal Current Gain	f = 1KHz						
* D. In	t = 200uc & < 20/					<u> </u>		

^{*} Pulse test $t_p = 300\mu s$, $\delta < 2\%$