

TUV MANAGEMENT SERVICE

An ISO/TS16949 and ISO 9001 Certified Company

PNP SILICON PLANAR EPITAXIAL TRANSISTOR

2N6710



TO-237
Plastic Package

General Purpose Medium Power Amplifier

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

DESCRIPTION	SYMBOL	VALUE	UNIT	
Collector Base Voltage	V_{CBO}	100	V	
Collector Emitter Voltage	V_{CEO}	80	V CO.C.	
Emitter Base Voltage	V_{EBO}	5 WWW.	V	
Collector Current Continuous	I _C	1.5	А	
Total Power Dissipation	P_{D}	850	mW	
Operating And Storage Junction	T_{j},T_{stg}	-55 to +150	٥C	
Temperature Range	T.C.C.CO.			

ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)

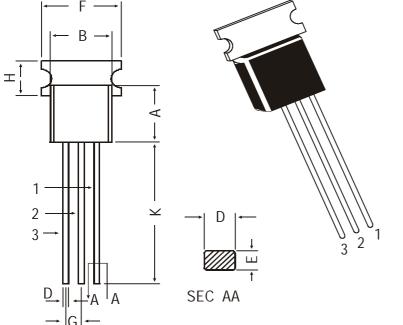
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Collector Emitter Voltage	V_{CEO}	I _C =10mA, I _B =0	80	E YAY	V
Collector Base Voltage	V_{CBO}	$I_{C}=100\mu A, I_{E}=0$	100		V
Emitter Base Voltage	V_{EBO}	$I_E=10\mu A, I_C=0$	5		V
Collector Cut off Current	I_{CBO}	V _{CB} =100V, I _E =0		0.1	μΑ
Emitter Cut off Current	I _{EBO}	V _{EB} =4V, I _C =0		0.1	μΑ
DC Current Gain	h _{FE}	I _C =50mA,V _{CE} =2V	40		
		$I_C=250$ mA, $V_{CE}=2$ V	40	250	
		$I_C=500$ mA, $V_{CE}=2$ V	25		
Collector Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=500$ mA, $I_B=50$ mA		0.5	V
		$I_C=1A,I_B=100mA$		1	V
Transition Frequency	f_T	V_{CE} =10V, I_{C} =50mA,	50		MHz
			7-1	Paret	38



2N6710

TO-237 Plastic Package





	DIM	MIN.	MAX.		
	Α	4.32	5.33		
	В	4.45	5.20		
	С	3.18	4.19		
	D	0.41	0.55		
All diminsions in min.	Ε	0.35	0.50		
	F		5.40		
	G	1.14	1.40		
JIIS	Н	_	2.54		
IISII	K	12.70	_		
	L	5 DEG			
All (J	1.14	1.53		

PIN CONFIGURATION

- 1. BASE
- 2. COLLECTOR
- 3. EMITTER

Packing	Dotail
Packillu	Detail

· dokung Dokun							
PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-237 Bulk	1K/polybag	240 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	26.2 kgs
TO-237 T&A	2K/ammo box	725 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	13.8 kgs

Notes 2N6710

TO-237
Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of

Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119
email@cdil.com www.cdilsemi.com