

## 2N7002T

# N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

### **Features**

Low On-Resistance

Low Gate Threshold Voltage

Low Input Capacitance

Fast Switching Speed

Low Input/Output Leakage

Ultra-Small Surface Mount Package

Lead Free/RoHS Compliant (Note 2)

Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

Case: SOT-523

Case Material: Molded Plastic. UL Flammability

Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020C

Terminals: Solderable per MIL-STD-202, Method 208

Lead Free Plating (Matte Tin Finish annealed over Alloy 42

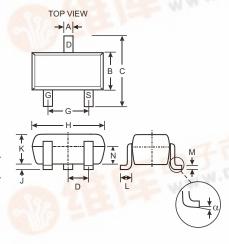
leadframe).

Terminal Connections: See Diagram

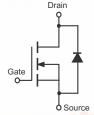
Marking: 72 (See Page 3)

Ordering & Date Code Information, See Page 3

Weight: 0.002 grams (approximate)



SOT-523									
Dim	Min	Max	Тур						
Α	0.15	0.30	0.22						
В	0.75	0.85	0.80						
С	1.45	1.75	1.60						
D			0.50						
G	0.90	1.10	1.00						
Н	1.50	1.70	1.60						
J	0.00	0.10	0.05						
K	0.60	0.80	0.75						
L	0.10	0.30	0.22						
M	0.10	0.20	0.12						
N	0.45	0.65	0.50						
	0	8							
All Dimensions in mm									



## Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	$V_{DSS}$	60	V		
Drain-Gate Voltage R <sub>GS</sub> 1.0M	$V_{DGR}$	60	V		
Gate-Source Voltage Continuous Pulsed	V <sub>GSS</sub>	±20 ±40	T V		
Drain Current (Note 1) Continuous Continuous @ 100°C Pulsed	I <sub>D</sub>	115 73 800	mA		
Total Power Dissipation (Note 1)	P <sub>d</sub>	150	mW		
Thermal Resistance, Junction to Ambient	R JA	833	°C/W		
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C		

Note: 1. Device mounted on FR-5 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

2. No purposefully added lead.



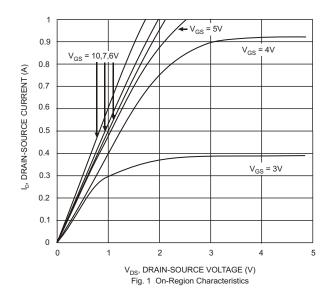


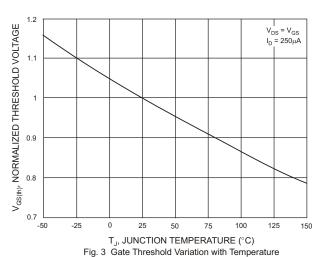
## **Electrical Chacteristics**

@ T<sub>A</sub> = 25 C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 3)									
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	60			V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 10 A		
Zero Gate Voltage Drain Current @ $T_C = 25^{\circ}C$ @ $T_C = 125^{\circ}C$		I <sub>DSS</sub>			1.0 500	μA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V		
Gate-Body Leakage		I <sub>GSS</sub>			±10	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 3)									
Gate Threshold Voltage		V <sub>GS(th)</sub>	1.0		2.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 A		
Static Drain-Source On-Resistance	@ T <sub>j</sub> = 25°C @ T <sub>j</sub> = 125°C	R <sub>DS</sub> (ON)		2.0 4.4	7.5 13.5		V <sub>GS</sub> = 5.0V, I <sub>D</sub> = 0.05A		
							$V_{GS} = 10V, I_D = 0.5A$		
On-State Drain Current		I <sub>D(ON)</sub>	0.5	1.0		Α	$V_{GS} = 10V, V_{DS} = 7.5V$		
Forward Transconductance			80			mS	V <sub>DS</sub> =10V, I <sub>D</sub> = 0.2A		
DYNAMIC CHARACTERISTICS									
Input Capacitance		C <sub>iss</sub>		22	50	pF			
Output Capacitance		Coss		11	25	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz		
Reverse Transfer Capacitance		Crss		2.0	5.0	pF	1 - 1.014112		
SWITCHING CHARACTERISTICS							,		
Turn-On Delay Time		t <sub>D(ON)</sub>		7.0	20	ns	$V_{DD} = 30V, I_D = 0.2A,$		
Turn-Off Delay Time		t <sub>D(OFF)</sub>		11	20	ns	R <sub>L</sub> = 150 , V <sub>GEN</sub> = 10V, R <sub>GEN</sub> = 25		

Note: 3. Short duration test pulse used to minimize self-heating effect.





2.2 V<sub>GS</sub> = 3V V<sub>GS</sub> = 4V V<sub>GS</sub> = 5, 6, 7, 10V V<sub>GS</sub> = 6, 7, 10V V<sub>GS</sub> =

I<sub>D</sub>, DRAIN-SOURCE CURRENT (A)
Fig. 2 On-Resistance Variation with Gate Voltage and Drain-Source Current

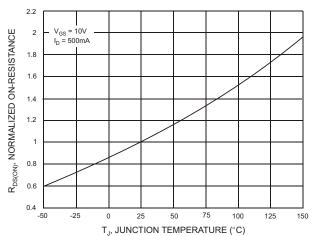
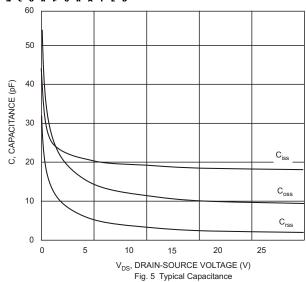
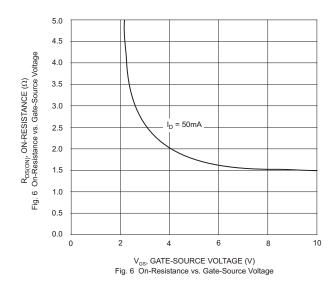


Fig. 4 On-Resistance Variation with Temperature





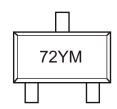


## Ordering Information (Note 4)

Device	Packaging	Shipping		
2N7002T-7-F	SOT-523	3000/Tape & Reel		

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**



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72 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: N = 2002) M = Month (ex: 9 = September)

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#### Date Code Key

Code

Year	2002		2003	2004	4	2005	2006	6	2007	2008		2009
Code	N		Р	R		S	Т		U	V		W
Month	Jan	Feb	March	Apr	May	/ Jun	Jul	Aug	g Sep	Oct	Nov	Dec

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