TOSHIBA Transistor Silicon PNP Epitaxial Type

## 2SA2061

High-Speed Switching Applications

DC-DC Converter Applications

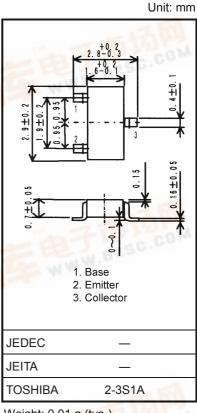
Strobe Applications

- High DC current gain:  $h_{FE} = 200 \text{ to } 500 \text{ (I}_{C} = 0.5 \text{ A)}$
- Low collector-emitter saturation voltage:  $V_{CE}$  (sat) = -0.19 V (max)
- High-speed switching: tf = 40 ns (typ.)

# Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V <sub>CBO</sub>	-20	V	
Collector-emitter voltage		V <sub>CEO</sub>	-20	V	
Emitter-base voltage		$V_{EBO}$	-7	V	
Collector current	DC	Ic	-2.5	Α	
	Pulse	I <sub>CP</sub>	-4.0		
Base current		l <sub>B</sub>	-250	mA	
Collector power dissipation	t = 10 s	P <sub>C</sub>	1000	mW	
	DC	(Note)	625		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

Note: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm<sup>2</sup>)



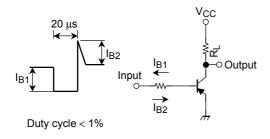
Weight: 0.01 g (typ.)

#### Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I <sub>CBO</sub>	V <sub>CB</sub> = -20 V, I <sub>E</sub> = 0	_		-100	nA	
Emitter cut-off current		I <sub>EBO</sub>	V <sub>EB</sub> = -7 V, I <sub>C</sub> = 0	1		-100	nA	
Collector-emitter breakdown voltage		V (BR) CEO	I <sub>C</sub> = −10 mA, I <sub>B</sub> = 0	-20	4.1	1750	V	
DC current gain		h <sub>FE</sub> (1)	$V_{CE} = -2 \text{ V}, I_{C} = 0.5 \text{ A}$	200	_	500		
		h <sub>FE</sub> (2)	V <sub>CE</sub> = -2 V, I <sub>C</sub> = -1.6 A	100	_	_		
Collector-emitter saturation voltage		V <sub>CE</sub> (sat)	I <sub>C</sub> = -1.6 A, I <sub>B</sub> = -53 mA	_	_	-0.19	V	
Base-emitter saturation voltage		V <sub>BE</sub> (sat)	I <sub>C</sub> = -1.6 A, I <sub>B</sub> = -53 mA	_	_	-1.10	V	
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = −10 V, I <sub>E</sub> = 0, f = 1 MHz	_	28	_	pF	
Switching time	Rise time	t <sub>r</sub>	See Figure 1 circuit diagram.	_	70	_		
	Storage time	t <sub>stg</sub>	$V_{CC} \approx -12 \text{ V}, R_L = 7.5 \Omega$	_	150	_	ns	
	Fall time	t <sub>f</sub>	$-I_{B1} = I_{B2} = -53 \text{ mA}$	_	40	_		



### Marking



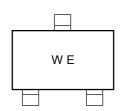
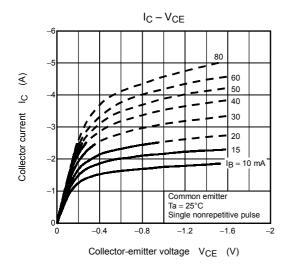
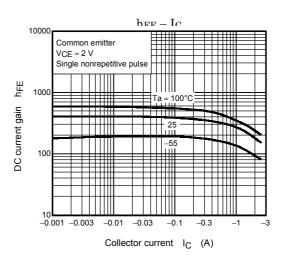
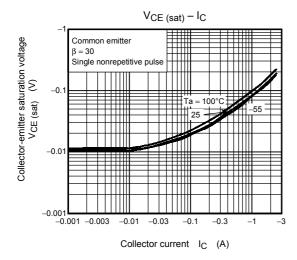


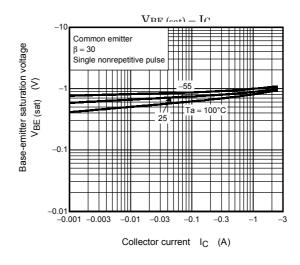
Figure 1 Switching Time Test Circuit & Timing Chart

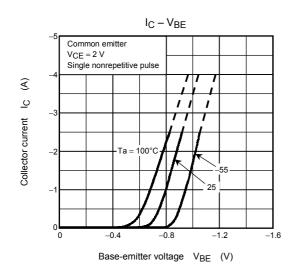
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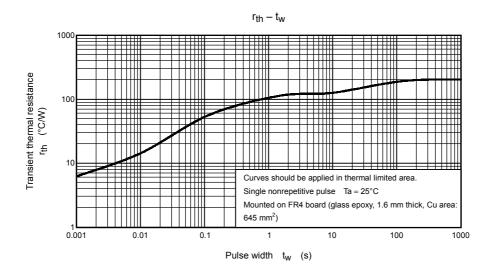


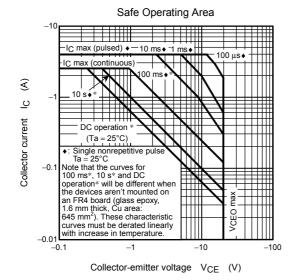






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