2SA2092

Transistors

-1A / -60V Bipolar transistor 2SA2092

Applications

High-speed switching, low frequency amplification

Feature

- 1) High speed switching. (tf : Typ. : 30ns at Ic = -1A)
- 2) Low saturation voltage. (Typ. : -200mV at Ic = -500mA, IB = -50mA)
- 3) Strong discharge resistance for inductive load and capacitance load.
- 4) Low switching noise.

Structure

PNP epitaxial planar silicon transistor

Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Collector-base voltage		Vсво	-60	V
Collector-emitter voltage		VCEO	-60	V
Emitter-base voltage		Vево	-6	V
Collector current	DC	lc	-1	Α
	PULSE	CP *1	-2	Α
Power dissipation		Pc *2	500	mW
Junction temperature		Tj	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

Packaging specifications

2.8

Abbreviated symbol : VN

External dimensions (Unit : mm)

2.9

0.4

0.95 0.95

1.9

(3)

+(1) (2)

TSMT3

(1) Base

(2) Emitter

(3) Collector

	Package	TSMT3
	Packaging type	Taping
	Code	TL
Part No.	Basic ordering unit (pieces)	3000
2SA2092		0

1.0MAX

0.85

0.16

0.7

0~0.1

Each lead has same dimensions

hfe rank

Q 120-270

*1 Pw=10ms

*2 Each terminal mounted on a recommended land

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVCEO	-60	-	-	V	Ic= -1mA
Collector-base breakdown voltage	ВУсво	-60	-	-	V	Ic= -100μA
Emitter-base breakdown voltage	BVEBO	-6	-	-	V	Iε= -100μA Vcв= -40V
Collector cut-off current	Ісво	-	-	-1.0	μA	Vсв= -40V
Emitter cut-off current	Іево	-	-	-1.0	μA	VEB= -4V
Collector-emitter saturation voltage	VCE(sat)		-200	<mark>-500</mark>	mV	Ic= - <mark>50</mark> 0mA, Iв= -50mA
DC current gain	hFE *3	120	- (270	-	Vce= -2V, Ic= -100mA
Transition frequency	ft *1	227	300	-	MHz	Vce= -10V, le=100mA, f=10MHz
Collector output capacitance	Cob		15	-	pF	Vcb= -10V, IE=0, f=1MHz
Turn-on time	ton	_	30	-	ns	Ic= -1A,
Storage time	tstg	_	100	-	ns	Ів1= –100mA Ів2=100mA
Fall time	tf *2	_	30	_	ns	$Vcc \simeq -25V$

*1 Pulse measurement

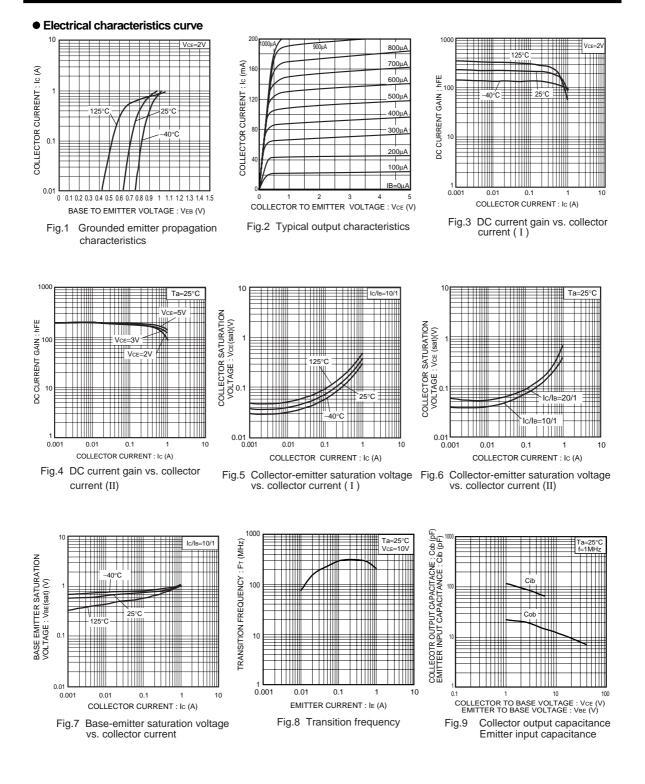
*2 See switching test circuit *3 hFE rank





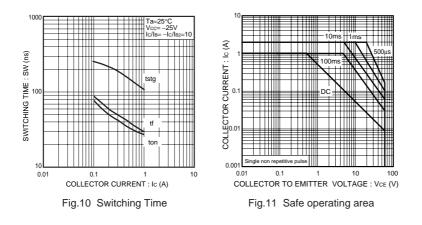
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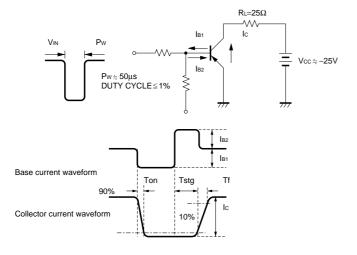


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•Switching test circuit



Appendix

Notes

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