Ordering number: ENA0307



SANYO Semiconductors DATA SHEET

2SA2192 — PNP Epitaxial Planar Silicon Transistor

High-Current Switching Applications

Applications

· Relay drivers, lamp drivers, motor drivers.

Features

- · Adoption of MBIT process.
- · Large current capacitance.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO	MADO	-50	V
Collector-to-Emitter Voltage	VCES	40.00	-50	V
Collector-to-Emitter Voltage	VCEO		-50	V
Emitter-to-Base Voltage	VEBO		-8	V
Collector Current	IC		-10	Α
Collector Current (Pulse)	ICP	PW≤100μs	-13	Α
Base Current	IB		-2	A
Collector Dissipation	D-		0.95	W
	PC	Tc=25°C	20	W
Junction Temperature	Tj	A 14 1	150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =-40V, I _E =0A			-10	μΑ
Emitter Cutoff Current	IEBO	V _{EB} =-4V, I _C =0A			-10	μΑ
DC Current Gain	hFE	VCE=-2V, IC=-1A	200		560	-
Gain-Bandwidth Product	fΤ	V _{CE} =-5V, I _C =-1A	1.00	130		MHz
Output Capacitance	Cob	V _{CB} =-10V, f=1MHz		90	100	pF
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=-5A, IB=-250mA		-290	-580	mV
Base-to-Emitterr Saturation Voltage	V _{BE} (sat)	I _C =-5A, I _B =-250mA		-0.93	-1.4	V

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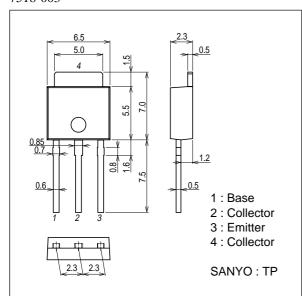
2SA2192

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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =-100μA, I _E =0A	-50			V
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I _C =-100μA, R _{BE} =0Ω	-50			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=-1mA, RBE=∞	-50			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =-100μA, I _C =0A	-8			V
Turn-On Time	ton	See specified Test Circuit.		70		ns
Storage Time	tstg	See specified Test Circuit.		650		ns
Fall Time	tf	See specified Test Circuit.		60		ns

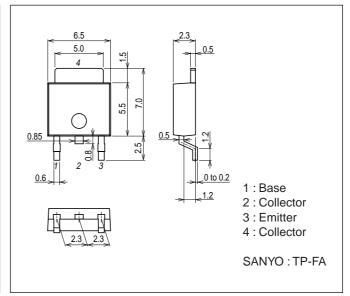
Package Dimensions

unit : mm 7518-003

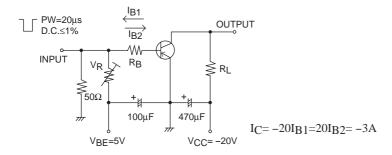


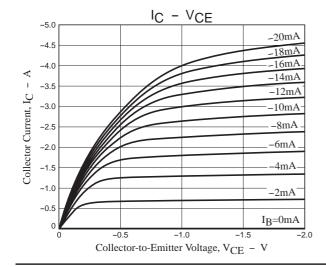
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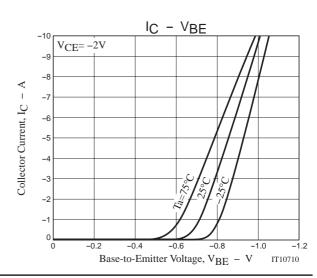
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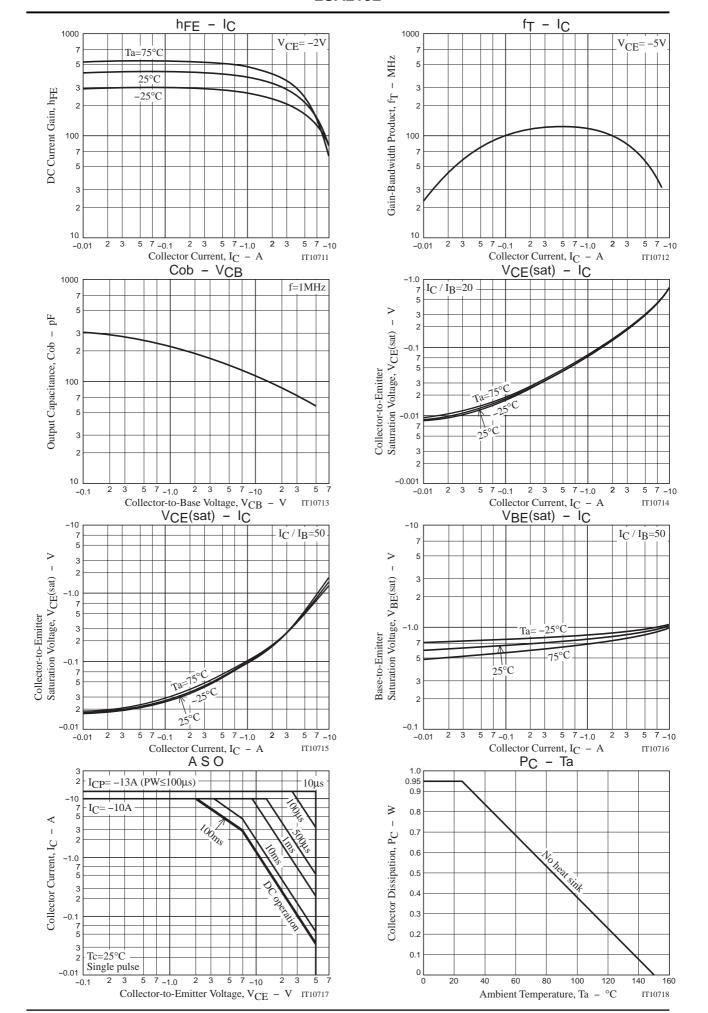


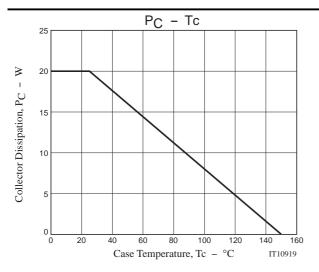
Switching Time Test Circuit











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