

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

2SC2290A

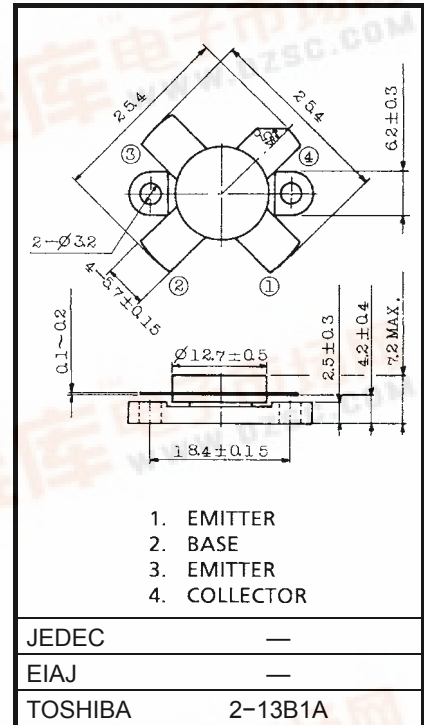
2~30MHz SSB LINEAR POWER AMPLIFIER APPLICATIONS
(LOW SUPPLY VOLTAGE USE)

- Specified 12.5V, 28MHz Characteristics
- Output Power : $P_o = 60W_{PEP}$ (Min.)
- Power Gain : $G_p = 11.8dB$ (Min.)
- Collector Efficiency : $\eta_C = 35%$ (Min.)
- Intermodulation Distortion: $IMD = -30dB$ (Max.)

MAXIMUM RATINGS ($T_c = 25^\circ C$)

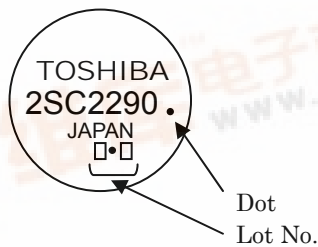
CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Base Voltage	V_{CBO}	45	V
Collector-Emitter Voltage	V_{CES}	45	V
Collector-Emitter Voltage	V_{CEO}	18	V
Emitter-Base Voltage	V_{EBO}	4	V
Collector Current	I_C	20	A
Collector Power Dissipation	P_C	175	W
Junction Temperature	T_j	175	$^\circ C$
Storage Temperature Range	T_{stg}	-65~175	$^\circ C$

Unit in mm



Weight: 5.2g

MARKING



ELECTRICAL CHARACTERISTICS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = 100mA, I_B = 0$	18	—	—	V
Collector-Emitter Breakdown Voltage	$V_{(BR)CES}$	$I_C = 100mA, V_{EB} = 0$	45	—	—	V
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E = 1mA, I_C = 0$	4	—	—	V
DC Current Gain	h_{FE}	$V_{CE} = 5V, I_C = 10A *$	10	—	150	—
Collector Output Capacitance	C_{ob}	$V_{CB} = 12.5V, I_E = 0$ $f = 1MHz$	—	—	500	pF
Power Gain	G_p	$V_{CC} = 12.5V, f_1 = 28.000MHz,$ $f_2 = 28.001MHz$ $I_{idle} = 50mA$ $P_o = 60W_{PEP}$ (Fig.)	11.8	13.8	—	dB
Input Power	P_i		—	2.5	4	W_{PEP}
Collector Efficiency	η_C		35	—	—	%
Intermodulation Distortion	IMD		—	—	-30	dB
Series Equivalent Input Impedance	Z_{in}		—	1.02 -j0.17	—	Ω
Series Equivalent Output Impedance	Z_{out}	—	0.86 -j0.21	—	Ω	

* Pulse Test: Pulse Width $\leq 100\mu s$, Duty Cycle $\leq 3\%$

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