

# **2SJ533**

# Silicon P Channel MOS FET

REJ03G0883-0400

(Previous: ADE-208-649B)

Rev.4.00 Sep 07, 2005

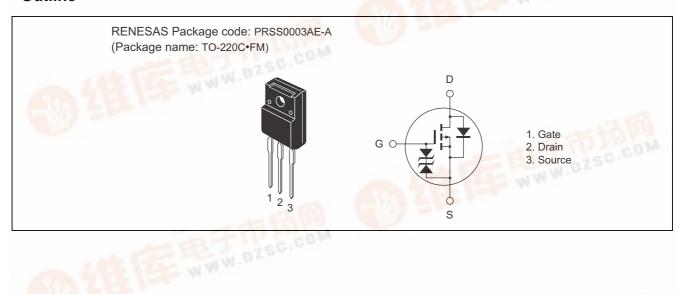
### **Description**

High speed power switching

#### **Features**

- $R_{DS (on)} = 0.028 \Omega \text{ typ.}$ Low drive current Low on-resistance
- Low drive current.
- 4 V gate drive devices.
- High speed switching.

#### **Outline**





## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	-30	Α
Drain peak current	I <sub>D (pulse)</sub> Note 1	-120	Α
Body to drain diode reverse drain current	I <sub>DR</sub>	-30	Α
Avalanche current	I <sub>AP</sub> Note 3	-30	Α
Avalanche energy	E <sub>AR</sub> Note 3	77	mJ
Channel dissipation	Pch Note 2	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at  $Tc = 25^{\circ}C$ 

3. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

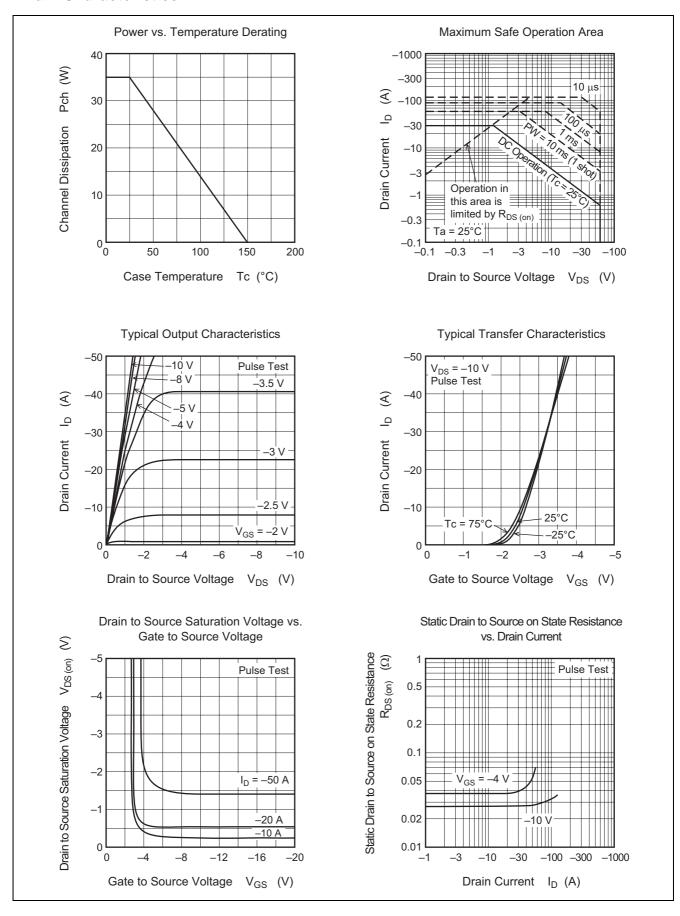
### **Electrical Characteristics**

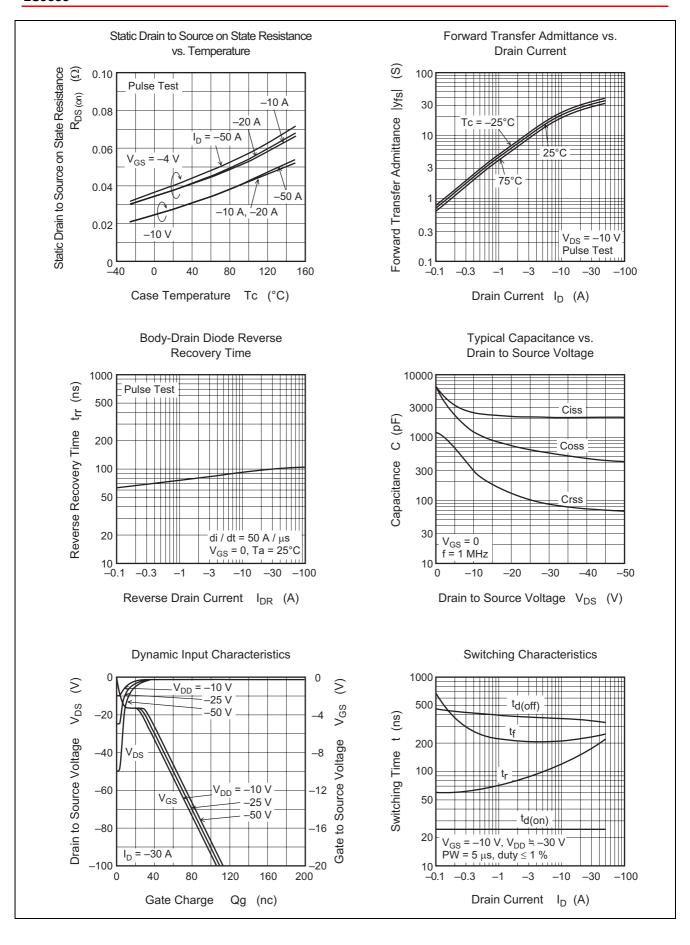
 $(Ta = 25^{\circ}C)$ 

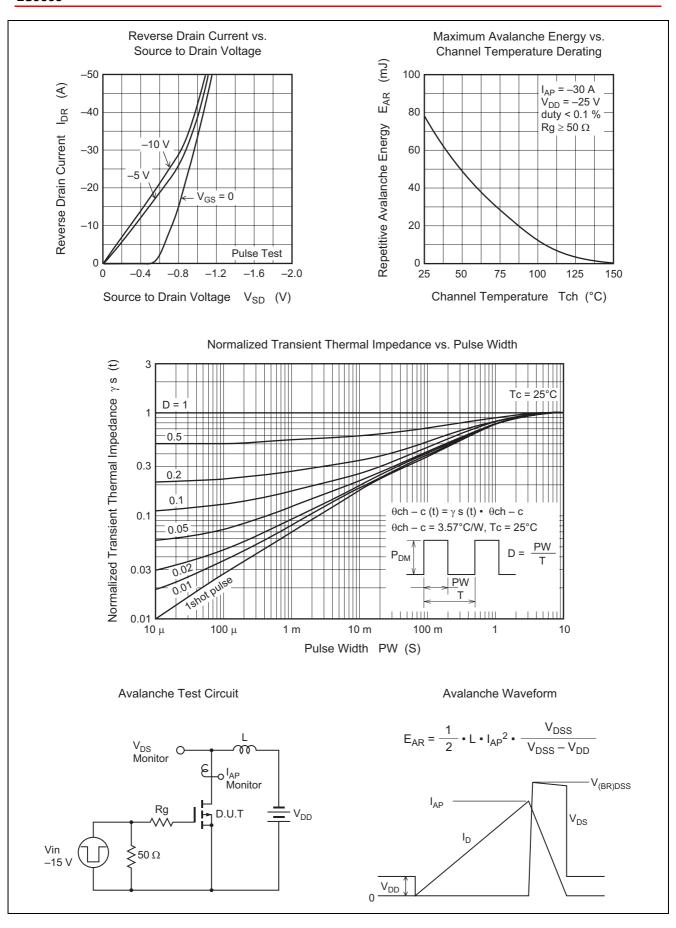
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±20			>	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_		-10	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>			±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-1.0	_	-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	0.028	0.037	Ω	$I_D = -15 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 4}}$
	R <sub>DS (on)</sub>	_	0.038	0.055	Ω	$I_D = -15 \text{ A}, V_{GS} = -4 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y <sub>fs</sub>	15	25	_	S	$I_D = -15 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	2500	_	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss	_	1300	_	рF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	300	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	_	25	_	ns	V <sub>GS</sub> = -10 V
Rise time	t <sub>r</sub>	_	150	_	ns	$I_D = -15 \text{ A}$
Turn-off delay time	t <sub>d (off)</sub>	_	350	_	ns	$R_L = 2 \Omega$
Fall time	t <sub>f</sub>	_	220	_	ns	
Body to drain diode forward voltage	$V_{DF}$	_	-0.95	_	V	$I_F = -30 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	_	100	_	ns	$I_F = -30 \text{ A}, V_{GS} = 0$
						$di_F/dt = 50 A/\mu s$

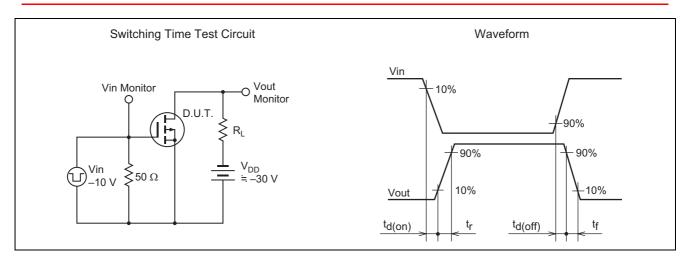
Note: 4. Pulse test

#### **Main Characteristics**

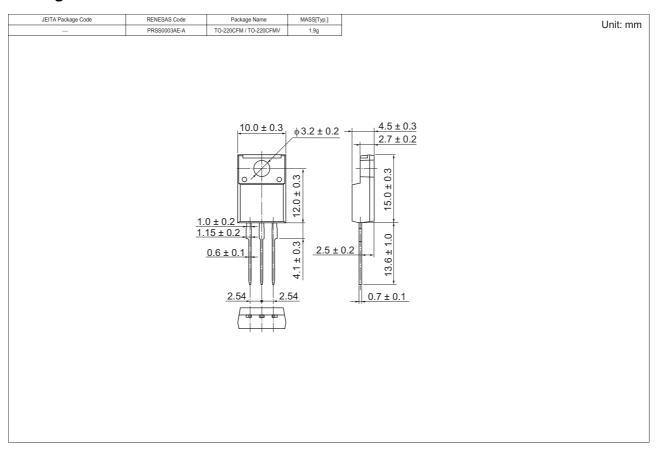








## **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
2SJ533-E	50 pcs	Plastic magazine

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