

捷多邦,专业PCB打样工厂,24小时加急出货

2SJ586

Silicon P Channel MOS FET High Speed Switching



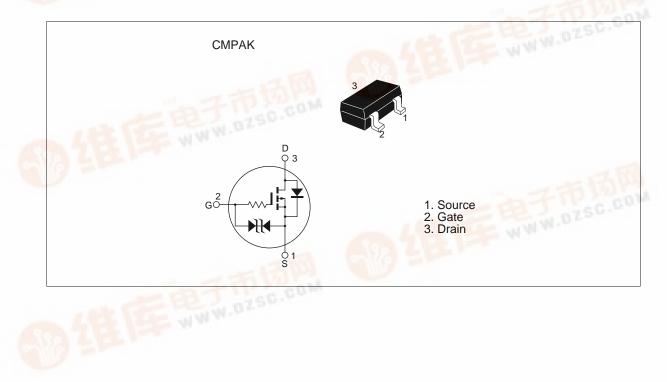
ADE-208-771A (Z) 2nd.Edition. June 1999

WWW.DZSC.C

Features

- Low on-resistance
 - $R_{\rm DS} = 4.1~\Omega$ typ. ($V_{\rm GS} = -4~V$, $I_{\rm D} = -50~mA$)
 - $R_{DS} = 6.0 \Omega$ typ. ($V_{GS} = -2.5 V$, $I_D = -50 mA$) DZSC.CC
- 2.5 V gate drive device.
- Small package (CMPAK) ٠

Outline





Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	-20	V	
Gate to source voltage	V _{GSS}	±10	V	
Drain current	I _D	-100	mA	
Drain peak current	Note1 D(pulse)	-400	mA	
Body-drain diode reverse drain current	I _{DR}	-100	mA	
Channel dissipation	Pch Note 2	300	mW	
Channel temperature	Tch	150	٥°	
Storage temperature	Tstg	-55 to +150	°C	

Note: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

2. Value on the alumina ceramic board (12.5x 20 x0.7 mm)

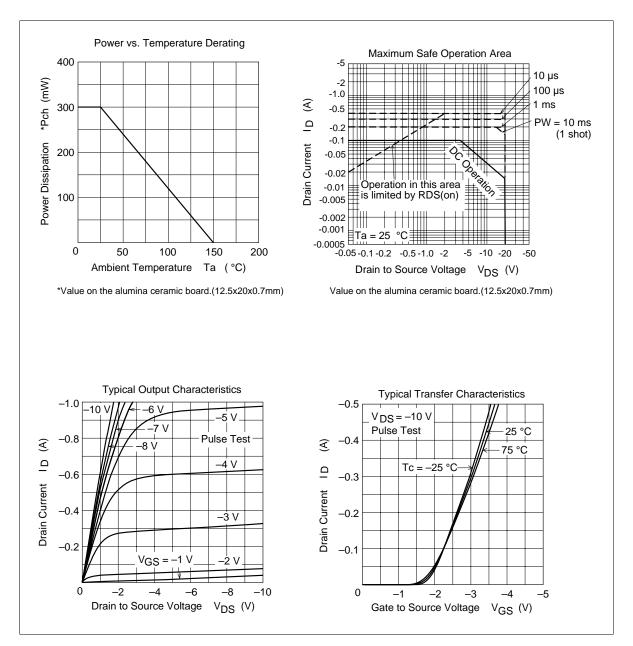
Electrical Characteristics (Ta = 25°C)

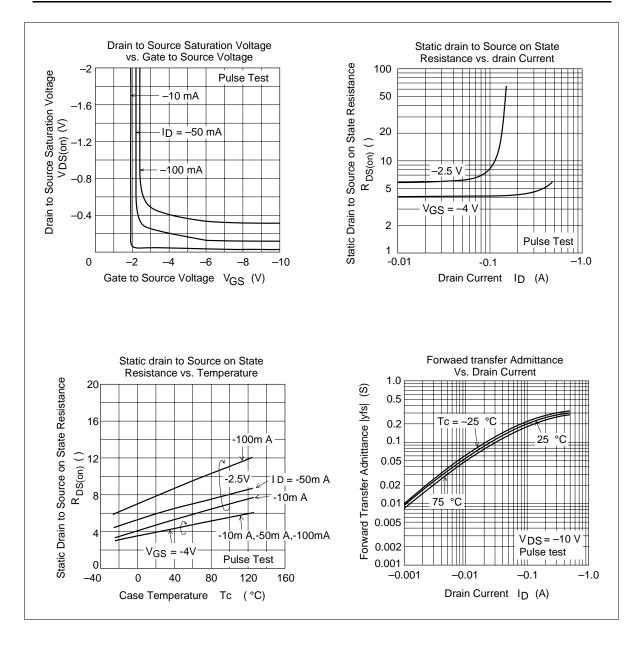
Item	Symbol	Min	Тур	Мах	Unit	Test Conditions
Drain to source breakdown voltage	$V_{\rm (BR)DSS}$	-20	—	—	V	$I_{\rm D}$ = -100 µA, $V_{\rm GS}$ = 0
Gate to source breakdown voltage	$V_{\rm (BR)GSS}$	±10	—	—	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±5	μA	$V_{GS} = \pm 8 V, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	—	-1	μΑ	$V_{\rm DS} = -20$ V, $V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-0.8	_	-1.8	V	$I_{\rm D}$ = -10 μ A, $V_{\rm DS}$ = -5 V
Static drain to source on state	$R_{\text{DS(on)}}$	_	4.1	5.0	Ω	$I_{\rm D}$ = -50 mA, $V_{\rm GS}$ = -4 V ^{Note 3}
resistance	R _{DS(on)}	_	6.0	8.5	Ω	$I_{\rm D}$ = -50 mA, $V_{\rm GS}$ = -2.5 V ^{Note 3}
Forward transfer admittance	y _{fs}	94	144	—	mS	I_D = -50 mA, V_{DS} = -10 V ^{Note 3}
Input capacitance	Ciss	—	28	—	pF	V _{DS} = -10 V
Output capacitance	Coss	_	21	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	7	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	30	_	ns	$I_{\rm D}$ = -50 mA, $V_{\rm GS}$ = -4 V
Rise time	t,	_	90	_	ns	R _L = 200 Ω
Turn-off delay time	t _{d(off)}	_	87	—	ns	
Fall time	t _f	—	97	_	ns	

Note: 3. Pulse test

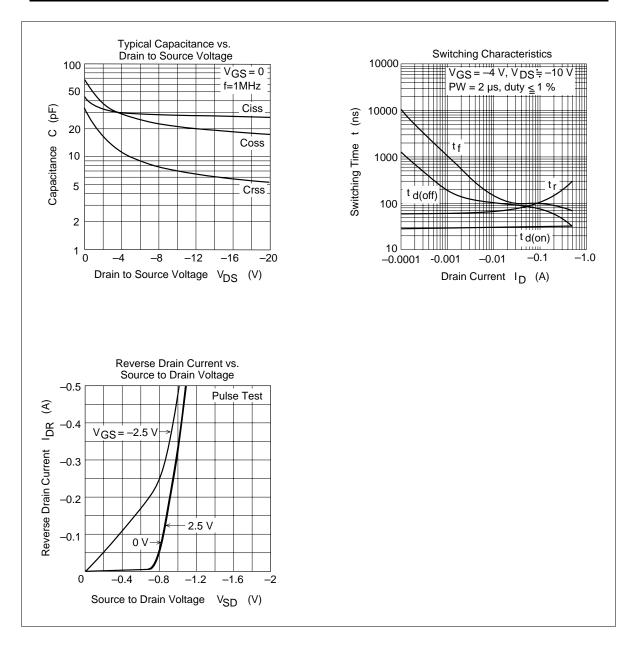
4. Marking is CP

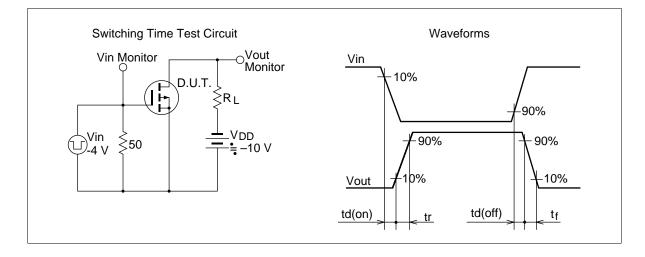






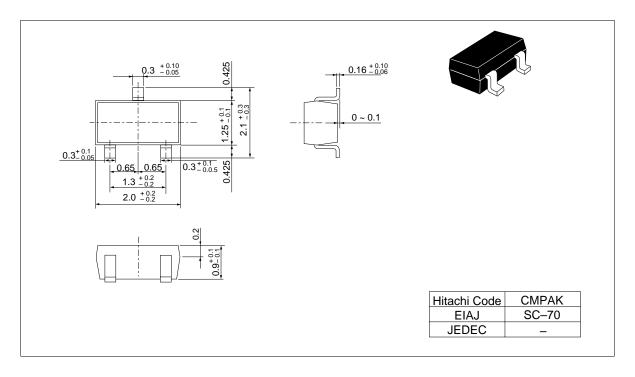
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Package Dimensions

Unit: mm



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