

# 2SK1167, 2SK1168

Silicon N Channel MOS FET

REJ03G0915-0200 (Previous: ADE-208-1253) Rev.2.00 Sep 07, 2005

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# Application

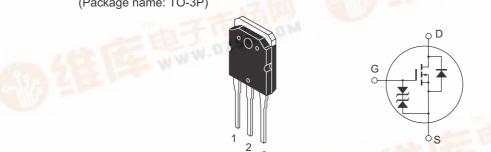
High speed power switching

### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

## Outline

RENESAS Package code: PRSS0004ZE-A (Package name: TO-3P)









# **Absolute Maximum Ratings**

				$(Ta = 25^{\circ}C)$
Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1167	V <sub>DSS</sub>	450	V
	2SK1168		500	
Gate to source voltage		V <sub>GSS</sub>	±30	V
Drain current		Ι <sub>D</sub>	15	А
Drain peak current		I <sub>D(pulse)</sub> * <sup>1</sup>	60	А
Body to drain diode reverse drain current		I <sub>DR</sub>	15	А
Channel dissipation		Pch* <sup>2</sup>	100	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at  $T_C = 25^{\circ}C$ 

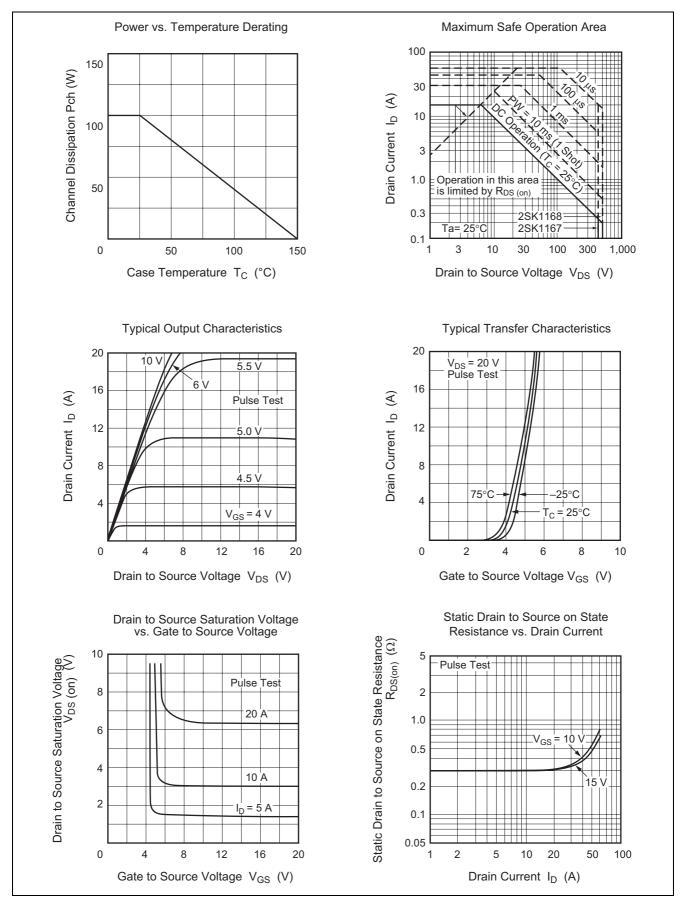
# **Electrical Characteristics**

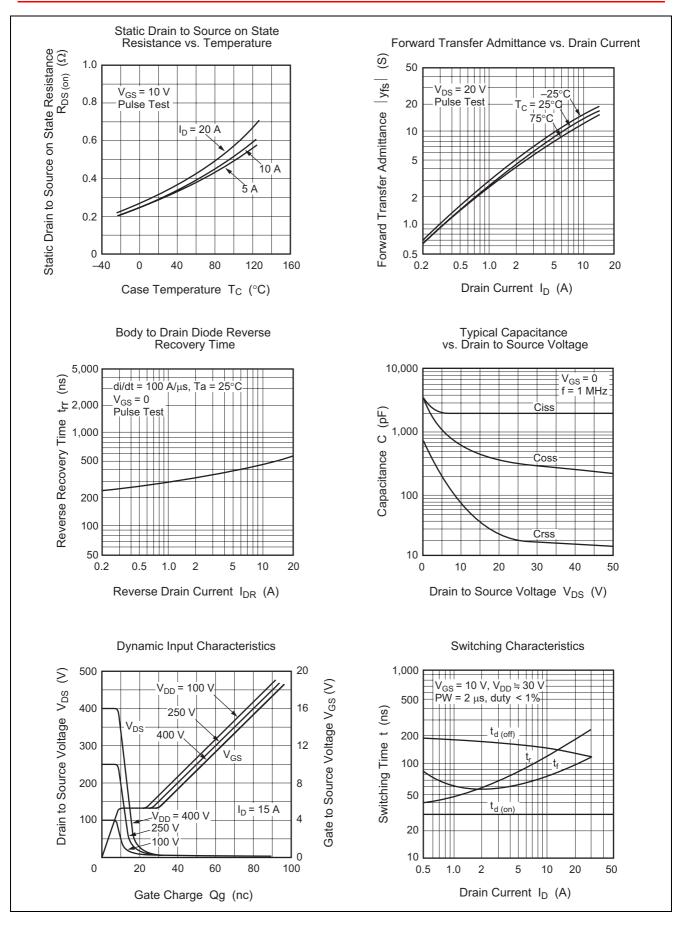
Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source	2SK1167	V <sub>(BR)DSS</sub>	450	_	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
breakdown voltage	2SK1168	Γ	500				
Gate to source breakdown voltage		V <sub>(BR)GSS</sub>	±30	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current		I <sub>GSS</sub>			±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	2SK1167	I <sub>DSS</sub>		_	250	μΑ	$V_{DS} = 360 \text{ V}, V_{GS} = 0$
	2SK1168						$V_{DS} = 400 \text{ V}, \text{ V}_{GS} = 0$
Gate to source cutoff voltage		V <sub>GS(off)</sub>	2.0	_	3.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	2SK1167	R <sub>DS(on)</sub>		0.25	0.36	Ω	$I_D = 8 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$
	2SK1168			0.30	0.40		
Forward transfer admittance		y <sub>fs</sub>	8	13	—	S	$I_D = 8 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$
Input capacitance		Ciss	_	2050	—	pF	$V_{DS} = 10 V, V_{GS} = 0,$
Output capacitance		Coss	_	600	—	pF	f = 1 MHz
Reverse transfer capacitance		Crss	_	75	—	pF	7
Turn-on delay time		t <sub>d(on)</sub>	_	30	—	ns	$I_D = 8 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time		tr	_	110	—	ns	R <sub>L</sub> = 3.75 Ω
Turn-off delay time		t <sub>d(off)</sub>	_	150	—	ns	7
Fall time		t <sub>f</sub>	_	70	—	ns	7
Body to drain diode forward voltage		V <sub>DF</sub>	_	1.0	—	V	$I_F = 15 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery		t <sub>rr</sub>		500	_	ns	$I_F = 15 \text{ A}, V_{GS} = 0,$
time							di <sub>F</sub> /dt = 100 A/µs

Note: 3. Pulse test

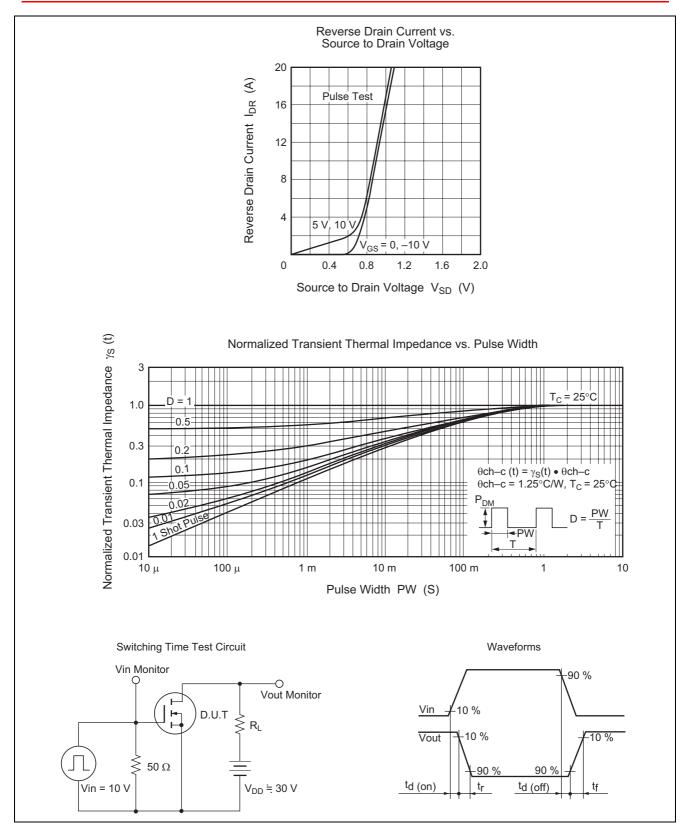
### 2SK1167, 2SK1168

### **Main Characteristics**

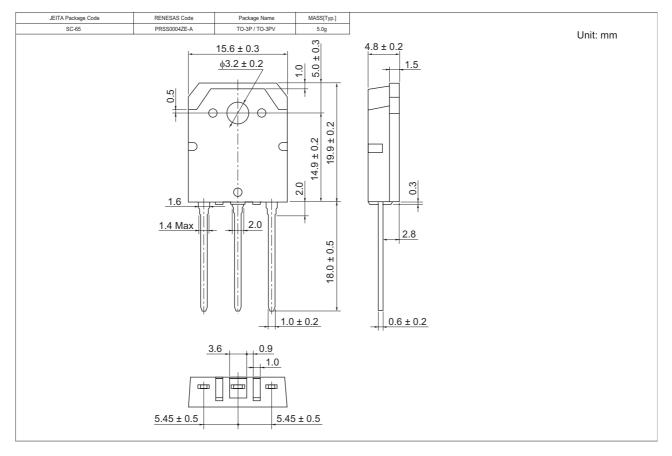




### 2SK1167, 2SK1168



# **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
2SK1167-E	360 pcs	Box (Tube)
2SK1168-E	360 pcs	Box (Tube)

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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