

No.2550

15

6

0.5

20

рF

pF

Ω

N-Channel Enhancement MOS Silicon FET

Analog Switch Applications

## **Features**

- . Large  $|Y_{fs}|$  . Enhancement type

Input Capacitance

Reverse Transfer

Capacitance ON Resistance

Output Capacitance

. Small ON resistance

Absolute Maximum Ratings a Drain to Source Voltage Gate to Source Voltage Drain Current Drain Current (Pulse) Allowable Power Dissipat Channel Temperature Storage Temperature	V <sub>DS</sub> V <sub>GS</sub> I <sub>D</sub> I <sub>DP</sub>		50 ±12 100 300 200 125 • +125	unit V V mA mA oC	75C	
Electrical Characteristics Drain to Source Breakdown Voltage		C I <sub>D</sub> =10uA, V <sub>GS</sub> =0V	min 50	typ	max	unit V
Gate Cutoff Current Cutoff Voltage Drain Current Forward Transfer Admittance	IGSS IGS(off) IDSS  Yfs	$V_{GS}=10V, V_{DS}=0V$ $V_{DS}=10V, I_{D}=100\mu A$ $V_{DS}=20V, V_{GS}=0V$ $V_{DS}=10V, I_{D}=50mA, f=1kHz$	_	0.01 0.9 40	10 1.5 1	nA V μA mS

RDS(on) VGS=10V, ID=10mA

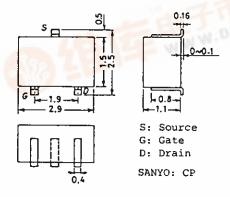
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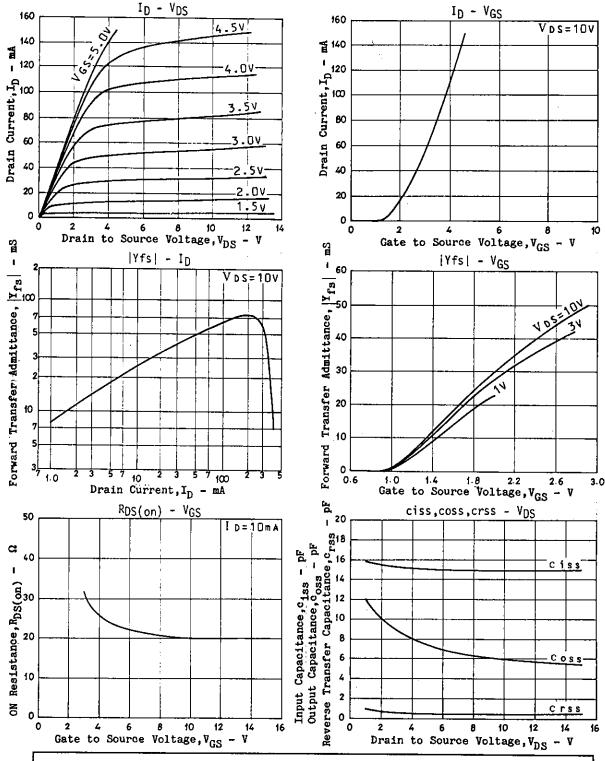
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c<sub>rss</sub>

 $\begin{array}{l} v_{\rm DS}\!=\!10\text{V}, v_{\rm GS}\!=\!0\,, \text{f=1MHz} \\ v_{\rm DS}\!=\!10\text{V}, v_{\rm GS}\!=\!0\,, \text{f=1MHz} \\ v_{\rm DS}\!=\!10\text{V}, v_{\rm GS}\!=\!0\,, \text{f=1MHz} \end{array}$ 

## Package Dimensions 2024A (unit:mm)





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