查询2SK4100LS供应商



# SANYO Semiconductors DATA SHEET

# N-Channel Silicon MOSFET **General-Purpose Switching Device Applications**

### Features

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- Attachment workability is good by Mica-less package.
- · Avalanche resistance guarantee.

2SK4100LS

# Specifications

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		650	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	IDc*1	Limited only by maximum temperature	6	Α
	IDpack*2	SANYO's ideal heat dissipation condition	5.6	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	24	Α
Allowable Power Dissipation	PD		2.0	W
		Tc=25°C (SANYO's ideal heat dissipation condition)	33	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *3	EAS		105	mJ
Avalanche Current *4	IAV		6	А
1 Shows chip capability	•	100	WW.DL	
2 Package limited				
0				

\*3 VDD=99V, L=5mH, IAV=6A

\*4 L≤5mH, single pulse

Marking: K4100

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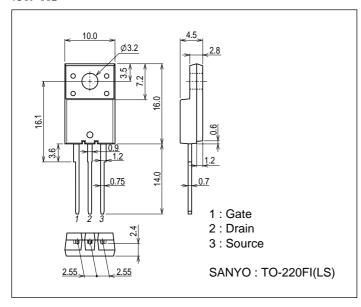
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

### Electrical Characteristics at Ta=25°C

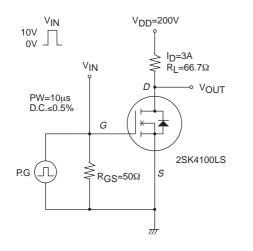
Parameter	Symbol	Conditions	Ratings			Linit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	650			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =520V, V <sub>GS</sub> =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V			±100	nA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	3		5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =3A	2	4		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	ID=3A, VGS=10V		1.05	1.35	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =30V, f=1MHz		600		pF
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		110		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =30V, f=1MHz		24		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		18		ns
Rise Time	tr	See specified Test Circuit.		41		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		78		ns
Fall Time	tf	See specified Test Circuit.		28		ns
Total Gate Charge	Qg	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =6A		23		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =6A		4.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =6A		13		nC
Diode Forward Voltage	VSD	IS=6A, VGS=0V		0.9	1.2	V

### Package Dimensions

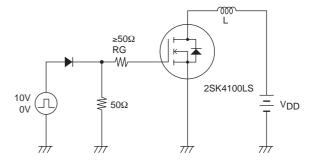
unit : mm (typ) 7509-002



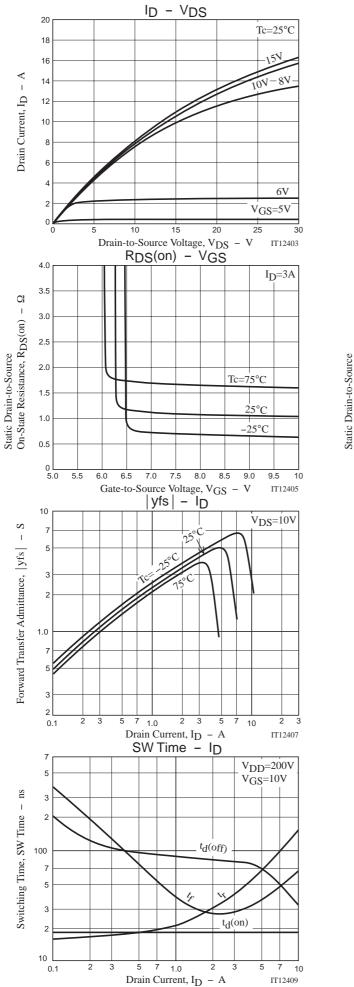
#### **Switching Time Test Circuit**

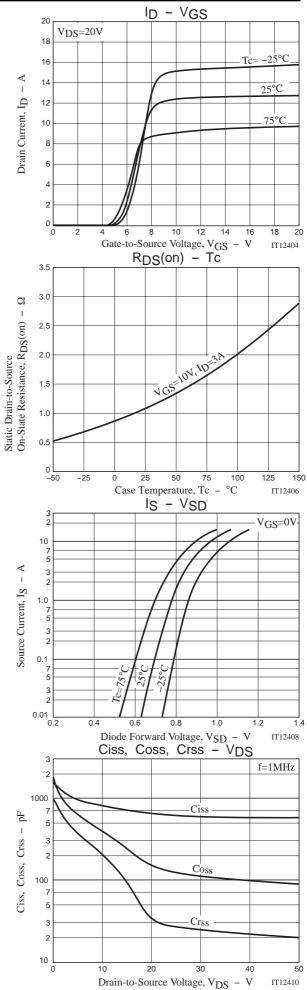


#### **Avalanche Resistance Test Circuit**

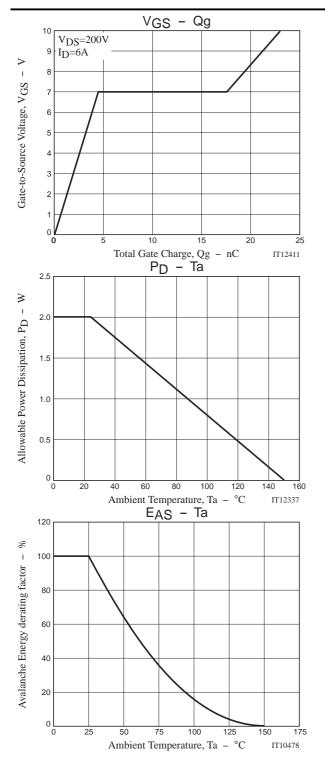


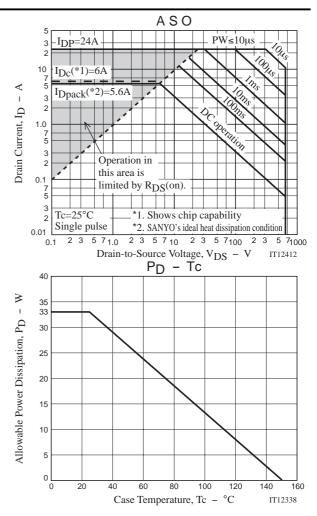
## 2SK4100LS





## 2SK4100LS





Note on usage : Since the 2SK4100LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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