查询2SK4123LS供应商



SANYO Semiconductors DATA SHEET

N-Channel Silicon MOSFET **General-Purpose Switching Device Applications**

Features

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- Attachment workability is good by Mica-less package.
- Avalanche resistance guarantee.

2SK4123LS

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		450	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	IDc*1	Limited only by maximum temperature	18	Α
	IDpack*2	SANYO's ideal heat dissipation condition	12.4	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	66	Α
Allowable Power Dissipation	PD		2.0	W
		Tc=25°C (SANYO's ideal heat dissipation condition)	40	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *3	EAS		412	mJ
Avalanche Current *4	IAV		18	А
1 Shows chip capability	•	1 44 192	WW.BL	
2 Package limited				
0				

- *3 VDD=99V, L=2mH, IAV=18A
- *4 L≤2mH, single pulse

Marking : K4123

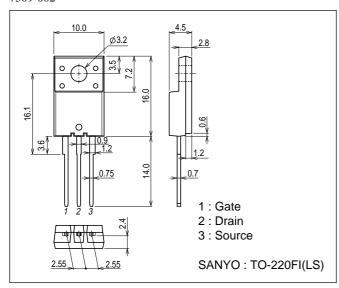
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Electrical Characteristics at Ta=25°C

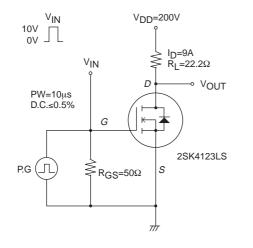
Parameter	Symbol	Conditions	Ratings			Linit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	450			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =360V, V _{GS} =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±30V, V _{DS} =0V			±100	nA
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	3		5	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =9A	4.5	9		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)	I _D =9A, V _{GS} =10V		0.26	0.34	Ω
Input Capacitance	Ciss	V _{DS} =30V, f=1MHz		1200		pF
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		280		pF
Reverse Transfer Capacitance	Crss	V _{DS} =30V, f=1MHz		60		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		28		ns
Rise Time	tr	See specified Test Circuit.		97		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		140		ns
Fall Time	tf	See specified Test Circuit.		56		ns
Total Gate Charge	Qg	V _{DS} =200V, V _{GS} =10V, I _D =18A		47.2		nC
Gate-to-Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =18A		7.8		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =200V, V _{GS} =10V, I _D =18A		28.4		nC
Diode Forward Voltage	VSD	IS=18A, VGS=0V		0.9	1.2	V

Package Dimensions

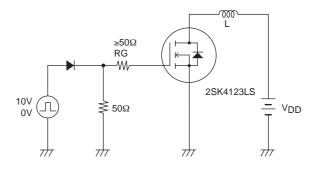
unit : mm (typ) 7509-002



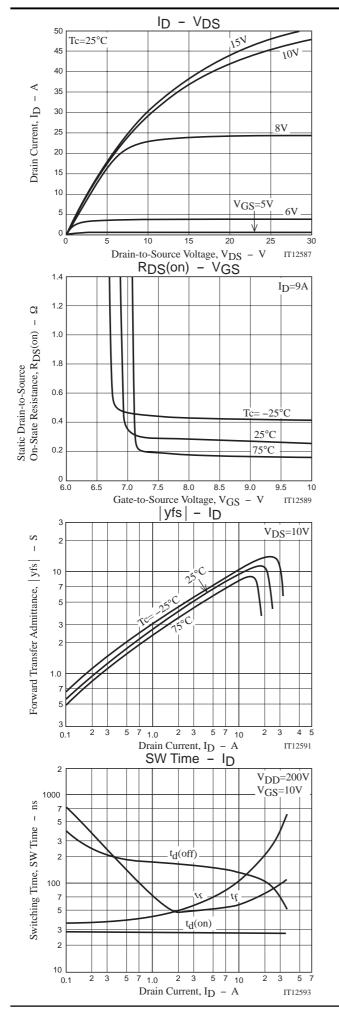
Switching Time Test Circuit

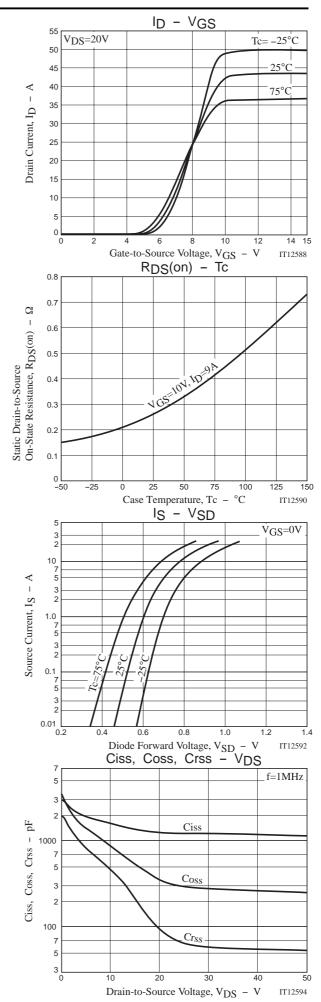


Avalanche Resistance Test Circuit

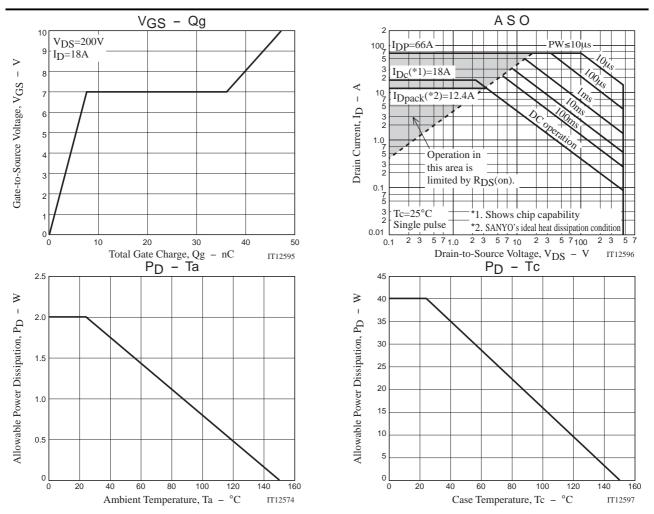


2SK4123LS





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Note on usage : Since the 2SK4123LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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