

# 2SK972

Silicon N-Channel MOS FET

## Application

High speed power switching

## Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device
  - Can be driven from 5 V source
- Suitable for motor drive, DC-DC converter, power switch and solenoid drive

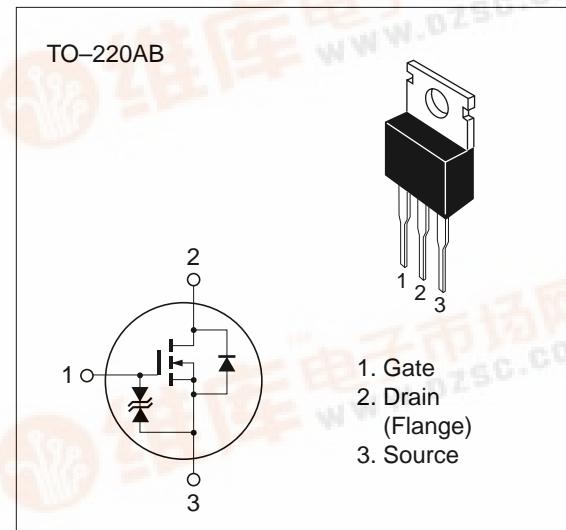


Table 1 Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	60	V
Gate to source voltage	$V_{GSS}$	$\pm 20$	V
Drain current	$I_D$	25	A
Drain peak current	$I_{D(\text{pulse})}^*$	100	A
Body to drain diode reverse drain current	$I_{DR}$	25	A
Channel dissipation	$P_{ch}^{**}$	50	W
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

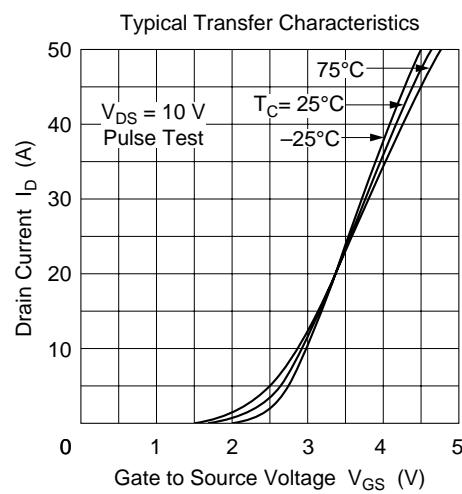
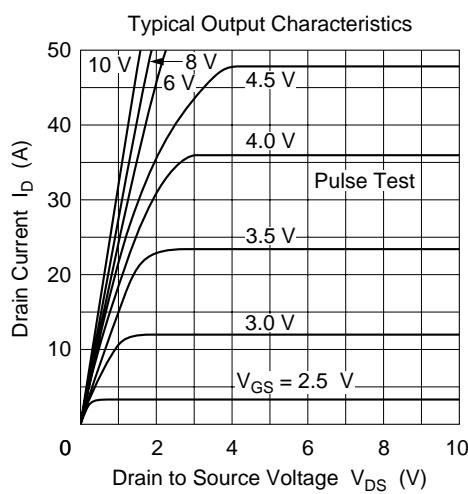
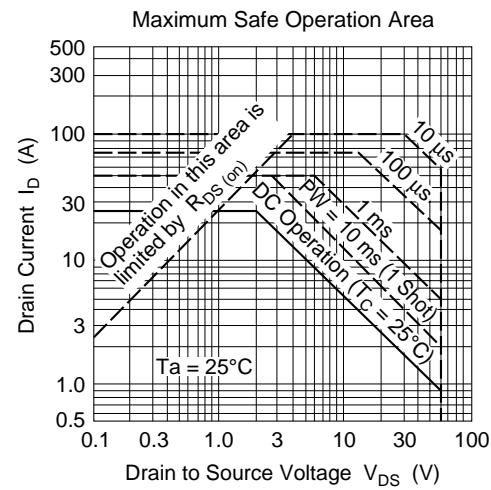
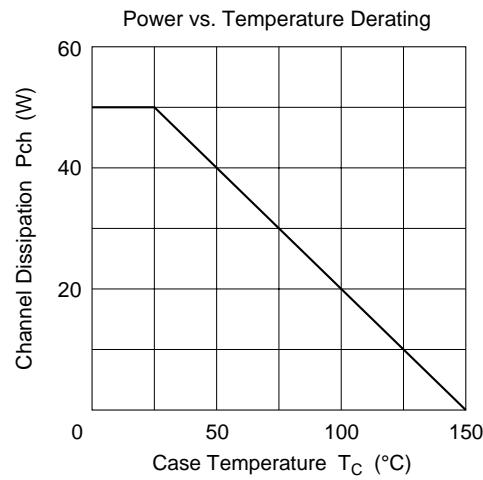
\* PW  $\leq 10 \mu\text{s}$ , duty cycle  $\leq 1\%$

\*\* Value at  $T_C = 25^\circ\text{C}$

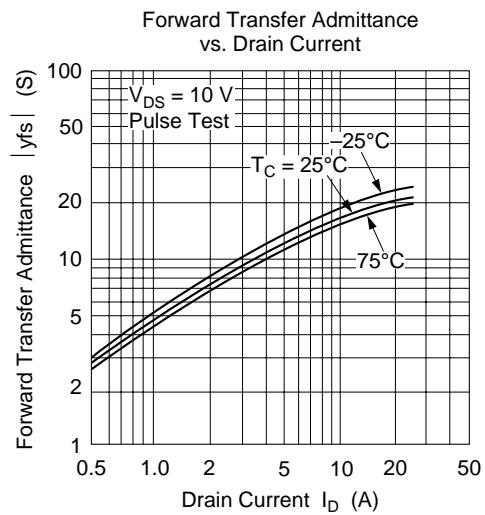
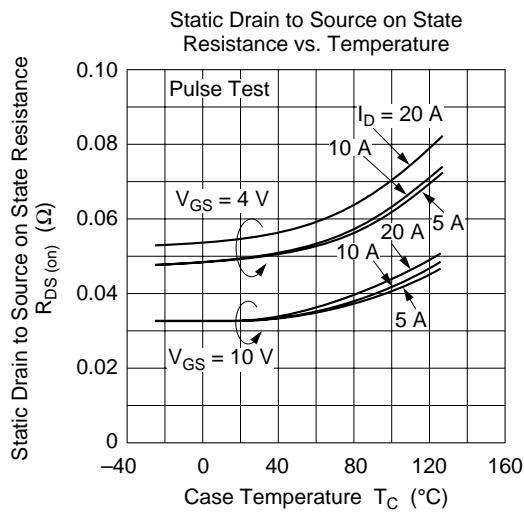
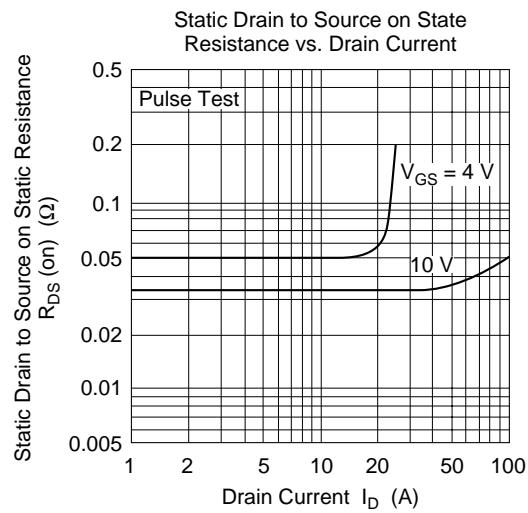
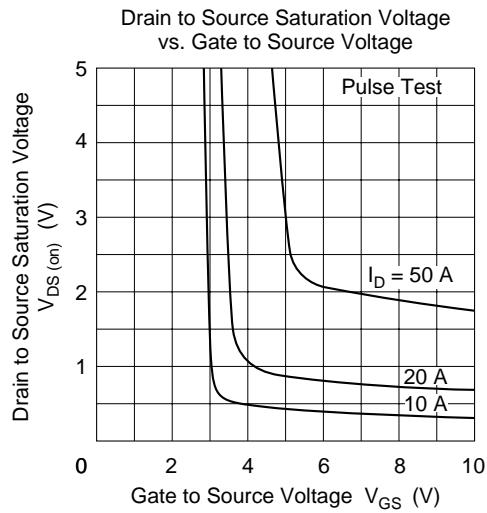
**Table 2 Electrical Characteristics (Ta = 25°C)**

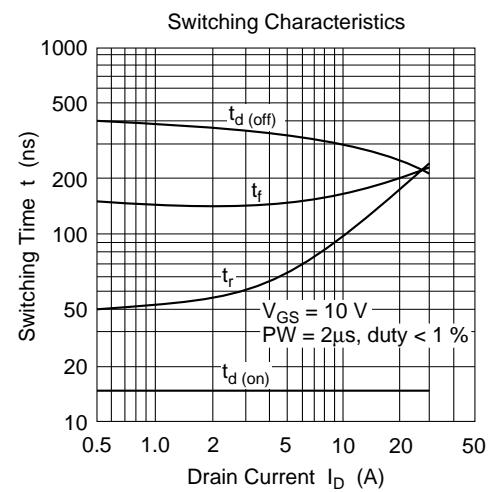
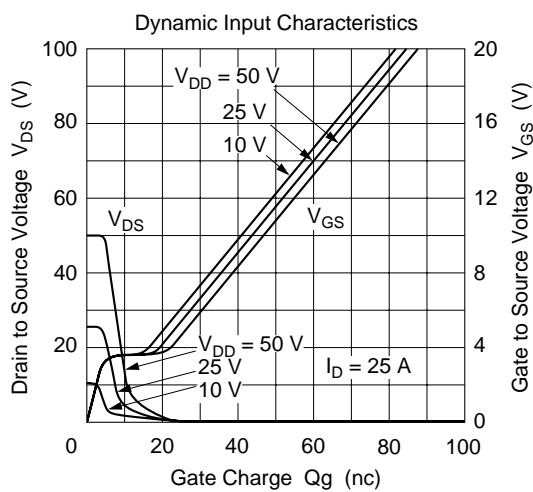
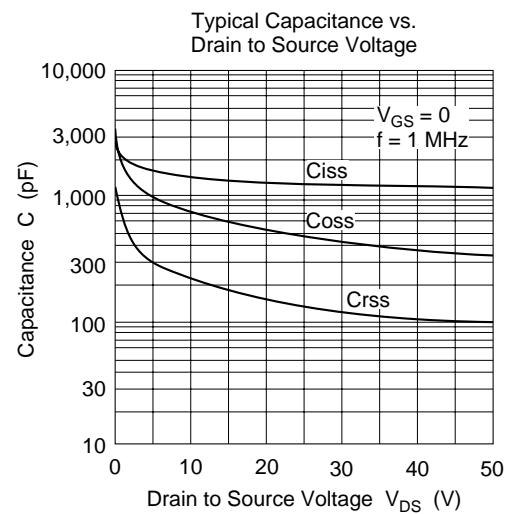
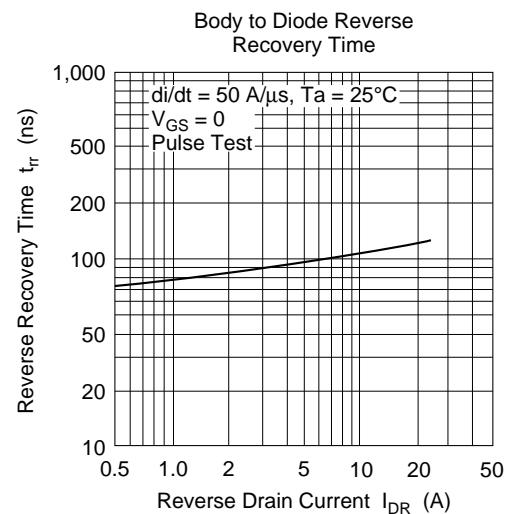
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	60	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20	—	—	V	I <sub>G</sub> = ±100 µA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	250	µA	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.0	—	2.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	0.033 0.05	0.04 0.06	Ω	I <sub>D</sub> = 15 A, V <sub>GS</sub> = 10 V * I <sub>D</sub> = 15 A, V <sub>GS</sub> = 4 V *
Forward transfer admittance	y <sub>fs</sub>	12	20	—	S	I <sub>D</sub> = 15 A, V <sub>DS</sub> = 10 V *
Input capacitance	C <sub>iss</sub>	—	1400	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0,
Output capacitance	C <sub>oss</sub>	—	720	—	pF	f = 1 MHz
Reverse transfer capacitance	C <sub>rss</sub>	—	220	—	pF	
Turn-on delay time	t <sub>d(on)</sub>	—	15	—	ns	I <sub>D</sub> = 15 A, V <sub>GS</sub> = 10 V,
Rise time	t <sub>r</sub>	—	130	—	ns	R <sub>L</sub> = 2 Ω
Turn-off delay time	t <sub>d(off)</sub>	—	270	—	ns	
Fall time	t <sub>f</sub>	—	180	—	ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	1.3	—	V	I <sub>F</sub> = 25 A, V <sub>GS</sub> = 0
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	135	—	ns	I <sub>F</sub> = 25 A, V <sub>GS</sub> = 0, di <sub>F</sub> /dt = 50 A/µs

\* Pulse Test

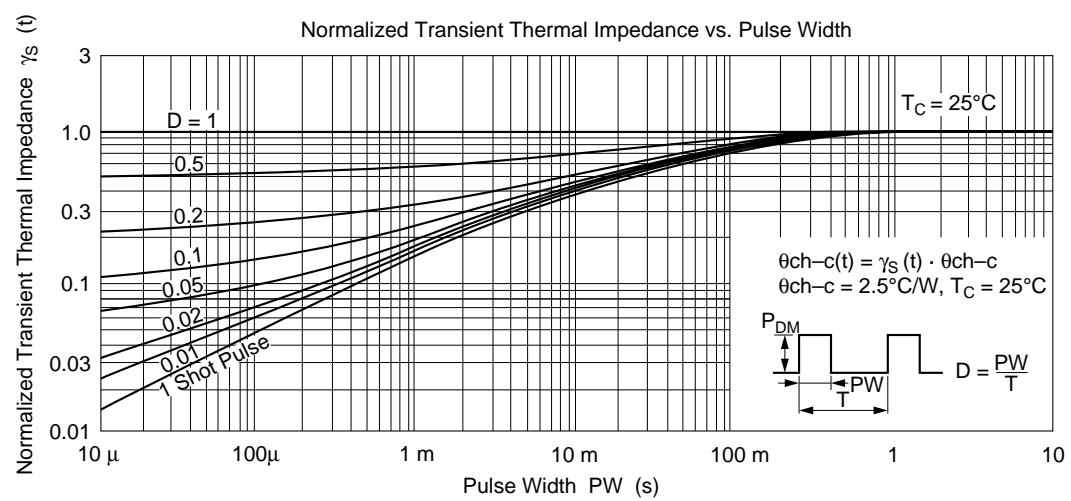
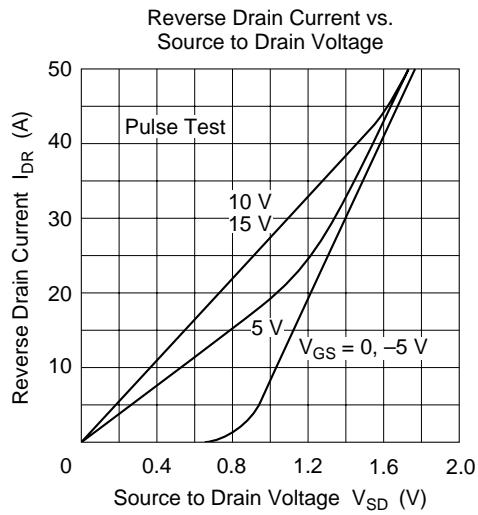


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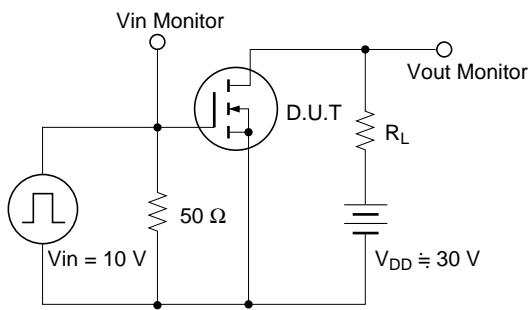




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Switching Time Test Circuit



Wavewforms

