

# 2SK974L, 2SK974S

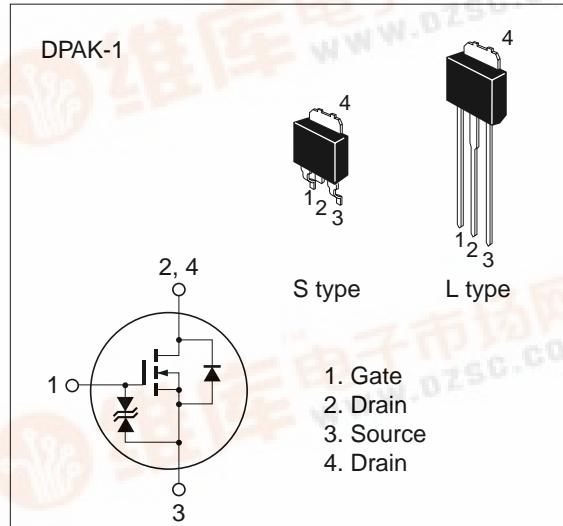
Silicon N-Channel MOS FET

## Application

High speed power switching

## Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device
  - Can be driven from 5 V source
- Suitable for motor drive, DC-DC converter, power switch and solenoid drive



**Table 1 Absolute Maximum Ratings (Ta = 25°C)**

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	3	A
Drain peak current	I <sub>D(peak)*</sub>	12	A
Body to drain diode reverse drain current	I <sub>DR</sub>	3	A
Channel dissipation	P <sub>ch</sub> **	20	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* PW ≤ 10 μs, duty cycle ≤ 1 %

\*\* Value at T<sub>C</sub> = 25 °C

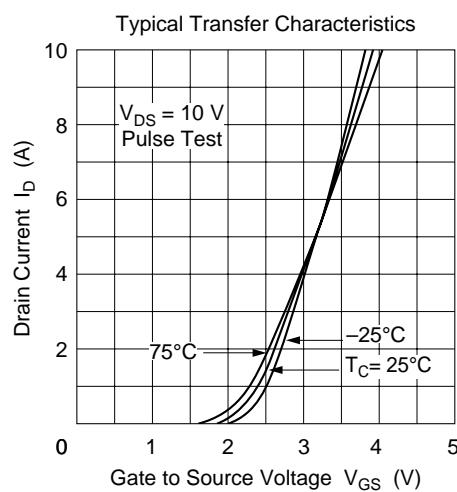
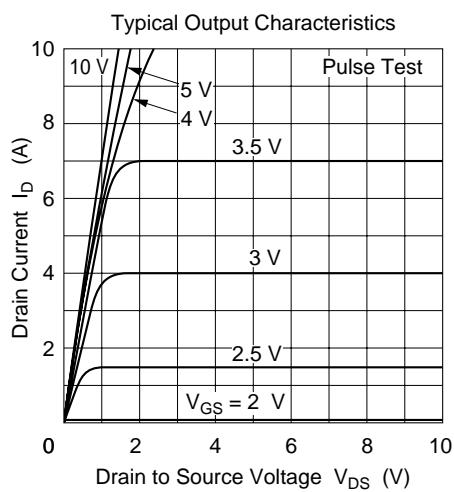
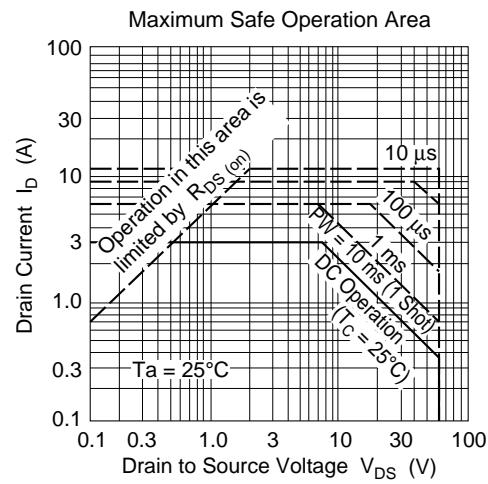
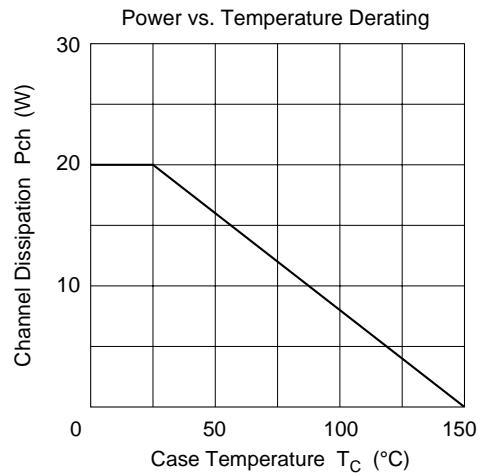
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**Table 2 Electrical Characteristics (Ta = 25°C)**

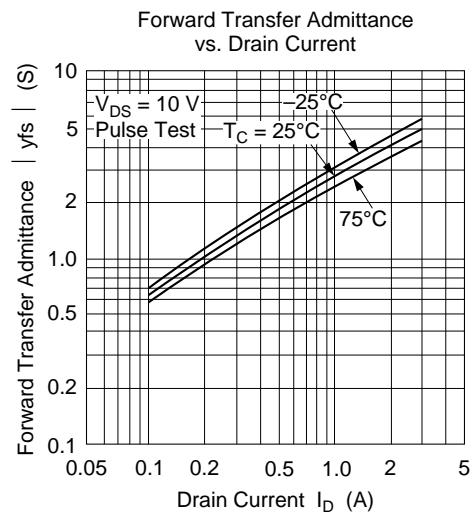
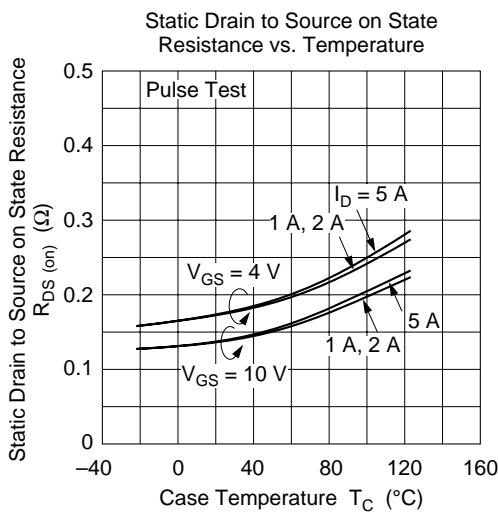
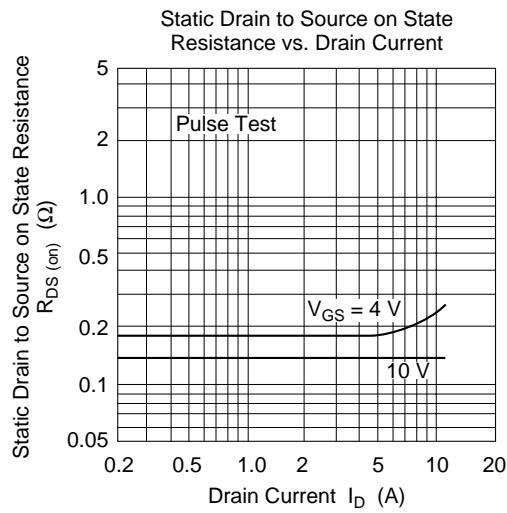
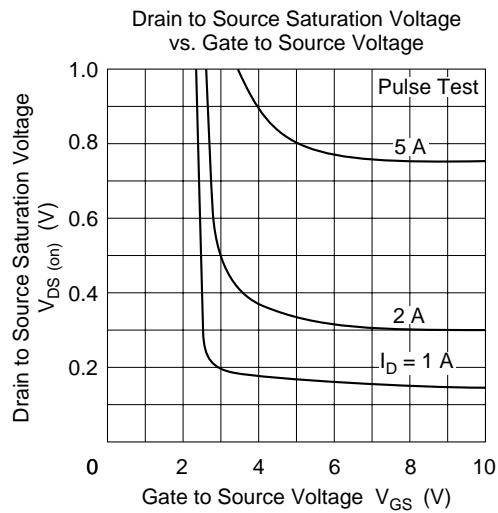
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	60	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20	—	—	V	I <sub>G</sub> = ±100 µA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	100	µA	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.0	—	2.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	0.15 0.20	0.18 0.25	Ω	I <sub>D</sub> = 2 A, V <sub>GS</sub> = 10 V * I <sub>D</sub> = 2 A, V <sub>GS</sub> = 4 V *
Forward transfer admittance	y <sub>fs</sub>	2.4	4.0	—	S	I <sub>D</sub> = 2 A, V <sub>DS</sub> = 10 V *
Input capacitance	C <sub>iss</sub>	—	400	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0,
Output capacitance	C <sub>oss</sub>	—	230	—	pF	f = 1 MHz
Reverse transfer capacitance	C <sub>rss</sub>	—	60	—	pF	
Turn-on delay time	t <sub>d(on)</sub>	—	5	—	ns	I <sub>D</sub> = 2 A, V <sub>GS</sub> = 10 V,
Rise time	t <sub>r</sub>	—	25	—	ns	R <sub>L</sub> = 15 Ω
Turn-off delay time	t <sub>d(off)</sub>	—	180	—	ns	
Fall time	t <sub>f</sub>	—	75	—	ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	0.9	—	V	I <sub>F</sub> = 3 A, V <sub>GS</sub> = 0
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	85	—	ns	I <sub>F</sub> = 3 A, V <sub>GS</sub> = 0, diF/dt = 50 A/µs

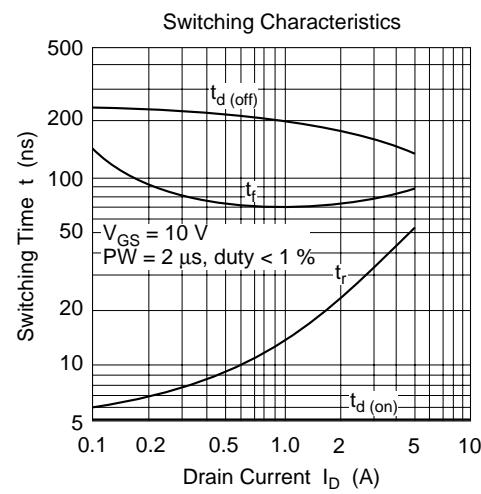
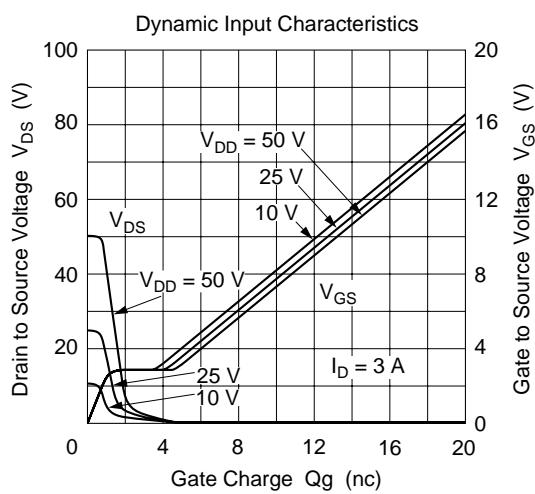
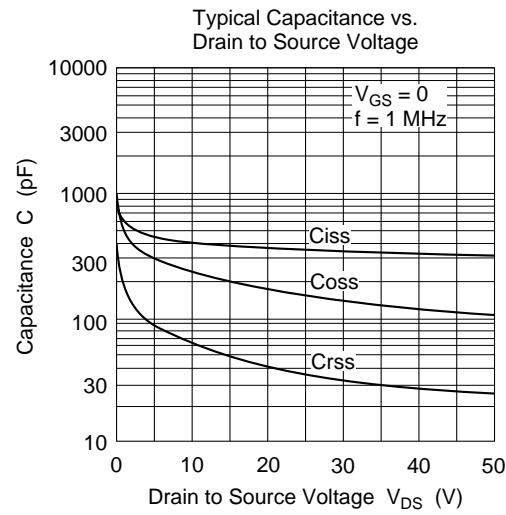
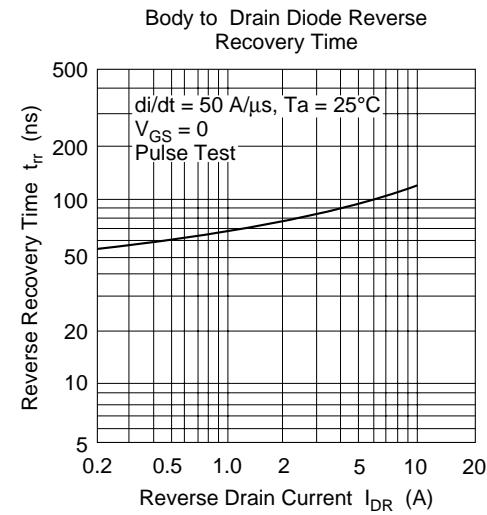
\* Pulse Test

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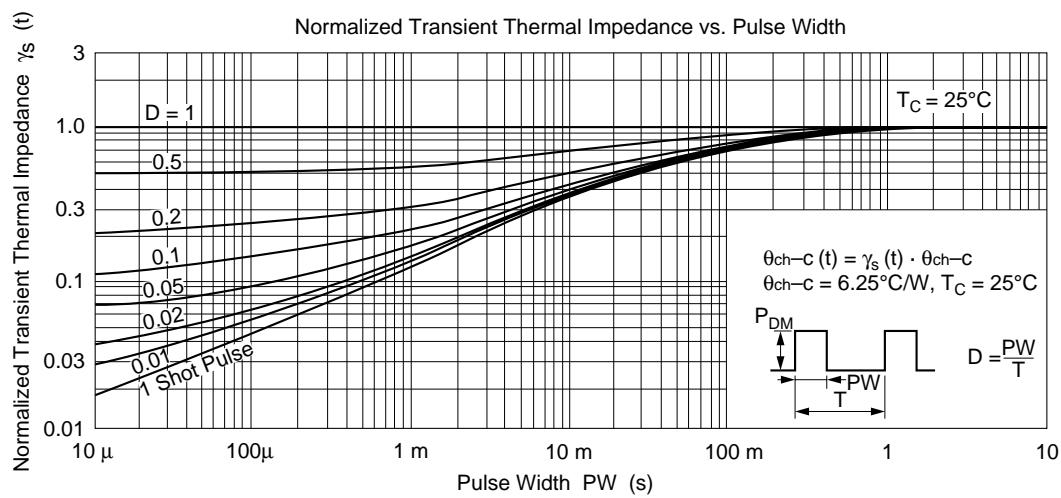
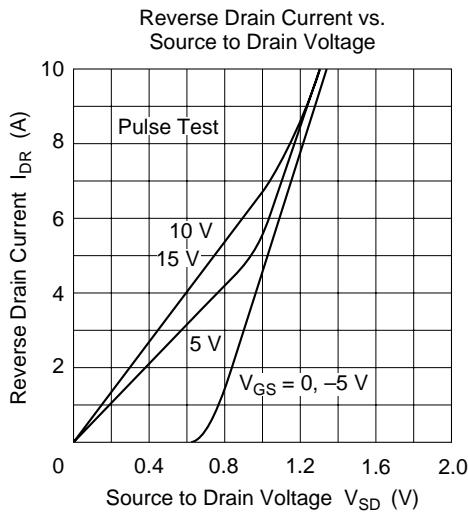


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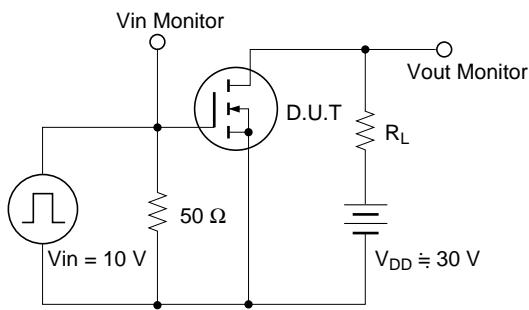




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Switching Time Test Circuit



Wavewforms

