

SHINDENGEN

VX-2 Series Power MOSFET

N-Channel Enhancement type

**2SK2198
(F30Z50VX2)**

500V 30A

FEATURES

- Input capacitance (C_{iss}) is small.
Especially, input capacitance at 0 bias is small.
- The static $R_{ds(on)}$ is small.
- The switching time is fast.

APPLICATION

- Switching power supply of AC 100V input
- High voltage power supply
- Inverter

RATINGS

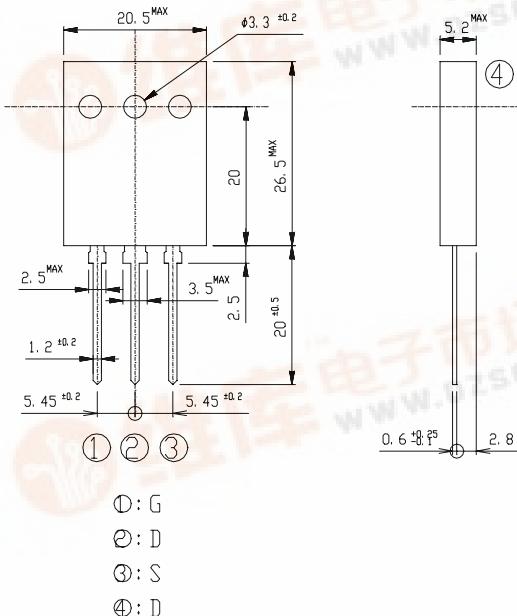
● Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$)

Item	Symbol	Conditions	Ratings	Unit
Storage Temperature	T_{stg}	$T_{ch} = 25^\circ\text{C}$	-55~150	$^\circ\text{C}$
Channel Temperature	T_{ch}		150	
Drain-Source Voltage	V_{DSS}	$T_{ch} = 25^\circ\text{C}$	500	V
Gate-Source Voltage	V_{GSS}		± 30	
Continuous Drain Current (DC)	I_D	$T_{ch} = 25^\circ\text{C}$	30	A
Continuous Drain Current (Peak)	I_{DP}		90	
Continuous Source Current (DC)	I_S		30	
Total Power Dissipation	P_T		220	W
Single Pulse Avalanche Current	I_{AS}	$T_{ch} = 25^\circ\text{C}$	30	A
Mounting Torque	T_{OR}	(Recommended torque : 0.5N·m)	0.8	N·m

OUTLINE DIMENSIONS

Case : MTO-3L

(Unit : mm)



①: G

②: D

③: S

④: D

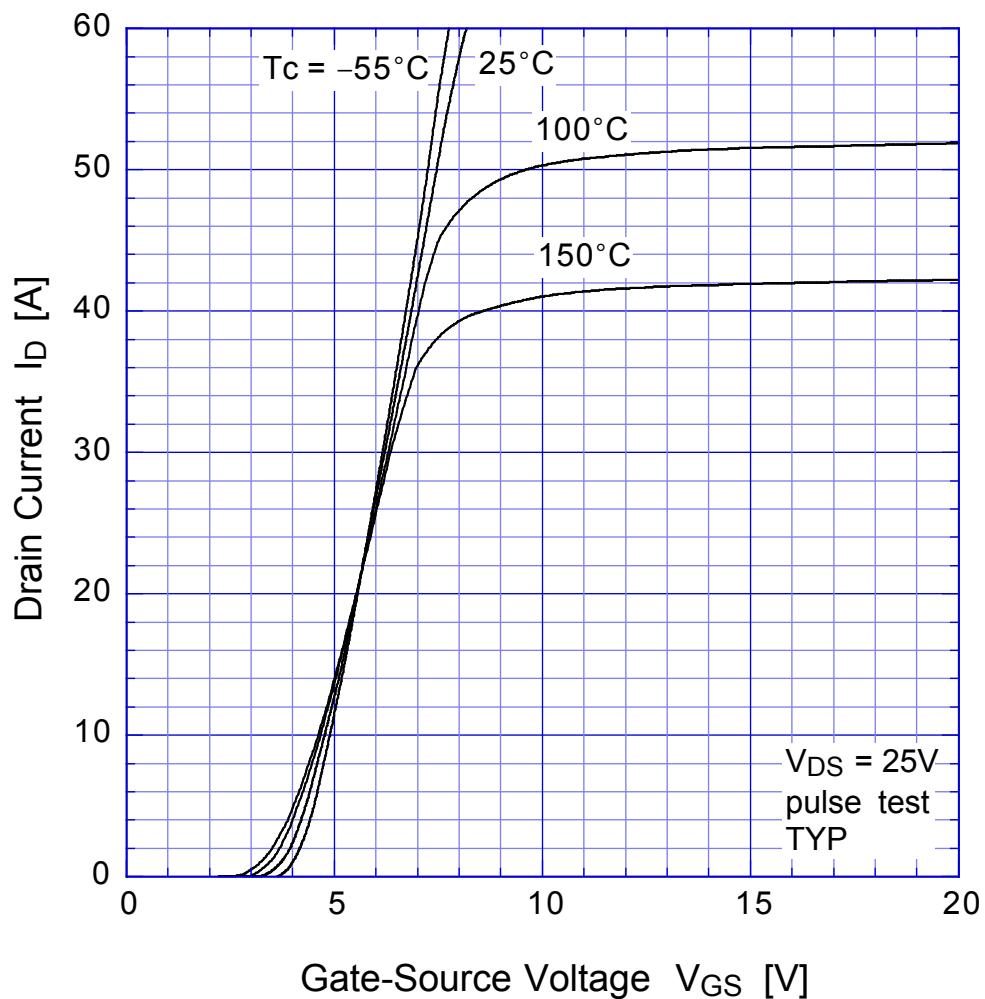
VX-2 Series Power MOSFET

2SK2198 (F30Z50VX2)

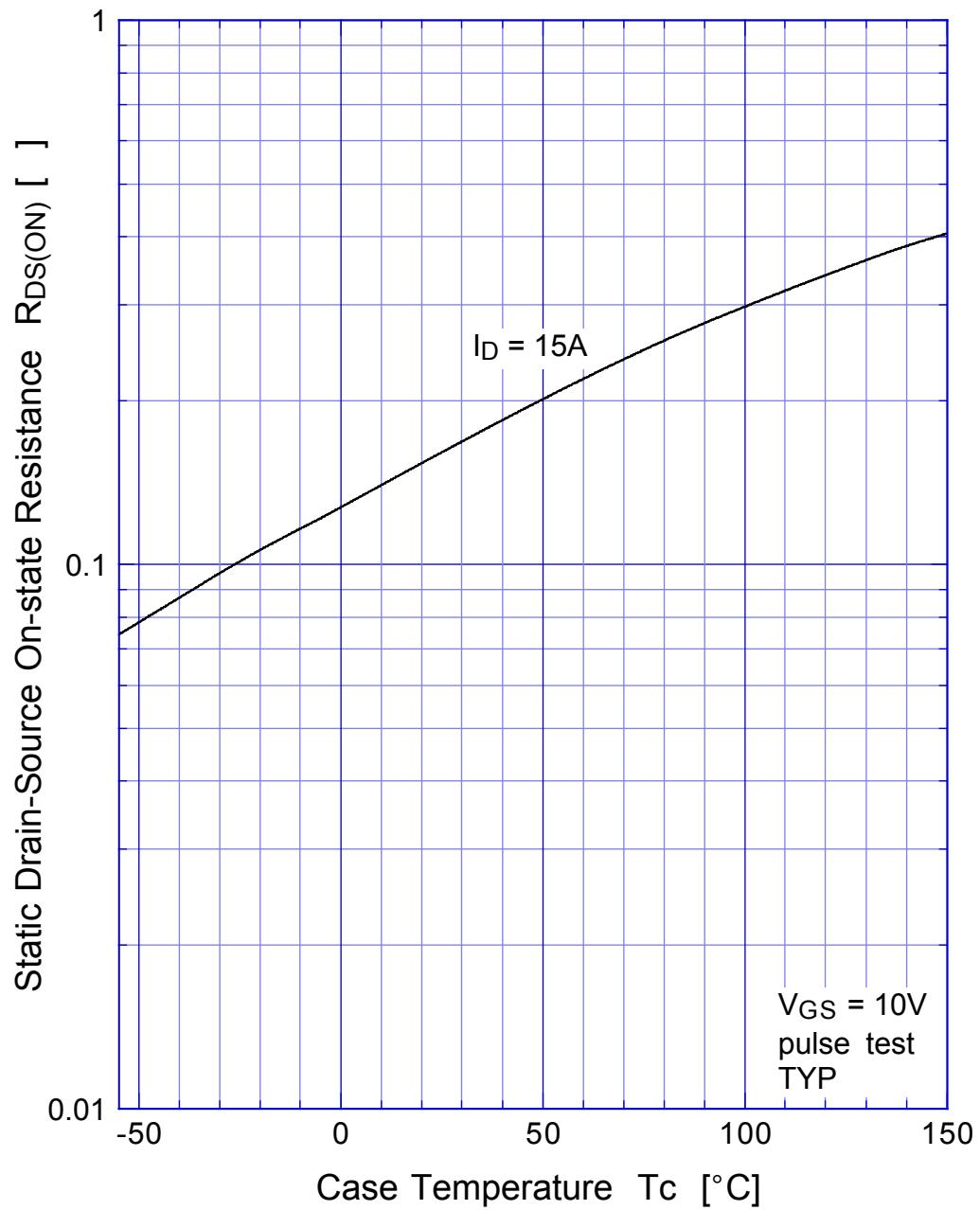
● Electrical Characteristics $T_c = 25^\circ\text{C}$

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$\text{ID} = 1\text{mA}, \text{VGS} = 0\text{V}$	500			V
Zero Gate Voltage Drain Current	ID_{SS}	$\text{VDS} = 500\text{V}, \text{VGS} = 0\text{V}$			250	μA
Gate-Source Leakage Current	I_{GSS}	$\text{VGS} = \pm 30\text{V}, \text{VDS} = 0\text{V}$			± 0.1	
Forward Transconductance	g_{fs}	$\text{ID} = 15\text{A}, \text{VDS} = 10\text{V}$	9	20		S
Static Drain-Source On-state Resistance	$R_{\text{DS}(\text{ON})}$	$\text{ID} = 15\text{A}, \text{VGS} = 10\text{V}$		0.16	0.23	Ω
Gate Threshold Voltage	V_{TH}	$\text{ID} = 3\text{mA}, \text{VDS} = 10\text{V}$	2.5	3.0	3.5	V
Source-Drain Diode Forward Voltage	V_{SD}	$\text{IS} = 15\text{A}, \text{VGS} = 0\text{V}$			1.5	
Thermal Resistance	θ_{JC}	junction to case			0.568	$^\circ\text{C}/\text{W}$
Total Gate Charge	Q_g	$\text{VDD} = 400\text{V}, \text{VGS} = 10\text{V}, \text{ID} = 30\text{A}$		125		nC
Input Capacitance	C_{iss}	$\text{VDS} = 10\text{V}, \text{VGS} = 0\text{V}, f = 1\text{MHz}$		3700		pF
Reverse Transfer Capacitance	C_{rss}			230		
Output Capacitance	C_{oss}			770		
Turn-On Time	t_{on}	$\text{ID} = 15\text{A}, \text{VGS} = 10\text{V}, \text{RL} = 10\Omega$		200	375	ns
Turn-Off Time	t_{off}			500	900	

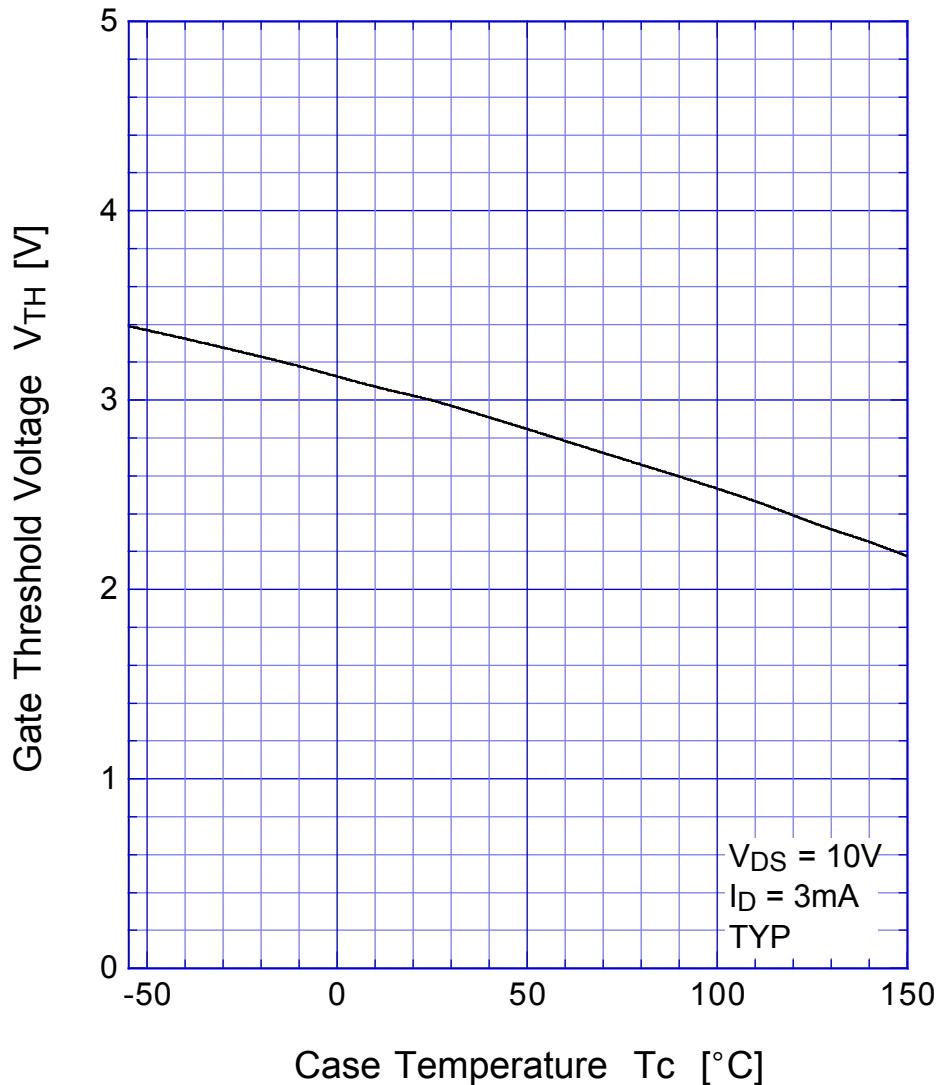
2SK2198 Transfer Characteristics



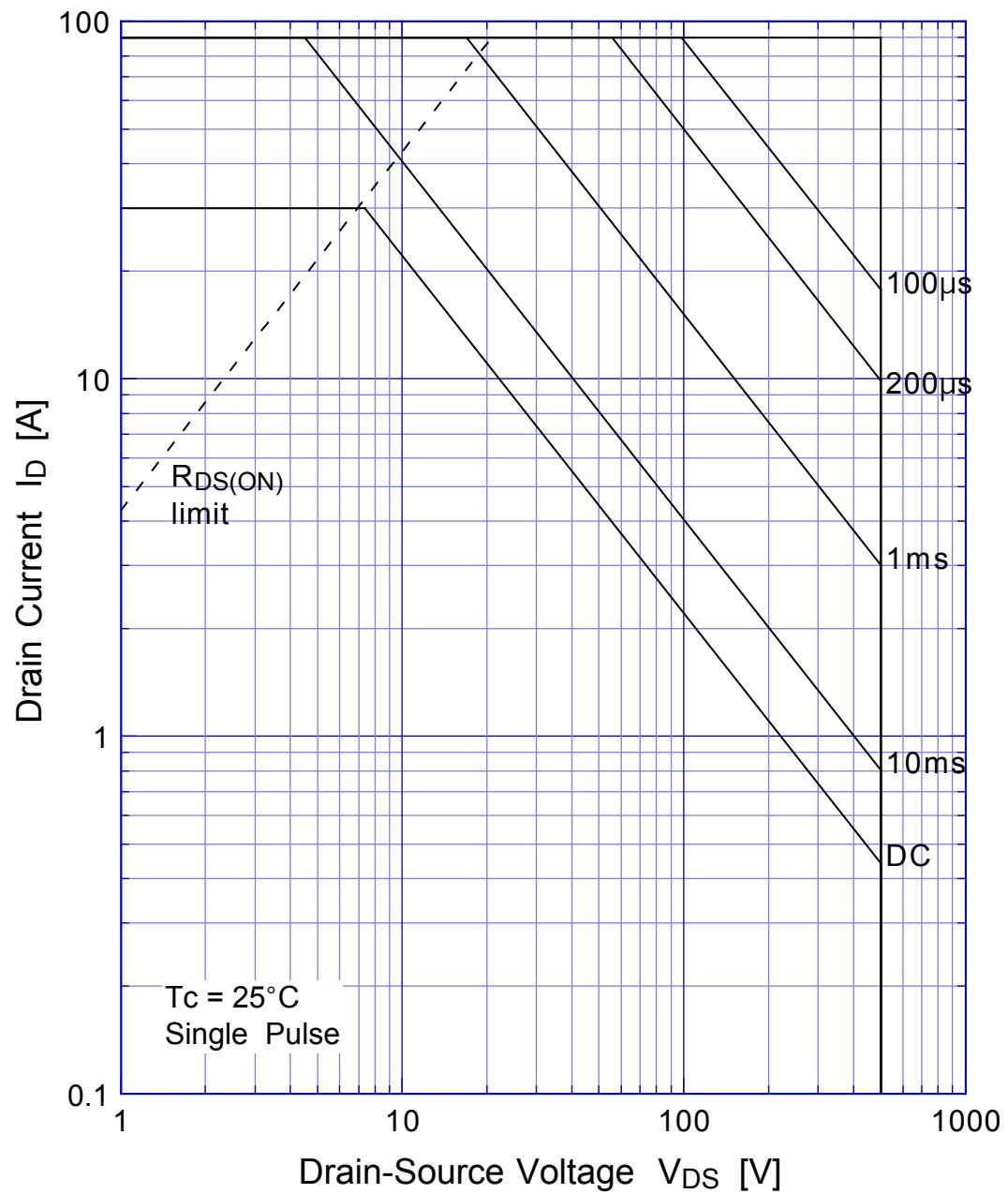
2SK2198 Static Drain-Source On-state Resistance



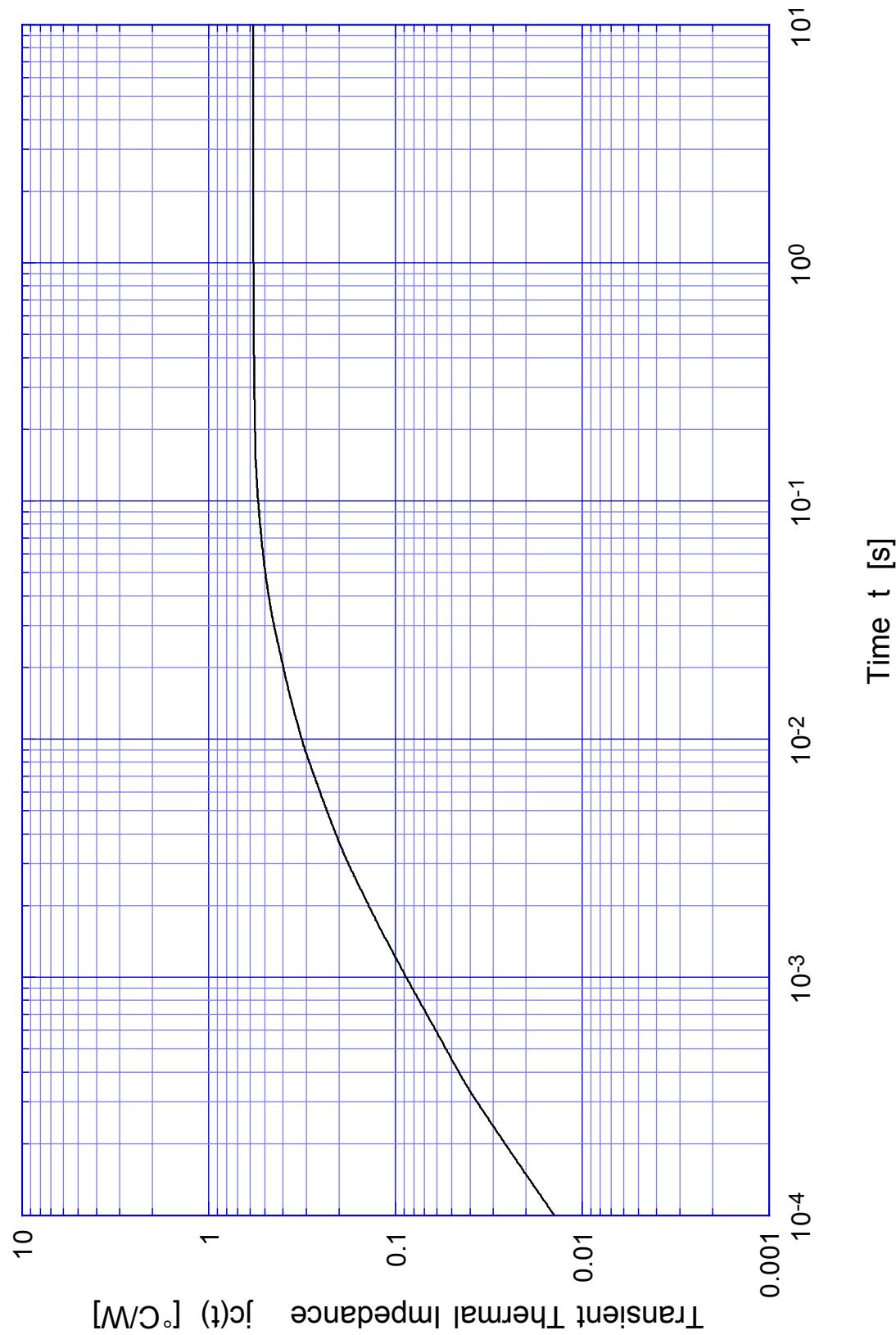
2SK2198 Gate Threshold Voltage



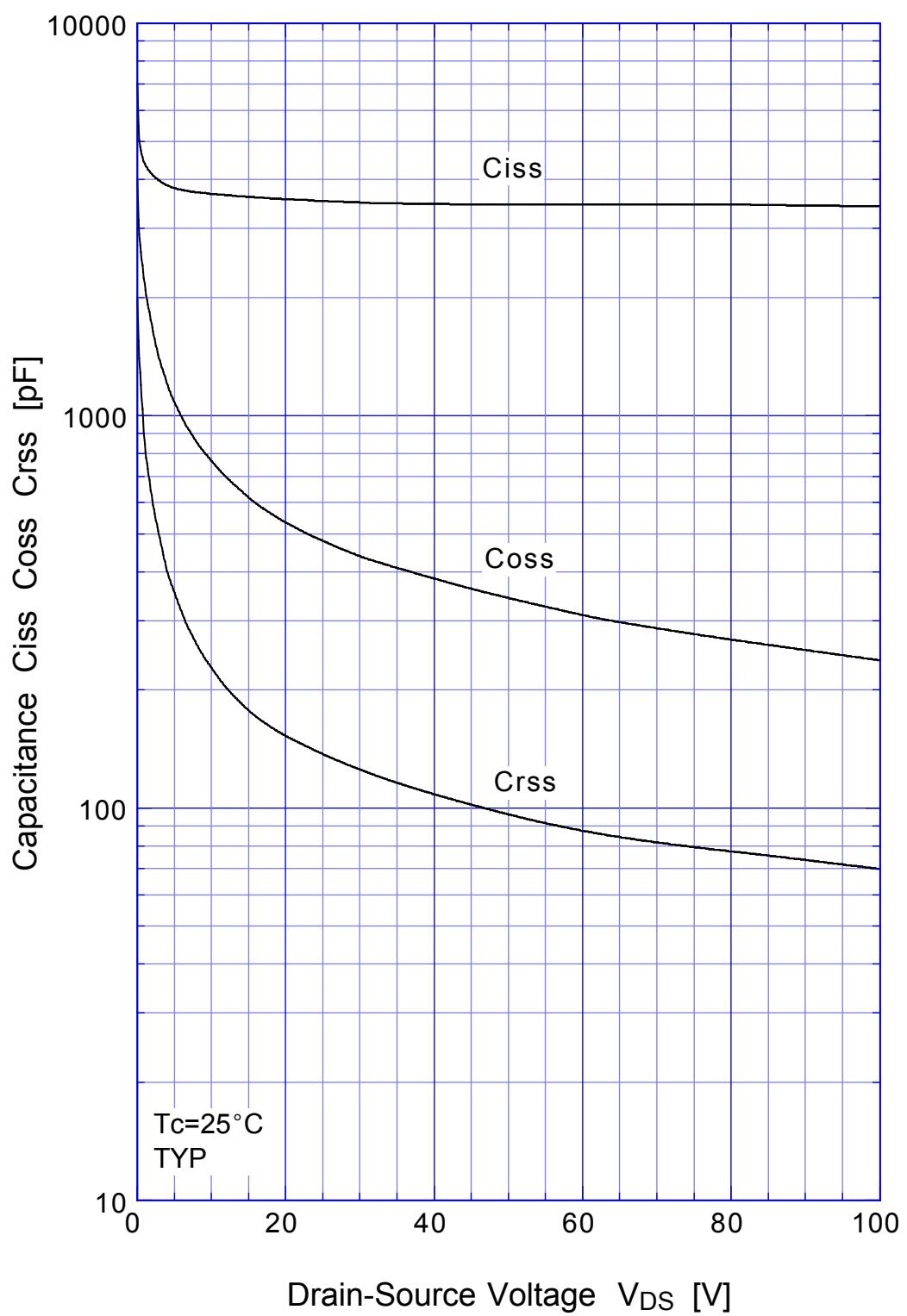
2SK2198 Safe Operating Area



2SK2198 Transient Thermal Impedance

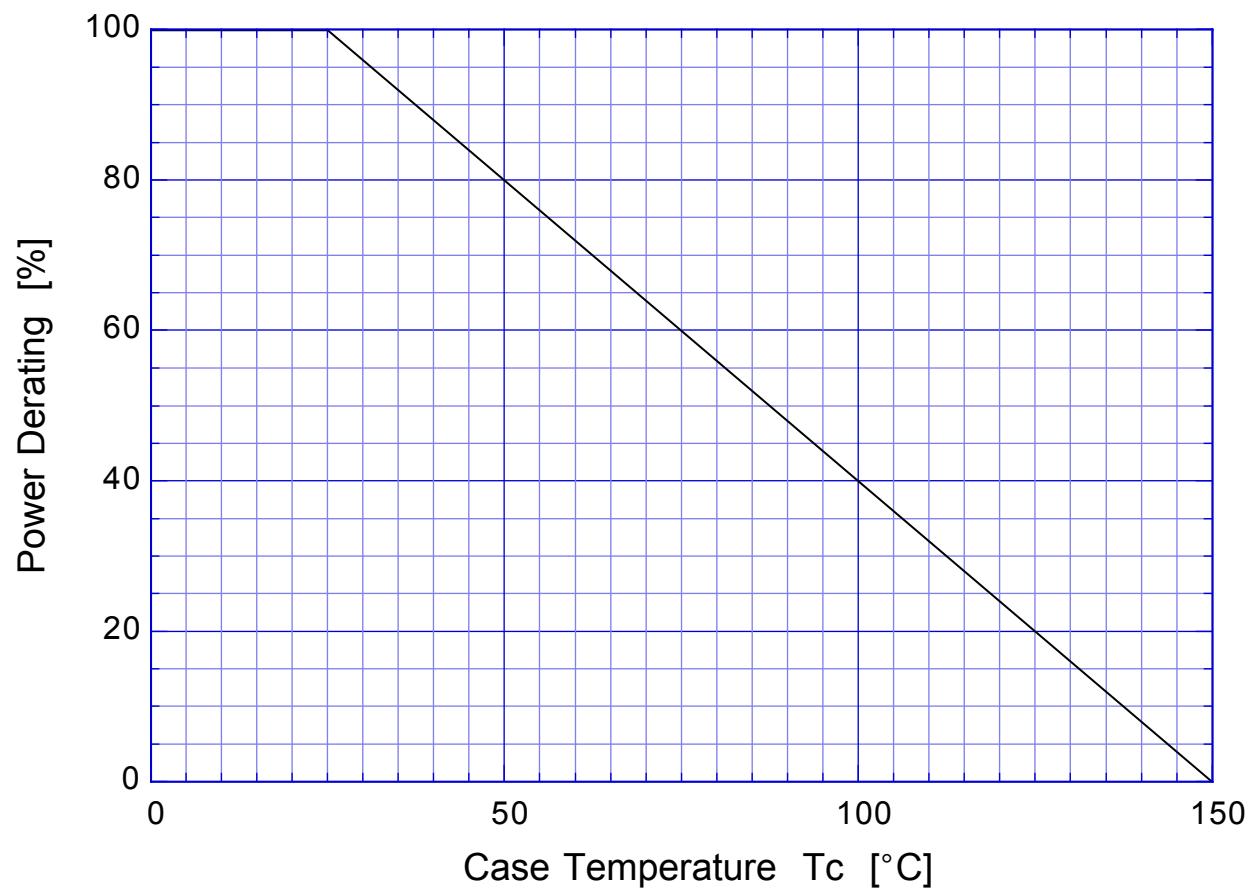


2SK2198 Capacitance



2SK2198

Power Derating



2SK2198

Gate Charge Characteristics

