TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (L²-π-MOSV)

2SK2231

Chopper Regulator, DC/DC Converter and Motor Drive Applications

4 V gate drive

• Low drain–source ON-resistance : $R_{DS (ON)} = 0.12 \Omega (typ.)$ • High forward transfer admittance : $|Y_{fs}| = 5.0 S (typ.)$ • Low leakage current : $I_{DSS} = 100 \mu A (max) (V_{DS} = 60 V)$

Enhancement mode : V_{th} = 0.8~2.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Character	istic	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	60	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	60	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	5	Α	
	Pulse (Note 1)	I _{DP}	20	Α	
Drain power dissipatio	n (Tc = 25°C)	PD	20	W	
Single-pulse avalanch	e energy (Note 2)	E _{AS}	129	mJ	
Avalanche current		I _{AR}	5	Α	
Repetitive avalanche energy (Note 3)		E _{AR} 2		mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th} (ch-c)	6.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

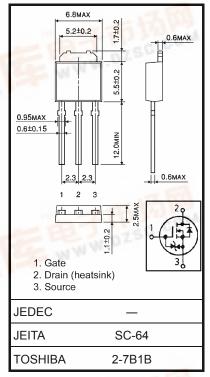
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 7 mH, $R_G = 25 \Omega$, $I_{AR} = 5 \text{ A}$

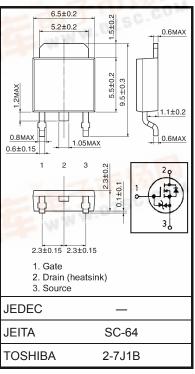
Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



Weight: 0.36 g (typ.)



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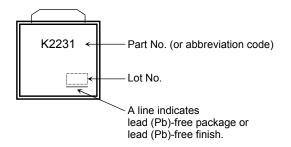
Electrical Characteristics (Ta = 25°C)

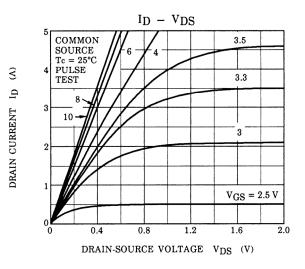
Charac	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ	
Drain cutoff curr	ent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_	_	100	μA	
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	60	_	_	V	
Gate threshold v	oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	8.0	_	2.0	V	
Drain-source ON-resistance		R _{DS (ON)}	V _{GS} = 4 V, I _D = 1.3 A	_	0.20	0.30	Ω	
			V _{GS} = 10 V, I _D = 2.5 A	_	0.12	0.16	22	
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	3.0	5.0	_	S	
Input capacitano	e	C _{iss}		_	370	_		
Reverse transfer capacitance		C _{rss}	C _{rss} V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	60	_	pF	
Output capacitance		Coss		_	180	_		
Switching time	Rise time	t _r	V_{GS} V_{OV} V_{OV} V_{OV} V_{OV} V_{OV} V_{OV} V_{OV} V_{OV}	_	18	_		
	Turn-on time	t _{on}		_	25	_		
	Fall time	t _f		_	55	_	ns	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu \text{s}$	_	170	_		
Total gate charge (gate-source plus gate-drain)		Qg		_	12	_		
Gate-source charge		Q _{gs}	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		8	_	nC	
Gate-drain ("Miller") charge		Q _{gd}		_	4	_		

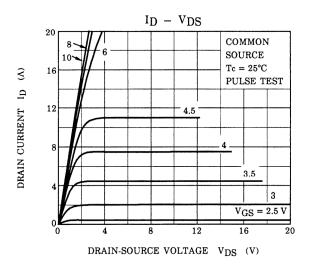
Source-Drain Ratings and Characteristics (Ta = 25°C)

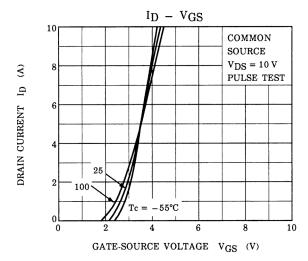
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	-	_	20	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	IDR = 5 A, VGS = 0 V, dIDR / dt = 50 A / µs	_	70	_	ns
Reverse recovery charge	Qrr	10R = 3 A, VGS = 0 V, αιDR / αι = 30 A / μs	_	0.1	_	μC

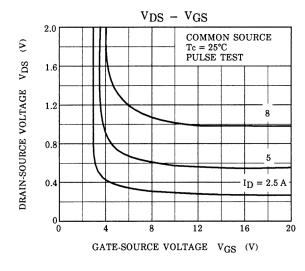
Marking

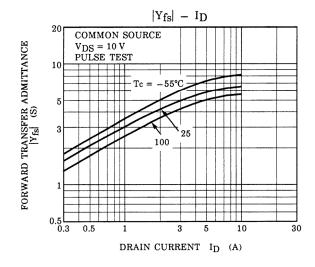


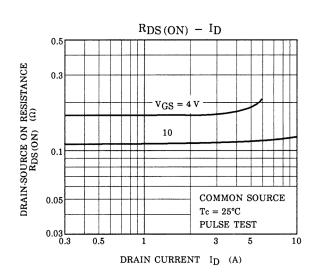


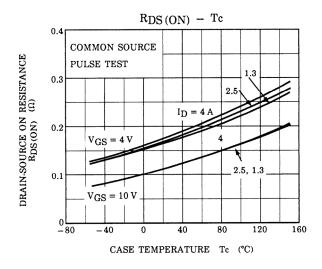


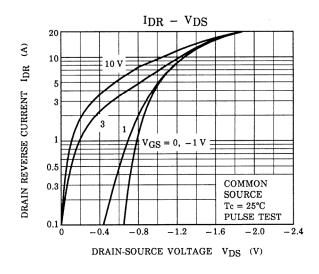


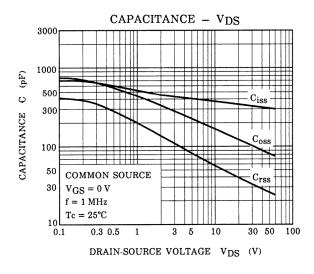


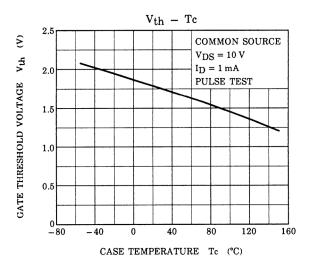


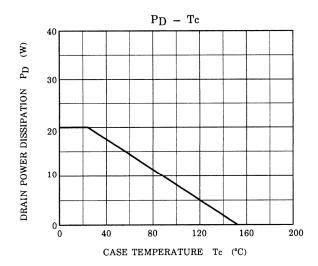


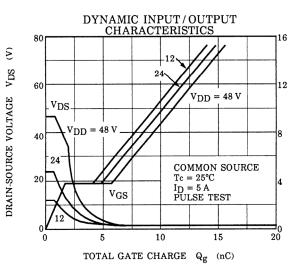


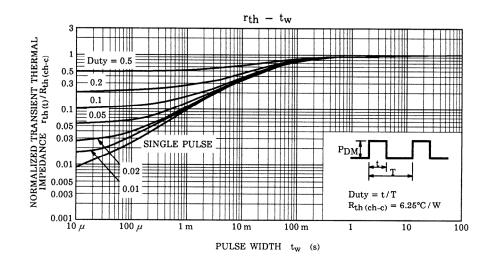


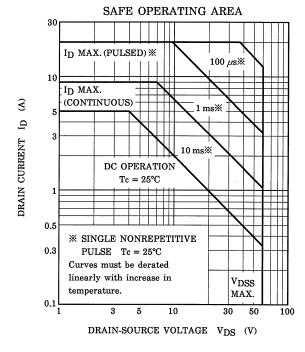


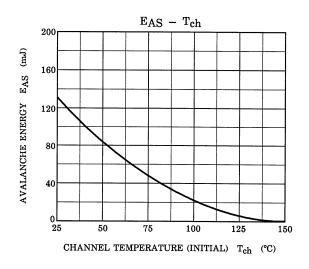


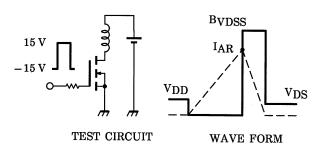












$$R_G = 25 \Omega$$

 $V_{DD} = 25 V$, $L = 7 mH$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

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