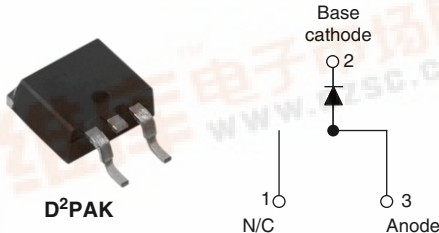


Schottky Rectifier, 20 A



FEATURES

- 125 °C T_J operation ($V_R < 5$ V)
- Single diode configuration
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified for Q101 level

DESCRIPTION

The Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

PRODUCT SUMMARY

$I_{F(AV)}$	20 A
V_R	15 V
I_{RM}	600 mA at 100 °C

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	20	A
V_{RRM}		15	V
I_{FSM}	$t_p = 5 \mu s$ sine	700	A
V_F	19 Apk, $T_J = 125$ °C (typical)	0.25	V
T_J	Range	- 55 to 125	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	20L15TS	UNITS
Maximum DC reverse voltage	V_R	$T_J = 100$ °C	15	V
Maximum working peak reverse voltage	V_{RWM}			

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 85\text{ }^{\circ}\text{C}$, rectangular waveform		20	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V_{RRM} applied	700	
		10 ms sine or 6 ms rect. pulse		330	
Non-repetitive avalanche energy	E_{AS}	$T_J = 25\text{ }^{\circ}\text{C}$, $I_{AS} = 2\text{ A}$, $L = 6\text{ mH}$		10	mJ
Repetitive avalanche current	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		2	A

20L15TS

Vishay High Power Products Schottky Rectifier, 20 A



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	19 A	T _J = 25 °C	-	0.41	V
		40 A		-	0.52	
		19 A	T _J = 125 °C	0.25	0.33	
		40 A		0.37	0.50	
Reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	-	10	mA
		T _J = 100 °C		-	600	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.182		V
Forward slope resistance	r _t			7.6		mΩ
Maximum junction capacitance	C _T	V _R = 5 V _{DC} , (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		8	-	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000		V/μs

Note

(1) Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range		T _J		- 55 to 125	°C
Maximum storage temperature range		T _{Stg}		- 55 to 150	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	1.5	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased (For TO-220)	0.50	
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	40	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf · cm (lbf · in)
	maximum			12 (10)	
Marking device			Case style D ² PAK	20L15TS	

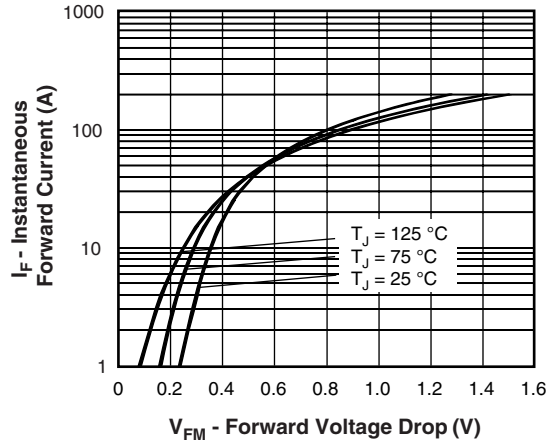


Fig. 1 - Maximum Forward Voltage Drop Characteristics

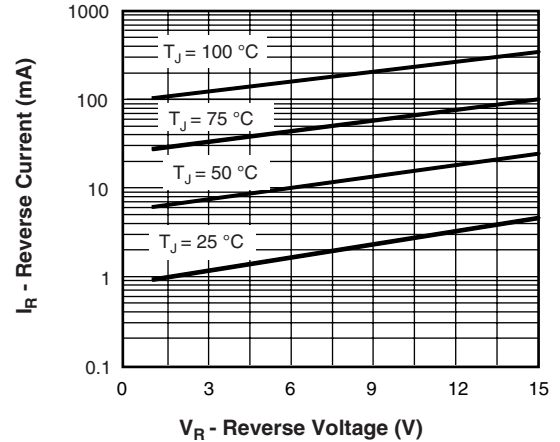


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

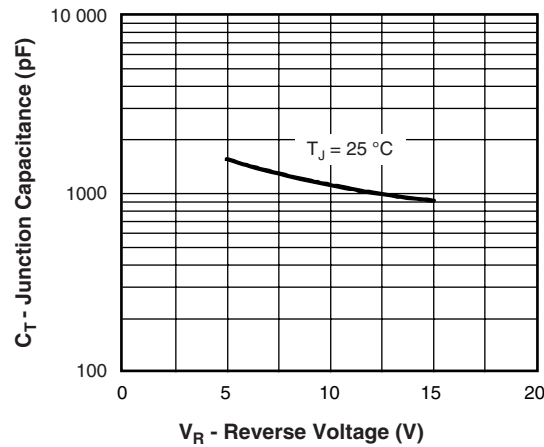
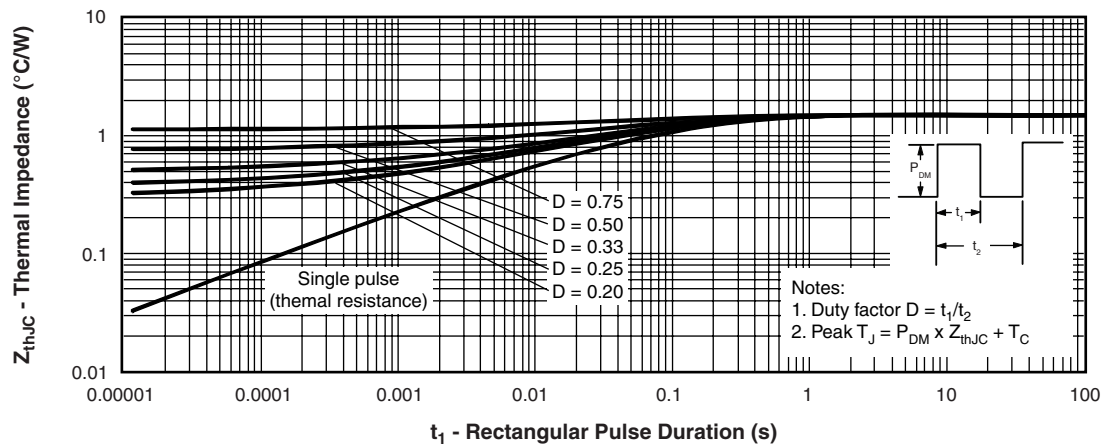


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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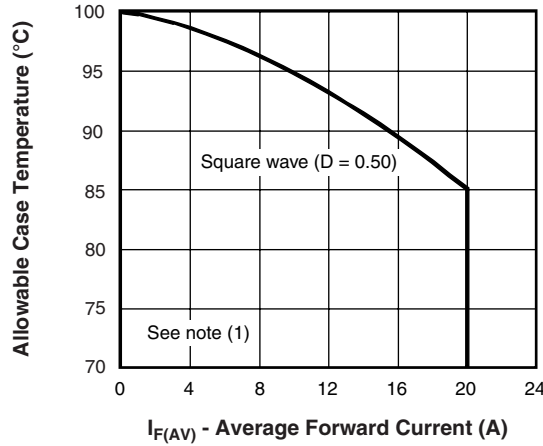


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

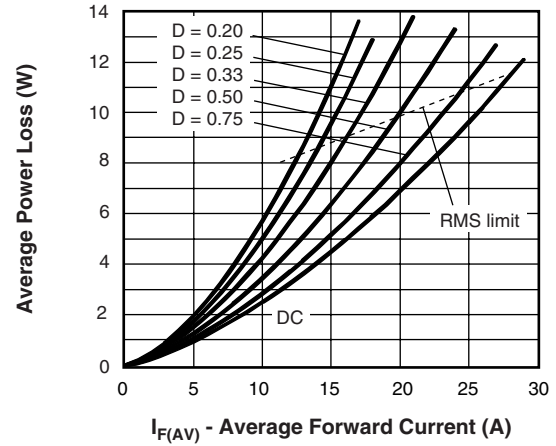


Fig. 6 - Forward Power Loss Characteristics

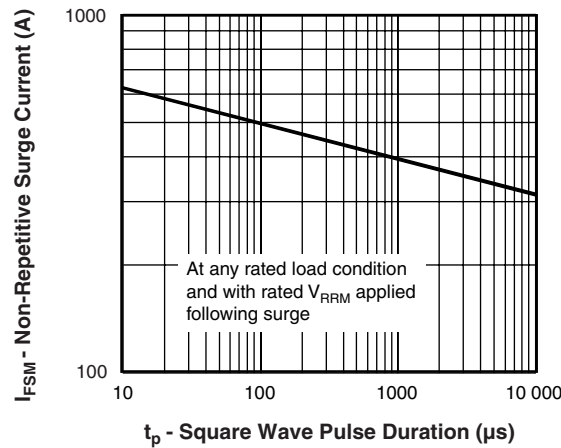


Fig. 7 - Maximum Non-Repetitive Surge Current

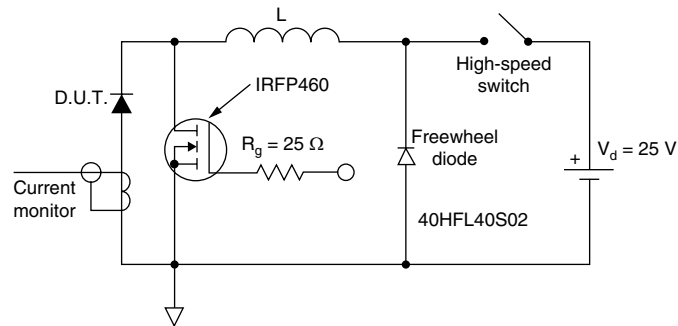


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 $P_{d_{REV}}$ = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

**ORDERING INFORMATION TABLE**

Device code	20	L	15	T	S	TRL	-
	1	2	3	4	5	6	7

- | | | |
|---|---|--|
| 1 | - | Current rating (20 A) |
| 2 | - | L = Low V_F |
| 3 | - | Voltage rating (15 = 15 V) |
| 4 | - | T = Schottky series |
| 5 | - | S = D ² PAK |
| 6 | - | <ul style="list-style-type: none">• None = Tube (50 pieces)• TRL = Tape and reel (left oriented)• TRR = Tape and reel (right oriented) |
| 7 | - | <ul style="list-style-type: none">• None = Standard production• PbF = Lead (Pb)-free |

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95014
Part marking information	http://www.vishay.com/doc?95008
Packaging information	http://www.vishay.com/doc?95032



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