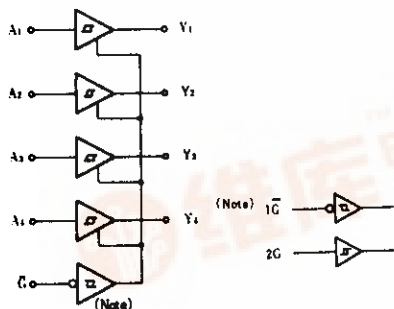


# HD74LS241

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捷多邦 专业PCB打样工厂, 24小时加急出货  
 ● Octal Buffers/Line Drivers/Line Receivers (non inverted three-state outputs)

## ■ BLOCK DIAGRAM (1/2)

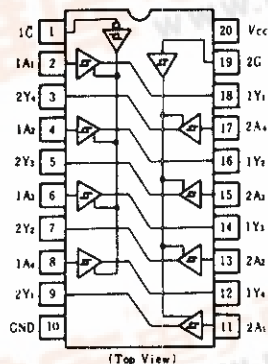


## ■ FUNCTION TABLE

Inputs			Output
1G	2G	A	Y
H	L	X	Z
L	H	H	H
L	H	L	L

Note) H; high level,  
 L; low level,  
 X; irrelevant  
 Z; off (high-impedance) state  
 of a 3-state output

## ■ PIN ARRANGEMENT



## ■ ELECTRICAL CHARACTERISTICS (Ta = -20 ~ +75°C)

Item	Symbol	Test Conditions	min	typ*	max	Unit	
Input voltage	V <sub>IH</sub>		2.0	—	—	V	
	V <sub>IL</sub>		—	—	0.8	V	
Hysteresis	V <sub>T+</sub> - V <sub>T-</sub>	V <sub>CC</sub> = 4.75V	0.2	0.4	—	V	
Output voltage	V <sub>OH</sub>	V <sub>CC</sub> = 4.75V, V <sub>IH</sub> = 2V, V <sub>IL</sub> = 0.8V, I <sub>OH</sub> = -3mA	2.4	—	—	V	
		V <sub>CC</sub> = 4.75V, V <sub>IH</sub> = 2V, V <sub>IL</sub> = 0.5V, I <sub>OH</sub> = -15mA	2.0	—	—	V	
	V <sub>OL</sub>	V <sub>CC</sub> = 4.75V, V <sub>IH</sub> = 2V, V <sub>IL</sub> = 0.8V					
Output current	I <sub>OZH</sub>	V <sub>CC</sub> = 5.25V, V <sub>IH</sub> = 2V, V <sub>O</sub> = 2.7V	I <sub>OL</sub> = 12mA	—	—	0.4	V
			I <sub>OL</sub> = 24mA	—	—	0.5	V
Output current	I <sub>OZL</sub>	V <sub>CC</sub> = 5.25V, V <sub>IH</sub> = 2V, V <sub>O</sub> = 0.4V		—	—	20	μA
				—	—	-20	μA
Input current	I <sub>IH</sub>	V <sub>CC</sub> = 5.25V, V <sub>I</sub> = 2.7V	—	—	20	μA	
	I <sub>IL</sub>	V <sub>CC</sub> = 5.25V, V <sub>I</sub> = 0.4V	—	—	-0.2	mA	
	I <sub>I</sub>	V <sub>CC</sub> = 5.25V, V <sub>I</sub> = 7V	—	—	0.1	mA	
Short-circuit output current	I <sub>OS</sub>	V <sub>CC</sub> = 5.25V	-40	—	-225	mA	
Supply current**	Outputs high	V <sub>CC</sub> = 5.25V	—	13	23	mA	
	Outputs low		—	27	46		
	All outputs disabled		—	32	54		
Input clamp voltage	V <sub>IK</sub>	V <sub>CC</sub> = 4.75V, I <sub>IN</sub> = -18mA	—	—	-1.5	V	

\* V<sub>CC</sub> = 5V, T<sub>a</sub> = 25°C

\*\* I<sub>CC</sub> is measured with all outputs open.

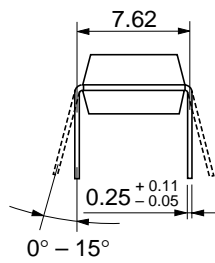
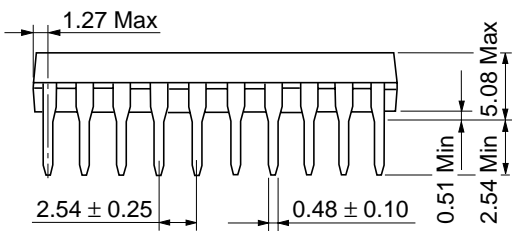
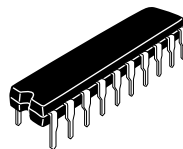
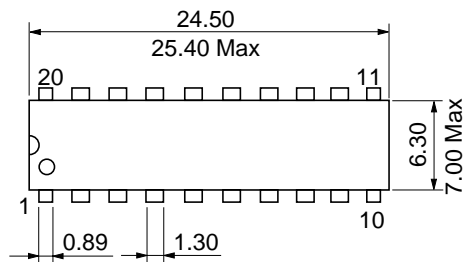
## ■ SWITCHING CHARACTERISTICS (V<sub>CC</sub> = 5V, T<sub>a</sub> = 25°C)

Item	Symbol	Test Conditions	min	typ	max	Unit
Propagation delay time	t <sub>PLH</sub>	C <sub>L</sub> = 45pF, R <sub>L</sub> = 667Ω	—	12	18	ns
	t <sub>PHL</sub>		—	12	18	
Output enable time	t <sub>ZL</sub>		—	20	30	ns
	t <sub>ZH</sub>		—	15	23	
Output disable time	t <sub>LZ</sub>	C <sub>L</sub> = 5pF, R <sub>L</sub> = 667Ω	—	15	25	ns
	t <sub>HZ</sub>		—	10	18	

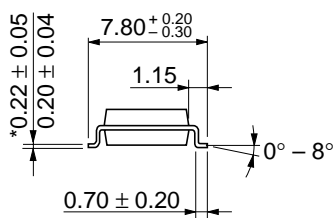
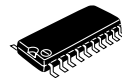
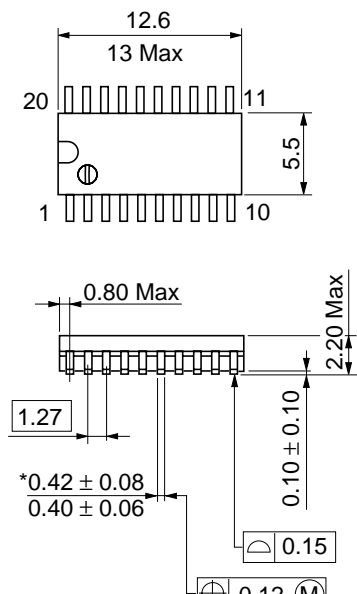
Note) Refer to Test Circuit and Waveform of the Common Item



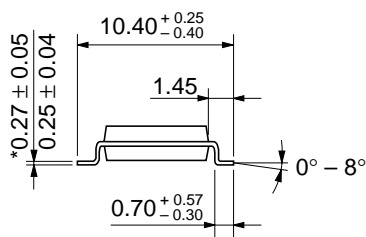
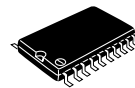
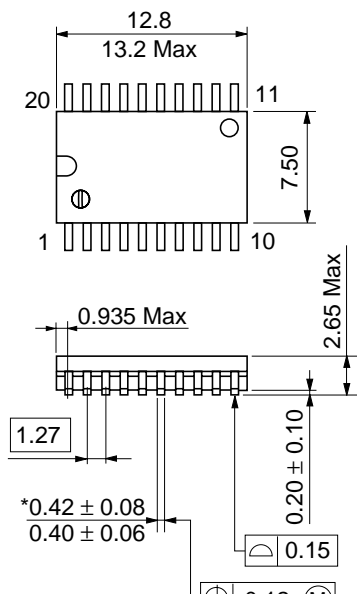
Unit: mm



Unit: mm



Unit: mm



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