

SMD Type

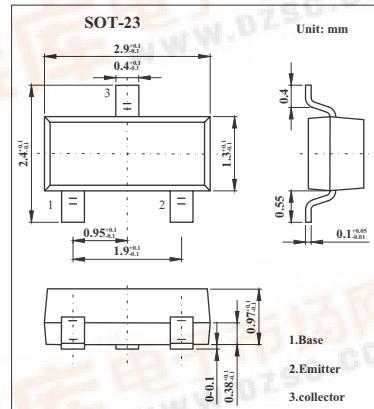
Transistors

NPN Epitaxial Planar Silicon Transistor

2SC4519

■ Features

- Adoption of FBET process.
- Low collector-to-emitter saturation voltage.
- Fast switching speed.
- Small-sized package.



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	60	V
Collector-emitter voltage	V _{CCEO}	45	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	I _C	500	mA
Collector current (pulse)	I _{CP}	1	A
Collector dissipation	P _C	200	mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

2SC4519■ Electrical Characteristics $T_a = 25^\circ C$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 45V, I_E = 0$			0.5	μA
Emitter cutoff current	I_{EBO}	$V_{EB} = 3V, I_C = 0$			0.5	μA
DC current gain	h_{FE}	$V_{CE} = 2V, I_C = 50mA$	100		400	
Gain bandwidth product	f_T	$V_{CE} = 2V, I_C = 50mA$		360		MHz
Output capacitance	C_{ob}	$V_{CB} = 10V, f = 1.0MHz$		4		pF
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 200mA, I_B = 10mA$		0.15	0.45	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C = 200mV, I_B = 10mA$		0.8	1.2	V
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = 10\mu A, I_E = 0$	60			V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = 1mA, R_{BE} = \infty$	45			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E = 10\mu A, I_C = 0$	5			V
Turn-on time	t_{on}	<p>$P_W = 20\mu s$ D.C.E 1%</p> <p>I_{B1} I_{B2}</p> <p>Input Output</p> <p>R_B</p> <p>50Ω</p> <p>$V_{BE} = 1V$</p> <p>100Ω</p> <p>470Ω</p> <p>$V_{CC} = 25V$</p> <p>$20I_{B1} = -20I_{B2} = I_c = 200mA$</p> <p>Unit (resistance : Ω, capacitance : F)</p>		60	120	ns
Storage time	t_{stg}			150	270	ns
Fall time	t_f			200	350	ns

■ h_{FE} Classification

Marking	TT		
Rank	4	5	6
h_{FE}	100~200	140~280	200~400